

Field Programmable Gate Array (FPGA) Market: Current Analysis and Forecast (2025-2033)

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Abstracts

The Field Programmable Gate Array (FPGA) Market is witnessing a significant growth rate of 11.88% during the forecast period (2025- 2033F). The Field Programmable Gate Array (FPGA) is a digital integrated circuit that can be programmed and reprogrammed after manufacturing. FPGAs' design consists of a programmable grid-connected array that can be interconnected with other logic blocks to perform different functions. These chips are coded in hardware description language. Earlier, they were coded in high-level languages such as C/C++, and the conversion of the machine model into hardware description language was difficult. With the increase in technological advancements, they can now be coded in Python, which makes it easier to convert an AI model into a hardware description language. These chips are in high demand due to their flexibility and reprogramming ability, enabling developers to modify hardware functions even after deployment. These properties make them ideal for situations where innovation and adaptability are required. In 5G networks, they provide high-speed data transmission and low-latency processing. In AI, they enable parallel processing, which is critical for deep learning and real-time decision-making. The growing need for industrial automation, where factories and robots require real-time control and customization, further enhances efficiency and propels the growth of the FPGA market.

Based on type, the Field programmable gate array (FPGA) market is segmented into Low-end FPGA, Mid-end FPGA, and High-end FPGA. In 2024, the low-end FPGA segment dominated the market, and it is expected to continue doing so during the forecast period. Low-end FPGAs are cheaper, smaller, and offer low power consumption as compared to high-end FPGAs, which are costlier and require more power. Due to their low power, small size, and cost efficiency, they are well-suited for businesses that have a tight budget. Low-end FPGAs are well-suited for devices that need to process the dataset but don't require powerful

servers. Additionally, the growing focus on decreasing costs in electronics has increased the need for low-end FPGAs even more. With advancements in technology, such as the integration of AI, these chips have become more powerful and efficient. For example, Lattice Ice40 UltraPlus provides the resources and low power usage for AI-based applications or devices, such as smart homes or IoT devices, to implement network protocols. Furthermore, these chips are used in wearable devices that have limited energy usage.

Based on node size, the Field programmable gate array (FPGA) market is segmented into 90nm. The 20-90nm segment dominated the FPGA market in 2024. The 20-90nm-featured FPGA is highly durable and has low power usage. The FPGA has balanced energy consumption and performance, high density, and large memory, and is available in 20-90nm size. This renders them excellent in the areas of data centers, telecommunication, automotive, and industrial categories. They find application in switches, routers, and other networking infrastructure because of the available flexibility in terms of data structure handling. Additionally, these FPGAs are an ideal solution to use in factory automation and Industry 4.0 environments because they are easily reconfigurable, and they perform well. These are implemented in more generalized applications like edge computing, IoT, defense, and machine vision. For example, in ADAS, these are used to handle the complex V2X communication, which allows vehicles to communicate with other vehicles and infrastructure that enhances traffic efficiency and safety.

Based on technology, the Field programmable gate array (FPGA) market is segmented into SRAM-based FPGA, Flash-based FPGA, EEPROM-based FPGA, and others. In 2024, the Flash-based FPGAs dominated the FPGA market. Flash-based FPGA uses non-volatile electronic components that hold data without power, making it suitable for high-speed storage solutions. Thus, when the system restarts, these take microseconds to power up, which saves time and allows for quick recovery from a power failure. These are widely used in aerospace sectors, such as in radar systems, spacecraft communication, and flight control. Their low power consumption and low density make them a suitable replacement for CPLD devices. Additionally, Flash-based FPGAs don't have any moving parts, which results in their long durability and reliability. Flash-based FPGAs are used to create interactive features such as animations, presentations, web components, and games. Furthermore, these FPGAs are increasingly being adopted in edge computing and embedded systems, where

power efficiency and quick boot times are critical. Their robust performance and adaptability also make them a preferred choice in automotive and industrial automation sectors, where consistent operation under harsh conditions is essential.

Based on applications, the Field programmable gate array (FPGA) market is segmented into telecommunication, Aerospace & Defense, Data Centre & Computing, Industrial, Healthcare, Consumer Electronics, and others. In 2024, the telecommunication segment dominated the market and is expected to maintain its leading position throughout the forecast period. This is due to the increasing demand for low latency and the introduction of 5G networks. FPGAs are well designed to perform parallel tasks. FPGAs are designed in such a manner that they can be reprogrammed as per the current need. This flexibility allows the admin to implement new functions, optimize algorithms, and update the network infrastructure as per the current demand. The network efficiency can be enhanced by offloading distributed tasks, which results in seamless data transmission, a high user experience, and a reduction in delays. They can be configured to enhance the performance of AI/ML models in a manner that allows predictive analytics and intelligent routing of the 5G network.

For a better understanding of the market of Field programmable gate array (FPGA) market, the market is analyzed based on its worldwide presence in countries such as North America (The US, Canada, and Rest of North America), Europe (Germany, The UK, France, and Rest of Europe), Asia-Pacific (China, Japan, India, Rest of Asia-Pacific), Rest of World. The Asia-Pacific FPGA market dominated the global FPGA market in 2024 and is expected to maintain its position in the forecast period. This can be attributed to the rising demand for IoT and AI-integrated devices in healthcare, financial, and telecommunication areas, as well as in the transportation industry. Smart cars and EV cars are another trend that is contributing immensely to this rising demand. The large manufacturing firms such as Samsung, LG, Toyota, and Taiwan Semiconductor Manufacturing Company Limited are all located in the region, which helps in the development of FPGA in the APAC region. The Asia-Pacific region is home to several countries that produce a high number of electric vehicles, including China, Japan, and India. Additionally, the growth of data centers in the region, as well as increased demand for cloud computing services, is expected to contribute significantly to the growth of the region within the forecast period. Moreover, the maturing automation trend, as well as the increasing role of smart cities in the Asia-Pacific region and digital transformation, also contribute to the

vitality of FPGA applications. As the world of IoT, AI, and machine learning grows, a critical need has arisen for versatile and high-performance computing that can perform complex tasks in an elegant and efficient manner. PGAs can be saved in different types of programming, and in a fast-changing world, the programmable gate array suits well.

Some of the major players operating in the market include Intel Corporation, Advanced Micro Devices, Inc., Lattice Semiconductor Corporation, Microchip Technology Inc., QuickLogic Corporation, Achronix Semiconductor Corporation, Efinix, Inc., Synopsis, Inc., GOWIN Semiconductor Corp., and Logic Fruit Technologies Private Limited.

Contents

1 MARKET INTRODUCTION

- 1.1. Market Definitions
- 1.2. Main Objective
- 1.3. Stakeholders
- 1.4. Limitation

2 RESEARCH METHODOLOGY OR ASSUMPTION

- 2.1. Research Process of the Field Programmable Gate Array (FPGA) Market
- 2.2. Research Methodology of the Field Programmable Gate Array (FPGA) Market
- 2.3. Respondent Profile

3 EXECUTIVE SUMMARY

- 3.1. Industry Synopsis
- 3.2. Segmental Outlook
 - 3.2.1. Market Growth Intensity
- 3.3. Regional Outlook

4 MARKET DYNAMICS

- 4.1. Drivers
- 4.2. Opportunity
- 4.3. Restraints
- 4.4. Trends
- 4.5. PESTEL Analysis
- 4.6. Demand Side Analysis
- 4.7. Supply Side Analysis
 - 4.7.1. Merger & Acquisition
 - 4.7.2. Investment Scenario
 - 4.7.3. Industry Insights: Leading Startups and Their Unique Strategies

5 PRICING ANALYSIS

- 5.1. Regional Pricing Analysis
- 5.2. Price Influencing Factors

6 GLOBAL FIELD PROGRAMMABLE GATE ARRAY (FPGA) MARKET REVENUE (USD MN), 2023-2033F

7 MARKET INSIGHTS BY TYPE

- 7.1. Low-end FPGA
- 7.2. Flash-Based FPGA
- 7.3. High-end FPGA

8 MARKET INSIGHTS BY NODE SIZE

- 8.1. 90nm

9 MARKET INSIGHTS BY TECHNOLOGY

- 9.1. SRAM-Based FPGA
- 9.2. Flash-Based FPGA
- 9.3. EEPROM-Based FPGA
- 9.4. Others

10 MARKET INSIGHTS BY APPLICATION

- 10.1. Telecommunication
- 10.2. Aerospace & Defense
- 10.3. Industrial
- 10.4. Healthcare
- 10.5. Consumer Electronics
- 10.6. Data Center & Computing
- 10.7. Others

11 MARKET INSIGHTS BY REGION

- 11.1. North America
 - 11.1.1. The US
 - 11.1.2. Canada
 - 11.1.3. Rest of North America
- 11.2. Europe
 - 11.2.1. Germany

- 11.2.2. The UK
- 11.2.3. France
- 11.2.4. Italy
- 11.2.5. Spain
- 11.2.6. Rest of Europe
- 11.3. Asia-Pacific
 - 11.3.1. China
 - 11.3.2. Japan
 - 11.3.3. India
 - 11.3.4. Rest of Asia-Pacific
- 11.4. Rest of World

12 VALUE CHAIN ANALYSIS

- 12.1. Marginal Analysis
- 12.2. List of Market Participants

13 COMPETITIVE LANDSCAPE

- 13.1 Competition Dashboard
- 13.2. Competitor Market Positioning Analysis
- 13.3. Porter Five Forces Analysis

14 COMPANY PROFILES

- 14.1. Intel Corporation
 - 14.1.1. Company Overview
 - 14.1.2. Key Financials
 - 14.1.3. SWOT Analysis
 - 14.1.4. Product Portfolio
 - 14.1.5. Recent Developments
- 14.2. Advanced Micro Devices, Inc. (AMD)
- 14.3. Lattice Semiconductor Corporation
- 14.4. Microchip Technology Inc
- 14.5. QuickLogic Corporation
- 14.6. Achronix Semiconductor Corporation
- 14.7. Efinix, Inc.
- 14.8. Flex Logic
- 14.9. GOWIN Semiconductor Corporation

14.10. Logic Fruit Technology Private Limited

15 ACRONYMS & ASSUMPTION

16 ANNEXURE

I would like to order

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