

# **Field Programmable Gate Array (FPGA) Market Forecasts to 2034 – Global Analysis By Configuration (Low-End FPGA, Mid-Range FPGA, High-End FPGA, SoC FPGA, and Embedded FPGA (eFPGA)), Technology, Node Size, Logic Density, Application, End User, Industry Vertical, and By Geography**

<https://marketpublishers.com/r/F7B9DD62A5D8EN.html>

Date: June 2026

Pages: 200

Price: US\$ 4,150.00 (Single User License)

ID: F7B9DD62A5D8EN

## **Abstracts**

According to Statistics MRC, the Global Field Programmable Gate Array (FPGA) Market is accounted for \$13.6 billion in 2026 and is expected to reach \$32.3 billion by 2034 growing at a CAGR of 11.4% during the forecast period. FPGAs are semiconductor devices consisting of configurable logic blocks and programmable interconnects, allowing post-manufacturing reconfiguration for specific applications. Unlike fixed-function application-specific integrated circuits, FPGAs offer flexibility, lower upfront costs, and rapid prototyping capabilities. These devices are critical in telecommunications, data centers, automotive systems, aerospace, and industrial automation, where evolving standards and performance demands require adaptable hardware. The market is segmented by node size, logic density, application, and end-user, reflecting diverse technological requirements across industries.

Market Dynamics:

Driver:

Rising demand for hardware acceleration in data centers

Cloud service providers and enterprises are increasingly adopting FPGAs to accelerate compute-intensive workloads such as artificial intelligence inference, encryption, and

real-time data analytics. Unlike graphics processing units, FPGAs can be dynamically reconfigured to match specific algorithmic requirements, delivering superior performance-per-watt for custom operations. Major hyperscalers, including companies operating large-scale data centers, have integrated FPGA-based accelerators into their server architectures to handle variable processing demands efficiently. This trend is intensifying as data traffic grows exponentially and latency constraints tighten, positioning FPGAs as essential components for next-generation cloud and edge computing infrastructure.

Restraint:

#### Complex programming and design barriers

FPGA adoption remains hindered by the steep learning curve associated with hardware description languages such as Verilog and VHDL, which differ significantly from conventional software programming paradigms. Organizations without specialized hardware engineering talent face substantial challenges in developing and optimizing FPGA-based solutions, limiting deployment to well-funded technical teams. Traditional design flows involve lengthy synthesis, placement, and routing processes, extending time-to-market compared to simpler processor-based implementations. Although high-level synthesis tools are emerging to bridge this gap, they often produce less efficient designs, preserving the programming complexity as a meaningful barrier to widespread adoption.

Opportunity:

#### Proliferation of edge AI and real-time inference

The rapid expansion of edge computing applications, including autonomous vehicles, industrial robotics, and smart surveillance, creates significant opportunities for reconfigurable hardware. Edge deployments demand low latency, power efficiency, and the ability to update algorithms in the field, all of which align naturally with FPGA capabilities. As neural network models evolve continuously, fixed-function chips quickly become obsolete, whereas FPGAs can be remotely reprogrammed to support new architectures. This adaptability is particularly valuable in automotive and industrial environments where device lifespans exceed typical technology cycles, positioning FPGAs as a compelling solution for long-deployed edge intelligence systems.

Threat:

## Intensifying competition from application-specific custom silicon

Major technology companies are increasingly developing custom ASICs and domain-specific accelerators optimized for their unique workloads, potentially displacing general-purpose FPGAs in high-volume applications. For instance, data center operators have designed tensor processing units and inference chips that outperform FPGAs on narrowly defined tasks while consuming less power. Although custom silicon lacks reconfigurability, the economies of scale in mass deployment can justify the upfront design investment. This trend threatens FPGA growth in large-scale, fixed-function scenarios, forcing FPGA vendors to differentiate by emphasizing programmability, time-to-market, and suitability for rapidly evolving or lower-volume applications where custom development is uneconomical.

## Covid-19 Impact:

The COVID-19 pandemic generated both disruptions and opportunities for the FPGA market. Supply chain interruptions and factory closures in early 2020 affected semiconductor production and component availability, causing delivery delays. Conversely, the accelerated digital transformation across healthcare, remote work, and online services increased demand for flexible computing infrastructure. Medical device manufacturers rapidly deployed FPGAs in ventilators and diagnostic equipment to address shortages, while network infrastructure upgrades for surging data traffic drove FPGA consumption. The crisis underscored the value of reconfigurable hardware in responding to unpredictable demand, prompting many organizations to incorporate FPGAs into resilience planning for future disruptions.

The 16 nm to 28 nm segment is expected to be the largest during the forecast period

The 16 nm to 28 nm segment is expected to account for the largest market share during the forecast period, representing the mature process node range that balances performance, power efficiency, and cost-effectiveness for most commercial and industrial applications. These nodes benefit from well-established manufacturing processes and extensive intellectual property libraries, enabling reliable production at scale. Mid-range FPGAs in this category serve telecommunications infrastructure, industrial control, automotive systems, and defense electronics where extreme power reduction of smaller nodes is less critical than proven reliability. The continued production of these devices by leading vendors, combined with their widespread design-in across existing products, secures their dominant revenue contribution throughout the

forecast timeline.

The High Logic Density segment is expected to have the highest CAGR during the forecast period

Over the forecast period, the High Logic Density segment is predicted to witness the highest growth rate, driven by escalating demand for complex programmable logic in advanced applications such as 5G baseband processing, high-performance computing, and AI acceleration. These devices incorporate hundreds of thousands to millions of logic cells, enabling implementation of entire systems on a single programmable chip. Data center operators, aerospace contractors, and communications equipment manufacturers increasingly require high-density FPGAs to process massive data throughputs and implement sophisticated algorithms. As process technologies advance below 16 nm, high-density devices achieve greater integration, further expanding addressable workloads and attracting premium pricing, thereby accelerating revenue growth in this segment.

Region with largest share:

During the forecast period, the North America region is expected to hold the largest market share, attributed to the presence of leading FPGA manufacturers, strong defense and aerospace sectors, and early adoption of advanced communications infrastructure. The United States hosts headquarters of major FPGA vendors and a dense ecosystem of design houses, system integrators, and end users spanning cloud computing, automotive, and industrial automation. Government-funded research initiatives and defense programs drive continuous demand for reconfigurable hardware. Proximity between design teams and production partners accelerates innovation cycles, while robust intellectual property protections encourage sustained investment in next-generation architectures, cementing North America's market leadership throughout the forecast period.

Region with highest CAGR:

Over the forecast period, the Asia Pacific region is anticipated to exhibit the highest CAGR, fueled by rapid expansion of consumer electronics manufacturing, telecommunications infrastructure deployment, and industrial automation across China, Taiwan, South Korea, and India. The region's semiconductor foundries are increasingly capable of producing advanced node FPGAs, reducing supply chain dependencies and lowering costs. Government initiatives promoting domestic chip design, particularly in

China, stimulate local FPGA innovation and adoption. Rising 5G base station construction, electric vehicle production, and smart factory investments generate substantial demand for programmable logic. As regional original equipment manufacturers transition from fixed-function chips to flexible FPGA solutions, Asia Pacific emerges as the fastest-growing market.

### Key players in the market

Some of the key players in Field Programmable Gate Array (FPGA) Market include Advanced Micro Devices, Inc., Intel Corporation, Lattice Semiconductor Corporation, Microchip Technology Incorporated, Achronix Semiconductor Corporation, QuickLogic Corporation, Efinix, Inc., Flex Logix Technologies, Inc., Gowin Semiconductor Corporation, Menta S.A.S., NanoXplore Inc., Aldec, Inc., EnSilica plc, S2C Inc., BittWare, Inc., Ayar Labs, Inc., and Xiphera Ltd.

### Key Developments:

In April 2026, Gowin announced a collaboration with JLCPCB to expand access to FPGA prototyping. Selected Gowin devices are now available via the LCSC component ecosystem, simplifying sourcing for educators, makers, and small-volume commercial teams.

In March 2026, Lattice joined the NVIDIA Holoscan ecosystem, introducing the Holoscan Sensor Bridge to advance safety and real-time processing for physical AI applications.

In February 2026, AMD unveiled the Kintex UltraScale+ Gen 2 FPGA family, a strategic update for the mid-range market. The new series features an architectural modernization of the 16nm platform, integrating LPDDR5X memory and PCIe Gen 4 support. AMD committed to product availability until 2045, specifically targeting long-lifecycle industries like aerospace and defense.

### Configurations Covered:

Low-End FPGA

Mid-Range FPGA

High-End FPGA

SoC FPGA

Embedded FPGA (eFPGA)

Technologies Covered:

SRAM-Based FPGA

Flash-Based FPGA

Antifuse-Based FPGA

EEPROM-Based FPGA

Node Sizes Covered:

Less than 16 nm

16 nm to 28 nm

28 nm to 90 nm

Above 90 nm

Logic Densities Covered:

Low Logic Density

Medium Logic Density

High Logic Density

Applications Covered:

Data Processing

Artificial Intelligence & Machine Learning

Signal Processing

Embedded Computing

Image & Video Processing

Network Processing

Security & Cryptography

High-Performance Computing

Industrial Control

Test & Measurement

Edge Computing

#### End Users Covered:

OEMs

Cloud Service Providers

Enterprises

Government & Defense Organizations

Research Institutions

#### Industry Verticals Covered:

Telecommunications

Data Centers & Cloud Computing

Consumer Electronics

Automotive

Aerospace & Defense

Industrial

Healthcare

BFSI

Media & Entertainment

Energy & Utilities

Research & Academia

Regions Covered:

North America

United States

Canada

Mexico

Europe

United Kingdom

Germany

France

Italy

Spain

Netherlands

Belgium

Sweden

Switzerland

Poland

Rest of Europe

Asia Pacific

China

Japan

India

South Korea

Australia

Indonesia

Thailand

Malaysia

Singapore

Vietnam

Rest of Asia Pacific

South America

Brazil

Argentina

Colombia

Chile

Peru

Rest of South America

Rest of the World (RoW)

Middle East

Saudi Arabia

United Arab Emirates

Qatar

Israel

Rest of Middle East

Africa

South Africa

Egypt

Morocco

Rest of Africa

### What our report offers:

Market share assessments for the regional and country-level segments

Strategic recommendations for the new entrants

Covers Market data for the years 2023, 2024, 2025, 2026, 2027, 2028, 2030, 2032 and 2034

Market Trends (Drivers, Constraints, Opportunities, Threats, Challenges, Investment Opportunities, and recommendations)

Strategic recommendations in key business segments based on the market estimations

Competitive landscaping mapping the key common trends

Company profiling with detailed strategies, financials, and recent developments

Supply chain trends mapping the latest technological advancements

### Free Customization Offerings:

All the customers of this report will be entitled to receive one of the following free customization options:

Company Profiling

Comprehensive profiling of additional market players (up to 3)

SWOT Analysis of key players (up to 3)

Regional Segmentation

Market estimations, Forecasts and CAGR of any prominent country as per the client's interest (Note: Depends on feasibility check)

## Competitive Benchmarking

Benchmarking of key players based on product portfolio, geographical presence, and strategic alliances

## Contents

### **1 EXECUTIVE SUMMARY**

- 1.1 Market Snapshot and Key Highlights
- 1.2 Growth Drivers, Challenges, and Opportunities
- 1.3 Competitive Landscape Overview
- 1.4 Strategic Insights and Recommendations

### **2 RESEARCH FRAMEWORK**

- 2.1 Study Objectives and Scope
- 2.2 Stakeholder Analysis
- 2.3 Research Assumptions and Limitations
- 2.4 Research Methodology
  - 2.4.1 Data Collection (Primary and Secondary)
  - 2.4.2 Data Modeling and Estimation Techniques
  - 2.4.3 Data Validation and Triangulation
  - 2.4.4 Analytical and Forecasting Approach

### **3 MARKET DYNAMICS AND TREND ANALYSIS**

- 3.1 Market Definition and Structure
- 3.2 Key Market Drivers
- 3.3 Market Restraints and Challenges
- 3.4 Growth Opportunities and Investment Hotspots
- 3.5 Industry Threats and Risk Assessment
- 3.6 Technology and Innovation Landscape
- 3.7 Emerging and High-Growth Markets
- 3.8 Regulatory and Policy Environment
- 3.9 Impact of COVID-19 and Recovery Outlook

### **4 COMPETITIVE AND STRATEGIC ASSESSMENT**

- 4.1 Porter's Five Forces Analysis
  - 4.1.1 Supplier Bargaining Power
  - 4.1.2 Buyer Bargaining Power
  - 4.1.3 Threat of Substitutes
  - 4.1.4 Threat of New Entrants

- 4.1.5 Competitive Rivalry
- 4.2 Market Share Analysis of Key Players
- 4.3 Product Benchmarking and Performance Comparison

## **5 GLOBAL FIELD PROGRAMMABLE GATE ARRAY (FPGA) MARKET, BY CONFIGURATION**

- 5.1 Low-End FPGA
- 5.2 Mid-Range FPGA
- 5.3 High-End FPGA
- 5.4 SoC FPGA
- 5.5 Embedded FPGA (eFPGA)

## **6 GLOBAL FIELD PROGRAMMABLE GATE ARRAY (FPGA) MARKET, BY TECHNOLOGY**

- 6.1 SRAM-Based FPGA
- 6.2 Flash-Based FPGA
- 6.3 Antifuse-Based FPGA
- 6.4 EEPROM-Based FPGA

## **7 GLOBAL FIELD PROGRAMMABLE GATE ARRAY (FPGA) MARKET, BY NODE SIZE**

- 7.1 Less than 16 nm
- 7.2 16 nm to 28 nm
- 7.3 28 nm to 90 nm
- 7.4 Above 90 nm

## **8 GLOBAL FIELD PROGRAMMABLE GATE ARRAY (FPGA) MARKET, BY LOGIC DENSITY**

- 8.1 Low Logic Density
- 8.2 Medium Logic Density
- 8.3 High Logic Density

## **9 GLOBAL FIELD PROGRAMMABLE GATE ARRAY (FPGA) MARKET, BY APPLICATION**

- 9.1 Data Processing
- 9.2 Artificial Intelligence & Machine Learning
- 9.3 Signal Processing
- 9.4 Embedded Computing
- 9.5 Image & Video Processing
- 9.6 Network Processing
- 9.7 Security & Cryptography
- 9.8 High-Performance Computing
- 9.9 Industrial Control
- 9.10 Test & Measurement
- 9.11 Edge Computing

## **10 GLOBAL FIELD PROGRAMMABLE GATE ARRAY (FPGA) MARKET, BY END USER**

- 10.1 OEMs
- 10.2 Cloud Service Providers
- 10.3 Enterprises
- 10.4 Government & Defense Organizations
- 10.5 Research Institutions

## **11 GLOBAL FIELD PROGRAMMABLE GATE ARRAY (FPGA) MARKET, BY INDUSTRY VERTICAL**

- 11.1 Telecommunications
- 11.2 Data Centers & Cloud Computing
- 11.3 Consumer Electronics
- 11.4 Automotive
- 11.5 Aerospace & Defense
- 11.6 Industrial
- 11.7 Healthcare
- 11.8 BFSI
- 11.9 Media & Entertainment
- 11.10 Energy & Utilities
- 11.11 Research & Academia

## **12 GLOBAL FIELD PROGRAMMABLE GATE ARRAY (FPGA) MARKET, BY GEOGRAPHY**

- 12.1 North America
  - 12.1.1 United States
  - 12.1.2 Canada
  - 12.1.3 Mexico
- 12.2 Europe
  - 12.2.1 United Kingdom
  - 12.2.2 Germany
  - 12.2.3 France
  - 12.2.4 Italy
  - 12.2.5 Spain
  - 12.2.6 Netherlands
  - 12.2.7 Belgium
  - 12.2.8 Sweden
  - 12.2.9 Switzerland
  - 12.2.10 Poland
  - 12.2.11 Rest of Europe
- 12.3 Asia Pacific
  - 12.3.1 China
  - 12.3.2 Japan
  - 12.3.3 India
  - 12.3.4 South Korea
  - 12.3.5 Australia
  - 12.3.6 Indonesia
  - 12.3.7 Thailand
  - 12.3.8 Malaysia
  - 12.3.9 Singapore
  - 12.3.10 Vietnam
  - 12.3.11 Rest of Asia Pacific
- 12.4 South America
  - 12.4.1 Brazil
  - 12.4.2 Argentina
  - 12.4.3 Colombia
  - 12.4.4 Chile
  - 12.4.5 Peru
  - 12.4.6 Rest of South America
- 12.5 Rest of the World (RoW)
  - 12.5.1 Middle East
    - 12.5.1.1 Saudi Arabia
    - 12.5.1.2 United Arab Emirates

- 12.5.1.3 Qatar
- 12.5.1.4 Israel
- 12.5.1.5 Rest of Middle East
- 12.5.2 Africa
  - 12.5.2.1 South Africa
  - 12.5.2.2 Egypt
  - 12.5.2.3 Morocco
  - 12.5.2.4 Rest of Africa

## **13 STRATEGIC MARKET INTELLIGENCE**

- 13.1 Industry Value Network and Supply Chain Assessment
- 13.2 White-Space and Opportunity Mapping
- 13.3 Product Evolution and Market Life Cycle Analysis
- 13.4 Channel, Distributor, and Go-to-Market Assessment

## **14 INDUSTRY DEVELOPMENTS AND STRATEGIC INITIATIVES**

- 14.1 Mergers and Acquisitions
- 14.2 Partnerships, Alliances, and Joint Ventures
- 14.3 New Product Launches and Certifications
- 14.4 Capacity Expansion and Investments
- 14.5 Other Strategic Initiatives

## **15 COMPANY PROFILES**

- 15.1 Advanced Micro Devices, Inc.
- 15.2 Intel Corporation
- 15.3 Lattice Semiconductor Corporation
- 15.4 Microchip Technology Incorporated
- 15.5 Achronix Semiconductor Corporation
- 15.6 QuickLogic Corporation
- 15.7 Efinix, Inc.
- 15.8 Flex Logix Technologies, Inc.
- 15.9 Gowin Semiconductor Corporation
- 15.10 Menta S.A.S.
- 15.11 NanoXplore Inc.
- 15.12 Aldec, Inc.
- 15.13 EnSilica plc

15.14 S2C Inc.

15.15 BittWare, Inc.

15.16 Ayar Labs, Inc.

15.17 Xiphera Ltd.

## List Of Tables

### LIST OF TABLES

Table 1 Global Field Programmable Gate Array (FPGA) Market Outlook, By Region (2023–2034) (\$MN)

Table 2 Global Field Programmable Gate Array (FPGA) Market Outlook, By Configuration (2023–2034) (\$MN)

Table 3 Global Field Programmable Gate Array (FPGA) Market Outlook, By Low-End FPGA (2023–2034) (\$MN)

Table 4 Global Field Programmable Gate Array (FPGA) Market Outlook, By Mid-Range FPGA (2023–2034) (\$MN)

Table 5 Global Field Programmable Gate Array (FPGA) Market Outlook, By High-End FPGA (2023–2034) (\$MN)

Table 6 Global Field Programmable Gate Array (FPGA) Market Outlook, By SoC FPGA (2023–2034) (\$MN)

Table 7 Global Field Programmable Gate Array (FPGA) Market Outlook, By Embedded FPGA (eFPGA) (2023–2034) (\$MN)

Table 8 Global Field Programmable Gate Array (FPGA) Market Outlook, By Technology (2023–2034) (\$MN)

Table 9 Global Field Programmable Gate Array (FPGA) Market Outlook, By SRAM-Based FPGA (2023–2034) (\$MN)

Table 10 Global Field Programmable Gate Array (FPGA) Market Outlook, By Flash-Based FPGA (2023–2034) (\$MN)

Table 11 Global Field Programmable Gate Array (FPGA) Market Outlook, By Antifuse-Based FPGA (2023–2034) (\$MN)

Table 12 Global Field Programmable Gate Array (FPGA) Market Outlook, By EEPROM-Based FPGA (2023–2034) (\$MN)

Table 13 Global Field Programmable Gate Array (FPGA) Market Outlook, By Node Size (2023–2034) (\$MN)

Table 14 Global Field Programmable Gate Array (FPGA) Market Outlook, By Less than 16 nm (2023–2034) (\$MN)

Table 15 Global Field Programmable Gate Array (FPGA) Market Outlook, By 16 nm to 28 nm (2023–2034) (\$MN)

Table 16 Global Field Programmable Gate Array (FPGA) Market Outlook, By 28 nm to 90 nm (2023–2034) (\$MN)

Table 17 Global Field Programmable Gate Array (FPGA) Market Outlook, By Above 90 nm (2023–2034) (\$MN)

Table 18 Global Field Programmable Gate Array (FPGA) Market Outlook, By Logic

Density (2023–2034) (\$MN)

Table 19 Global Field Programmable Gate Array (FPGA) Market Outlook, By Low Logic Density (2023–2034) (\$MN)

Table 20 Global Field Programmable Gate Array (FPGA) Market Outlook, By Medium Logic Density (2023–2034) (\$MN)

Table 21 Global Field Programmable Gate Array (FPGA) Market Outlook, By High Logic Density (2023–2034) (\$MN)

Table 22 Global Field Programmable Gate Array (FPGA) Market Outlook, By Application (2023–2034) (\$MN)

Table 23 Global Field Programmable Gate Array (FPGA) Market Outlook, By Data Processing (2023–2034) (\$MN)

Table 24 Global Field Programmable Gate Array (FPGA) Market Outlook, By Artificial Intelligence & Machine Learning (2023–2034) (\$MN)

Table 25 Global Field Programmable Gate Array (FPGA) Market Outlook, By Signal Processing (2023–2034) (\$MN)

Table 26 Global Field Programmable Gate Array (FPGA) Market Outlook, By Embedded Computing (2023–2034) (\$MN)

Table 27 Global Field Programmable Gate Array (FPGA) Market Outlook, By Image & Video Processing (2023–2034) (\$MN)

Table 28 Global Field Programmable Gate Array (FPGA) Market Outlook, By Network Processing (2023–2034) (\$MN)

Table 29 Global Field Programmable Gate Array (FPGA) Market Outlook, By Security & Cryptography (2023–2034) (\$MN)

Table 30 Global Field Programmable Gate Array (FPGA) Market Outlook, By High-Performance Computing (2023–2034) (\$MN)

Table 31 Global Field Programmable Gate Array (FPGA) Market Outlook, By Industrial Control (2023–2034) (\$MN)

Table 32 Global Field Programmable Gate Array (FPGA) Market Outlook, By Test & Measurement (2023–2034) (\$MN)

Table 33 Global Field Programmable Gate Array (FPGA) Market Outlook, By Edge Computing (2023–2034) (\$MN)

Table 34 Global Field Programmable Gate Array (FPGA) Market Outlook, By End User (2023–2034) (\$MN)

Table 35 Global Field Programmable Gate Array (FPGA) Market Outlook, By OEMs (2023–2034) (\$MN)

Table 36 Global Field Programmable Gate Array (FPGA) Market Outlook, By Cloud Service Providers (2023–2034) (\$MN)

Table 37 Global Field Programmable Gate Array (FPGA) Market Outlook, By Enterprises (2023–2034) (\$MN)

Table 38 Global Field Programmable Gate Array (FPGA) Market Outlook, By Government & Defense Organizations (2023–2034) (\$MN)

Table 39 Global Field Programmable Gate Array (FPGA) Market Outlook, By Research Institutions (2023–2034) (\$MN)

Table 40 Global Field Programmable Gate Array (FPGA) Market Outlook, By Industry Vertical (2023–2034) (\$MN)

Table 41 Global Field Programmable Gate Array (FPGA) Market Outlook, By Telecommunications (2023–2034) (\$MN)

Table 42 Global Field Programmable Gate Array (FPGA) Market Outlook, By Data Centers & Cloud Computing (2023–2034) (\$MN)

Table 43 Global Field Programmable Gate Array (FPGA) Market Outlook, By Consumer Electronics (2023–2034) (\$MN)

Table 44 Global Field Programmable Gate Array (FPGA) Market Outlook, By Automotive (2023–2034) (\$MN)

Table 45 Global Field Programmable Gate Array (FPGA) Market Outlook, By Aerospace & Defense (2023–2034) (\$MN)

Table 46 Global Field Programmable Gate Array (FPGA) Market Outlook, By Industrial (2023–2034) (\$MN)

Table 47 Global Field Programmable Gate Array (FPGA) Market Outlook, By Healthcare (2023–2034) (\$MN)

Table 48 Global Field Programmable Gate Array (FPGA) Market Outlook, By BFSI (2023–2034) (\$MN)

Table 49 Global Field Programmable Gate Array (FPGA) Market Outlook, By Media & Entertainment (2023–2034) (\$MN)

Table 50 Global Field Programmable Gate Array (FPGA) Market Outlook, By Energy & Utilities (2023–2034) (\$MN)

Table 51 Global Field Programmable Gate Array (FPGA) Market Outlook, By Research & Academia (2023–2034) (\$MN)

Note: Tables for North America, Europe, APAC, South America, and Rest of the World (RoW) Regions are also represented in the same manner as above.

## I would like to order

Product name: Field Programmable Gate Array (FPGA) Market Forecasts to 2034 – Global Analysis By Configuration (Low-End FPGA, Mid-Range FPGA, High-End FPGA, SoC FPGA, and Embedded FPGA (eFPGA)), Technology, Node Size, Logic Density, Application, End User, Industry Vertical, and By Geography

Product link: <https://marketpublishers.com/r/F7B9DD62A5D8EN.html>

Price: US\$ 4,150.00 (Single User License / Electronic Delivery)

If you want to order Corporate License or Hard Copy, please, contact our Customer Service:

[info@marketpublishers.com](mailto:info@marketpublishers.com)

## Payment

To pay by Credit Card (Visa, MasterCard, American Express, PayPal), please, click button on product page <https://marketpublishers.com/r/F7B9DD62A5D8EN.html>