

Embedded Non-Volatile Memory Market Forecasts to 2034 – Global Analysis By Memory Type (Embedded Flash Memory, Embedded EEPROM, Embedded MRAM, Embedded ReRAM / RRAM, Embedded FRAM / FeRAM, Embedded PCM / PCRAM, Embedded FeFET, and Other Emerging eNVM Technologies), Storage Density, Technology Node, Integration Type, Device Type, Application, End-Use Industry, and By Geography

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Abstracts

According to Statistics MRC, the Global Embedded Non Volatile Memory Market is accounted for \$5.7 billion in 2026 and is expected to reach \$15.1 billion by 2034 growing at a CAGR of 12.9% during the forecast period. Embedded non-volatile memory (eNVM) refers to memory integrated directly onto a chip alongside logic circuits, enabling data retention after power loss without external storage components. This technology is critical for microcontrollers (MCUs), system-on-chips (SoCs), and ASICs deployed across automotive, consumer electronics, industrial automation, and Internet of Things (IoT) applications. eNVM solutions including eFlash, MRAM, RRAM, and FeRAM are increasingly replacing external memory due to improved performance, reduced power consumption, and enhanced security against physical tampering.

Market Dynamics:

Driver:

Proliferation of IoT and edge computing devices

Rapid deployment of connected sensors, wearables, and smart home products demands compact, low-power memory solutions that retain data during intermittent power cycles. IoT devices operating on batteries or energy harvesting require eNVM that consumes minimal energy for read and write operations while maintaining code storage and device configuration data. Edge computing nodes performing local data processing further benefit from embedded memory's reduced latency compared to external storage. As billions of new connected devices enter the market annually, semiconductor manufacturers increasingly integrate non-volatile memory directly onto application-specific chips, driving substantial demand across technology nodes from mature 65nm down to advanced 16nm and below.

Restraint:

Complex manufacturing and scaling challenges

Integrating non-volatile memory with advanced logic processes presents significant technical obstacles that increase production costs and limit yield rates. Traditional eFlash technology faces difficulties scaling beyond 28nm due to high programming voltages that are incompatible with thin gate oxides of advanced nodes. Emerging memory technologies like MRAM and RRAM require additional material layers and process steps, raising wafer costs and requiring specialized foundry capabilities. These manufacturing complexities create supply constraints, particularly for leading-edge nodes below 16nm, and prolong development cycles for new products, potentially delaying time-to-market for semiconductor companies and restraining overall market growth.

Opportunity:

Automotive electrification and autonomous driving

The automotive industry's transition toward electric vehicles and advanced driver-assistance systems creates unprecedented demand for reliable, high-endurance eNVM solutions. Modern vehicles contain hundreds of MCUs and SoCs requiring instant-on capability, frequent firmware updates over-the-air, and robust data retention across temperature extremes. eNVM enables single-chip integration of memory and logic, reducing board space and improving system reliability critical for safety applications. As autonomous driving levels advance, the volume of code stored on embedded memory increases exponentially. This automotive megatrend is pushing foundries to accelerate

development of embedded MRAM and RRAM capable of meeting stringent AEC-Q100 automotive reliability standards.

Threat:

Emerging alternative memory technologies

Disruptive memory architectures including resistive RAM, phase-change memory, and ferroelectric RAM are competing for design wins while still maturing in manufacturing processes. Technology uncertainty regarding which eNVM solution will ultimately dominate creates hesitation among system designers who face potential risk of selecting a soon-to-be-obsolete approach. Additionally, advancements in non-volatile memory external to chips, such as emerging persistent memory modules, could reduce demand for certain embedded applications. This competitive landscape forces eNVM providers to continuously invest in research and development while offering multiple technology options, stretching limited engineering resources and potentially fragmenting the market.

Covid-19 Impact:

The COVID-19 pandemic initially disrupted eNVM supply chains through factory shutdowns and logistics bottlenecks, creating component shortages that affected automotive and industrial segments. However, lockdown-driven acceleration of remote work, online learning, and home entertainment dramatically increased demand for consumer electronics, laptops, and cloud infrastructure, all of which rely heavily on embedded memory. Semiconductor foundries adapted quickly, repurposing capacity to maintain supply. The pandemic also accelerated digital transformation across healthcare, logistics, and manufacturing, resulting in sustained elevated demand for IoT devices. These shifts created lasting structural growth in eNVM markets as businesses and consumers permanently adopted more connected, digitally dependent lifestyles.

The 28 nm to 45 nm segment is expected to be the largest during the forecast period

The 28 nm to 45 nm segment is expected to account for the largest market share during the forecast period, representing the optimal balance between cost, performance, and manufacturing maturity for eNVM integration. This technology node range supports mature embedded Flash processes that offer reliable program/erase endurance and data retention while maintaining reasonable wafer costs. A vast installed base of microcontroller units for automotive body electronics, industrial control, and consumer

appliances continues to be designed in these nodes due to established design flows and foundry capacity. The node range also serves as the transition point for emerging memories like MRAM entering volume production, ensuring this segment remains dominant throughout the forecast timeline.

The SoC Integrated eNVM segment is expected to have the highest CAGR during the forecast period

Over the forecast period, the SoC Integrated eNVM segment is predicted to witness the highest growth rate, driven by increasing complexity of system-on-chip designs requiring on-die memory for code storage and secure data retention. Modern SoCs powering smartphones, AI accelerators, and automotive domain controllers integrate multiple processing cores, requiring substantial embedded non-volatile memory for boot code, encryption keys, and calibration data. The shift toward heterogeneous integration and chiplets further boosts demand as memory is tightly coupled with compute dies for performance optimization. As leading-edge applications push below 16nm, emerging eNVM technologies such as embedded MRAM become essential for SoC integration, making this the fastest-growing integration type.

Region with largest share:

During the forecast period, the Asia Pacific region is expected to hold the largest market share, reflecting the region's dominance in semiconductor manufacturing, assembly, and electronics assembly. Countries including Taiwan, South Korea, China, and Japan host the world's leading foundries and integrated device manufacturers producing eNVM-enabled chips for global consumption. The concentration of consumer electronics, automotive, and industrial equipment manufacturing within the region creates a robust local demand base for embedded memory solutions. Government initiatives supporting domestic semiconductor capacity, particularly China's self-sufficiency goals, further reinforce Asia Pacific's leadership. The region's comprehensive ecosystem from materials to fabrication to end-product assembly ensures its dominant market position throughout the forecast period.

Region with highest CAGR:

Over the forecast period, the North America region is anticipated to exhibit the highest CAGR, driven by strong design activity for advanced eNVM solutions targeting automotive, aerospace, and data center applications. The region hosts leading fabless semiconductor companies and automotive electronics suppliers that are early adopters

of emerging memory technologies like MRAM and RRAM integrated into advanced nodes below 28nm. Significant research and development investment from government agencies and venture capital supports next-generation eNVM innovation. Additionally, the reshoring of semiconductor manufacturing through the CHIPS Act is expanding domestic foundry capacity for embedded memory production, accelerating regional market growth beyond traditional consumption patterns and establishing North America as the fastest-growing market.

Key players in the market

Some of the key players in Embedded Non Volatile Memory Market include Samsung Electronics Co. Ltd., Micron Technology Inc., SK hynix Inc., Kioxia Holdings Corporation, Western Digital Corporation, Winbond Electronics Corporation, Macronix International Co. Ltd., Infineon Technologies AG, STMicroelectronics N.V., Texas Instruments Incorporated, NXP Semiconductors N.V., Renesas Electronics Corporation, Microchip Technology Incorporated, Fujitsu Limited, Rambus Inc., ROHM Co. Ltd., Cypress Semiconductor Corporation, and Intel Corporation.

Key Developments:

In May 2026, Samsung Electronics and SK Hynix increased production of embedded MRAM (eMRAM) modules specifically for the Asia-Pacific region, responding to a 25% annual increase in IoT and mobile AI application requirements.

In May 2026, Western Digital and Kioxia reached a production milestone for 3D BiCS FLASH technology, achieving higher vertical stacking layers to reduce the cost-per-bit for high-density embedded systems.

In March 2026, Infineon Technologies and NXP Semiconductors announced a joint initiative to integrate AI-optimized non-volatile memory into Autonomous Driving Assistance Systems (ADAS), targeting Level 3 autonomy requirements.

In January 2026, STMicroelectronics launched a new series of low-power Ferroelectric RAM (FRAM) modules designed for European smart manufacturing and automotive ECUs to comply with tightening energy efficiency regulations.

Memory Types Covered:

Embedded Flash Memory

Embedded EEPROM

Embedded MRAM

Embedded ReRAM / RRAM

Embedded FRAM / FeRAM

Embedded PCM / PCRAM

Embedded FeFET

Other Emerging eNVM Technologies

Storage Densities Covered:

Less than 1 Mb

1 Mb to 16 Mb

16 Mb to 64 Mb

64 Mb to 256 Mb

Above 256 Mb

Technology Nodes Covered:

Above 65 nm

45 nm to 65 nm

28 nm to 45 nm

16 nm to 28 nm

Below 16 nm

Integration Types Covered:

SoC Integrated eNVM

MCU Integrated eNVM

ASIC Integrated eNVM

Memory IP Integration

Device Types Covered:

Microcontrollers

Microprocessors

ASICs

FPGAs

Sensor ICs

Connectivity ICs

AI Accelerators

Power Management ICs

Applications Covered:

Code Storage

Firmware Storage

Data Logging

Secure Key Storage

Configuration and Calibration Storage

AI and Edge Model Storage

Boot Memory

End Users Covered:

Consumer Electronics

Automotive

Industrial

Telecommunications

Healthcare

Aerospace & Defense

Data Centers

Energy & Utilities

IoT and Edge Devices

Regions Covered:

North America

United States

Canada

Mexico

Europe

United Kingdom

Germany

France

Italy

Spain

Netherlands

Belgium

Sweden

Switzerland

Poland

Rest of Europe

Asia Pacific

China

Japan

India

South Korea

Australia

Indonesia

Thailand

Malaysia

Singapore

Vietnam

Rest of Asia Pacific

South America

Brazil

Argentina

Colombia

Chile

Peru

Rest of South America

Rest of the World (RoW)

Middle East

Saudi Arabia

United Arab Emirates

Qatar

Israel

Rest of Middle East

Africa

South Africa

Egypt

Morocco

Rest of Africa

What our report offers:

Market share assessments for the regional and country-level segments

Strategic recommendations for the new entrants

Covers Market data for the years 2023, 2024, 2025, 2026, 2027, 2028, 2030, 2032 and 2034

Market Trends (Drivers, Constraints, Opportunities, Threats, Challenges, Investment Opportunities, and recommendations)

Strategic recommendations in key business segments based on the market estimations

Competitive landscaping mapping the key common trends

Company profiling with detailed strategies, financials, and recent developments

Supply chain trends mapping the latest technological advancements

Free Customization Offerings:

All the customers of this report will be entitled to receive one of the following free customization options:

Company Profiling

Comprehensive profiling of additional market players (up to 3)

SWOT Analysis of key players (up to 3)

Regional Segmentation

Market estimations, Forecasts and CAGR of any prominent country as per the client's interest (Note: Depends on feasibility check)

Competitive Benchmarking

Benchmarking of key players based on product portfolio, geographical presence, and strategic alliances

Contents

1 EXECUTIVE SUMMARY

- 1.1 Market Snapshot and Key Highlights
- 1.2 Growth Drivers, Challenges, and Opportunities
- 1.3 Competitive Landscape Overview
- 1.4 Strategic Insights and Recommendations

2 RESEARCH FRAMEWORK

- 2.1 Study Objectives and Scope
- 2.2 Stakeholder Analysis
- 2.3 Research Assumptions and Limitations
- 2.4 Research Methodology
 - 2.4.1 Data Collection (Primary and Secondary)
 - 2.4.2 Data Modeling and Estimation Techniques
 - 2.4.3 Data Validation and Triangulation
 - 2.4.4 Analytical and Forecasting Approach

3 MARKET DYNAMICS AND TREND ANALYSIS

- 3.1 Market Definition and Structure
- 3.2 Key Market Drivers
- 3.3 Market Restraints and Challenges
- 3.4 Growth Opportunities and Investment Hotspots
- 3.5 Industry Threats and Risk Assessment
- 3.6 Technology and Innovation Landscape
- 3.7 Emerging and High-Growth Markets
- 3.8 Regulatory and Policy Environment
- 3.9 Impact of COVID-19 and Recovery Outlook

4 COMPETITIVE AND STRATEGIC ASSESSMENT

- 4.1 Porter's Five Forces Analysis
 - 4.1.1 Supplier Bargaining Power
 - 4.1.2 Buyer Bargaining Power
 - 4.1.3 Threat of Substitutes
 - 4.1.4 Threat of New Entrants

- 4.1.5 Competitive Rivalry
- 4.2 Market Share Analysis of Key Players
- 4.3 Product Benchmarking and Performance Comparison

5 GLOBAL EMBEDDED NON-VOLATILE MEMORY MARKET, BY MEMORY TYPE

- 5.1 Embedded Flash Memory
 - 5.1.1 NOR Flash
 - 5.1.2 NAND Flash
- 5.2 Embedded EEPROM
- 5.3 Embedded MRAM
- 5.4 Embedded ReRAM / RRAM
- 5.5 Embedded FRAM / FeRAM
- 5.6 Embedded PCM / PCRAM
- 5.7 Embedded FeFET
- 5.8 Other Emerging eNVM Technologies

6 GLOBAL EMBEDDED NON-VOLATILE MEMORY MARKET, BY STORAGE DENSITY

- 6.1 Less than 1 Mb
- 6.2 1 Mb to 16 Mb
- 6.3 16 Mb to 64 Mb
- 6.4 64 Mb to 256 Mb
- 6.5 Above 256 Mb

7 GLOBAL EMBEDDED NON-VOLATILE MEMORY MARKET, BY TECHNOLOGY NODE

- 7.1 Above 65 nm
- 7.2 45 nm to 65 nm
- 7.3 28 nm to 45 nm
- 7.4 16 nm to 28 nm
- 7.5 Below 16 nm

8 GLOBAL EMBEDDED NON-VOLATILE MEMORY MARKET, BY INTEGRATION TYPE

- 8.1 SoC Integrated eNVM

- 8.2 MCU Integrated eNVM
- 8.3 ASIC Integrated eNVM
- 8.4 Memory IP Integration

9 GLOBAL EMBEDDED NON-VOLATILE MEMORY MARKET, BY DEVICE TYPE

- 9.1 Microcontrollers
- 9.2 Microprocessors
- 9.3 ASICs
- 9.4 FPGAs
- 9.5 Sensor ICs
- 9.6 Connectivity ICs
- 9.7 AI Accelerators
- 9.8 Power Management ICs

10 GLOBAL EMBEDDED NON-VOLATILE MEMORY MARKET, BY APPLICATION

- 10.1 Code Storage
- 10.2 Firmware Storage
- 10.3 Data Logging
- 10.4 Secure Key Storage
- 10.5 Configuration and Calibration Storage
- 10.6 AI and Edge Model Storage
- 10.7 Boot Memory

11 GLOBAL EMBEDDED NON-VOLATILE MEMORY MARKET, BY END-USE INDUSTRY

- 11.1 Consumer Electronics
 - 11.1.1 Smartphones
 - 11.1.2 Wearables
 - 11.1.3 Smart Home Devices
 - 11.1.4 Gaming Devices
- 11.2 Automotive
 - 11.2.1 ADAS
 - 11.2.2 Infotainment Systems
 - 11.2.3 Battery Management Systems
 - 11.2.4 Autonomous Driving Systems
- 11.3 Industrial

- 11.3.1 Industrial Automation
- 11.3.2 Robotics
- 11.3.3 Smart Manufacturing Systems
- 11.4 Telecommunications
 - 11.4.1 5G Infrastructure
 - 11.4.2 Networking Equipment
- 11.5 Healthcare
 - 11.5.1 Medical Devices
 - 11.5.2 Implantable Electronics
- 11.6 Aerospace & Defense
- 11.7 Data Centers
- 11.8 Energy & Utilities
- 11.9 IoT and Edge Devices

12 GLOBAL EMBEDDED NON-VOLATILE MEMORY MARKET, BY GEOGRAPHY

- 12.1 North America
 - 12.1.1 United States
 - 12.1.2 Canada
 - 12.1.3 Mexico
- 12.2 Europe
 - 12.2.1 United Kingdom
 - 12.2.2 Germany
 - 12.2.3 France
 - 12.2.4 Italy
 - 12.2.5 Spain
 - 12.2.6 Netherlands
 - 12.2.7 Belgium
 - 12.2.8 Sweden
 - 12.2.9 Switzerland
 - 12.2.10 Poland
 - 12.2.11 Rest of Europe
- 12.3 Asia Pacific
 - 12.3.1 China
 - 12.3.2 Japan
 - 12.3.3 India
 - 12.3.4 South Korea
 - 12.3.5 Australia
 - 12.3.6 Indonesia

- 12.3.7 Thailand
- 12.3.8 Malaysia
- 12.3.9 Singapore
- 12.3.10 Vietnam
- 12.3.11 Rest of Asia Pacific
- 12.4 South America
 - 12.4.1 Brazil
 - 12.4.2 Argentina
 - 12.4.3 Colombia
 - 12.4.4 Chile
 - 12.4.5 Peru
 - 12.4.6 Rest of South America
- 12.5 Rest of the World (RoW)
 - 12.5.1 Middle East
 - 12.5.1.1 Saudi Arabia
 - 12.5.1.2 United Arab Emirates
 - 12.5.1.3 Qatar
 - 12.5.1.4 Israel
 - 12.5.1.5 Rest of Middle East
 - 12.5.2 Africa
 - 12.5.2.1 South Africa
 - 12.5.2.2 Egypt
 - 12.5.2.3 Morocco
 - 12.5.2.4 Rest of Africa

13 STRATEGIC MARKET INTELLIGENCE

- 13.1 Industry Value Network and Supply Chain Assessment
- 13.2 White-Space and Opportunity Mapping
- 13.3 Product Evolution and Market Life Cycle Analysis
- 13.4 Channel, Distributor, and Go-to-Market Assessment

14 INDUSTRY DEVELOPMENTS AND STRATEGIC INITIATIVES

- 14.1 Mergers and Acquisitions
- 14.2 Partnerships, Alliances, and Joint Ventures
- 14.3 New Product Launches and Certifications
- 14.4 Capacity Expansion and Investments
- 14.5 Other Strategic Initiatives

15 COMPANY PROFILES

- 15.1 Samsung Electronics Co. Ltd.
- 15.2 Micron Technology Inc.
- 15.3 SK hynix Inc.
- 15.4 Kioxia Holdings Corporation
- 15.5 Western Digital Corporation
- 15.6 Winbond Electronics Corporation
- 15.7 Macronix International Co. Ltd.
- 15.8 Infineon Technologies AG
- 15.9 STMicroelectronics N.V.
- 15.10 Texas Instruments Incorporated
- 15.11 NXP Semiconductors N.V.
- 15.12 Renesas Electronics Corporation
- 15.13 Microchip Technology Incorporated
- 15.14 Fujitsu Limited
- 15.15 Rambus Inc.
- 15.16 ROHM Co. Ltd.
- 15.17 Cypress Semiconductor Corporation
- 15.18 Intel Corporation

List Of Tables

LIST OF TABLES

- Table 1 Global Embedded Non-Volatile Memory Market Outlook, By Region (2023–2034) (\$MN)
- Table 2 Global Embedded Non-Volatile Memory Market Outlook, By Memory Type (2023–2034) (\$MN)
- Table 3 Global Embedded Non-Volatile Memory Market Outlook, By Embedded Flash Memory (2023–2034) (\$MN)
- Table 4 Global Embedded Non-Volatile Memory Market Outlook, By NOR Flash (2023–2034) (\$MN)
- Table 5 Global Embedded Non-Volatile Memory Market Outlook, By NAND Flash (2023–2034) (\$MN)
- Table 6 Global Embedded Non-Volatile Memory Market Outlook, By Embedded EEPROM (2023–2034) (\$MN)
- Table 7 Global Embedded Non-Volatile Memory Market Outlook, By Embedded MRAM (2023–2034) (\$MN)
- Table 8 Global Embedded Non-Volatile Memory Market Outlook, By Embedded ReRAM / RRAM (2023–2034) (\$MN)
- Table 9 Global Embedded Non-Volatile Memory Market Outlook, By Embedded FRAM / FeRAM (2023–2034) (\$MN)
- Table 10 Global Embedded Non-Volatile Memory Market Outlook, By Embedded PCM / PCRAM (2023–2034) (\$MN)
- Table 11 Global Embedded Non-Volatile Memory Market Outlook, By Embedded FeFET (2023–2034) (\$MN)
- Table 12 Global Embedded Non-Volatile Memory Market Outlook, By Other Emerging eNVM Technologies (2023–2034) (\$MN)
- Table 13 Global Embedded Non-Volatile Memory Market Outlook, By Storage Density (2023–2034) (\$MN)
- Table 14 Global Embedded Non-Volatile Memory Market Outlook, By Less than 1 Mb (2023–2034) (\$MN)
- Table 15 Global Embedded Non-Volatile Memory Market Outlook, By 1 Mb to 16 Mb (2023–2034) (\$MN)
- Table 16 Global Embedded Non-Volatile Memory Market Outlook, By 16 Mb to 64 Mb (2023–2034) (\$MN)
- Table 17 Global Embedded Non-Volatile Memory Market Outlook, By 64 Mb to 256 Mb (2023–2034) (\$MN)
- Table 18 Global Embedded Non-Volatile Memory Market Outlook, By Above 256 Mb

(2023–2034) (\$MN)

Table 19 Global Embedded Non-Volatile Memory Market Outlook, By Technology Node (2023–2034) (\$MN)

Table 20 Global Embedded Non-Volatile Memory Market Outlook, By Above 65 nm (2023–2034) (\$MN)

Table 21 Global Embedded Non-Volatile Memory Market Outlook, By 45 nm to 65 nm (2023–2034) (\$MN)

Table 22 Global Embedded Non-Volatile Memory Market Outlook, By 28 nm to 45 nm (2023–2034) (\$MN)

Table 23 Global Embedded Non-Volatile Memory Market Outlook, By 16 nm to 28 nm (2023–2034) (\$MN)

Table 24 Global Embedded Non-Volatile Memory Market Outlook, By Below 16 nm (2023–2034) (\$MN)

Table 25 Global Embedded Non-Volatile Memory Market Outlook, By Integration Type (2023–2034) (\$MN)

Table 26 Global Embedded Non-Volatile Memory Market Outlook, By SoC Integrated eNVM (2023–2034) (\$MN)

Table 27 Global Embedded Non-Volatile Memory Market Outlook, By MCU Integrated eNVM (2023–2034) (\$MN)

Table 28 Global Embedded Non-Volatile Memory Market Outlook, By ASIC Integrated eNVM (2023–2034) (\$MN)

Table 29 Global Embedded Non-Volatile Memory Market Outlook, By Memory IP Integration (2023–2034) (\$MN)

Table 30 Global Embedded Non-Volatile Memory Market Outlook, By Device Type (2023–2034) (\$MN)

Table 31 Global Embedded Non-Volatile Memory Market Outlook, By Microcontrollers (2023–2034) (\$MN)

Table 32 Global Embedded Non-Volatile Memory Market Outlook, By Microprocessors (2023–2034) (\$MN)

Table 33 Global Embedded Non-Volatile Memory Market Outlook, By ASICs (2023–2034) (\$MN)

Table 34 Global Embedded Non-Volatile Memory Market Outlook, By FPGAs (2023–2034) (\$MN)

Table 35 Global Embedded Non-Volatile Memory Market Outlook, By Sensor ICs (2023–2034) (\$MN)

Table 36 Global Embedded Non-Volatile Memory Market Outlook, By Connectivity ICs (2023–2034) (\$MN)

Table 37 Global Embedded Non-Volatile Memory Market Outlook, By AI Accelerators (2023–2034) (\$MN)

- Table 38 Global Embedded Non-Volatile Memory Market Outlook, By Power Management ICs (2023–2034) (\$MN)
- Table 39 Global Embedded Non-Volatile Memory Market Outlook, By Application (2023–2034) (\$MN)
- Table 40 Global Embedded Non-Volatile Memory Market Outlook, By Code Storage (2023–2034) (\$MN)
- Table 41 Global Embedded Non-Volatile Memory Market Outlook, By Firmware Storage (2023–2034) (\$MN)
- Table 42 Global Embedded Non-Volatile Memory Market Outlook, By Data Logging (2023–2034) (\$MN)
- Table 43 Global Embedded Non-Volatile Memory Market Outlook, By Secure Key Storage (2023–2034) (\$MN)
- Table 44 Global Embedded Non-Volatile Memory Market Outlook, By Configuration and Calibration Storage (2023–2034) (\$MN)
- Table 45 Global Embedded Non-Volatile Memory Market Outlook, By AI and Edge Model Storage (2023–2034) (\$MN)
- Table 46 Global Embedded Non-Volatile Memory Market Outlook, By Boot Memory (2023–2034) (\$MN)
- Table 47 Global Embedded Non-Volatile Memory Market Outlook, By End-Use Industry (2023–2034) (\$MN)
- Table 48 Global Embedded Non-Volatile Memory Market Outlook, By Consumer Electronics (2023–2034) (\$MN)
- Table 49 Global Embedded Non-Volatile Memory Market Outlook, By Smartphones (2023–2034) (\$MN)
- Table 50 Global Embedded Non-Volatile Memory Market Outlook, By Wearables (2023–2034) (\$MN)
- Table 51 Global Embedded Non-Volatile Memory Market Outlook, By Smart Home Devices (2023–2034) (\$MN)
- Table 52 Global Embedded Non-Volatile Memory Market Outlook, By Gaming Devices (2023–2034) (\$MN)
- Table 53 Global Embedded Non-Volatile Memory Market Outlook, By Automotive (2023–2034) (\$MN)
- Table 54 Global Embedded Non-Volatile Memory Market Outlook, By ADAS (2023–2034) (\$MN)
- Table 55 Global Embedded Non-Volatile Memory Market Outlook, By Infotainment Systems (2023–2034) (\$MN)
- Table 56 Global Embedded Non-Volatile Memory Market Outlook, By Battery Management Systems (2023–2034) (\$MN)
- Table 57 Global Embedded Non-Volatile Memory Market Outlook, By Autonomous

Driving Systems (2023–2034) (\$MN)

Table 58 Global Embedded Non-Volatile Memory Market Outlook, By Industrial (2023–2034) (\$MN)

Table 59 Global Embedded Non-Volatile Memory Market Outlook, By Industrial Automation (2023–2034) (\$MN)

Table 60 Global Embedded Non-Volatile Memory Market Outlook, By Robotics (2023–2034) (\$MN)

Table 61 Global Embedded Non-Volatile Memory Market Outlook, By Smart Manufacturing Systems (2023–2034) (\$MN)

Table 62 Global Embedded Non-Volatile Memory Market Outlook, By Telecommunications (2023–2034) (\$MN)

Table 63 Global Embedded Non-Volatile Memory Market Outlook, By 5G Infrastructure (2023–2034) (\$MN)

Table 64 Global Embedded Non-Volatile Memory Market Outlook, By Networking Equipment (2023–2034) (\$MN)

Table 65 Global Embedded Non-Volatile Memory Market Outlook, By Healthcare (2023–2034) (\$MN)

Table 66 Global Embedded Non-Volatile Memory Market Outlook, By Medical Devices (2023–2034) (\$MN)

Table 67 Global Embedded Non-Volatile Memory Market Outlook, By Implantable Electronics (2023–2034) (\$MN)

Table 68 Global Embedded Non-Volatile Memory Market Outlook, By Aerospace & Defense (2023–2034) (\$MN)

Table 69 Global Embedded Non-Volatile Memory Market Outlook, By Data Centers (2023–2034) (\$MN)

Table 70 Global Embedded Non-Volatile Memory Market Outlook, By Energy & Utilities (2023–2034) (\$MN)

Table 71 Global Embedded Non-Volatile Memory Market Outlook, By IoT and Edge Devices (2023–2034) (\$MN)

Note: Tables for North America, Europe, APAC, South America, and Rest of the World (RoW) Regions are also represented in the same manner as above.

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