

# Global Wafer Level Package Dielectrics Sales Market Report 2018

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## Abstracts

In this report, the global Wafer Level Package Dielectrics market is valued at USD XX million in 2017 and is expected to reach USD XX million by the end of 2025, growing at a CAGR of XX% between 2017 and 2025.

Geographically, this report split global into several key Regions, with sales (K Units), revenue (Million USD), market share and growth rate of Wafer Level Package Dielectrics for these regions, from 2013 to 2025 (forecast), covering

United States

China

Europe

Japan

Southeast Asia

India

Global Wafer Level Package Dielectrics market competition by top manufacturers/players, with Wafer Level Package Dielectrics sales volume, Price (USD/Unit), revenue (Million USD) and market share for each manufacturer/player; the top players including

ChipMOS TECHNOLOGIES INC

STATS ChipPAC Ltd

IQE PLC

Amkor Technology Inc

TriQuint Semiconductor Inc

Deca Technologies

KLA-Tencor Corporation

Siliconware Precision Industries Co. Ltd

China Wafer Level CSP Co. Ltd

Jiangsu Changjiang Electronics Technology Co. Ltd

On the basis of product, this report displays the production, revenue, price, market share and growth rate of each type, primarily split into

FOWLP (Fan-Out Wafer Level Package)

FIWLP (Fan-in Wafer Level Package)

FIWLCSP (Fan-in Wafer Level Chip Scale Package)

Others

On the basis on the end users/applications, this report focuses on the status and outlook for major applications/end users, sales volume, market share and growth rate for each application, including

Consumer Electronics

Automotive

Healthcare

Aerospace & Defense

If you have any special requirements, please let us know and we will offer you the report as you want.

## Contents

### Global Wafer Level Package Dielectrics Sales Market Report 2018

## 1 WAFER LEVEL PACKAGE DIELECTRICS MARKET OVERVIEW

- 1.1 Product Overview and Scope of Wafer Level Package Dielectrics
- 1.2 Classification of Wafer Level Package Dielectrics by Product Category
  - 1.2.1 Global Wafer Level Package Dielectrics Market Size (Sales) Comparison by Type (2013-2025)
  - 1.2.2 Global Wafer Level Package Dielectrics Market Size (Sales) Market Share by Type (Product Category) in 2017
  - 1.2.3 FOWLP (Fan-Out Wafer Level Package)
  - 1.2.4 FIWLP (Fan-in Wafer Level Package)
  - 1.2.5 FIWLCSP (Fan-in Wafer Level Chip Scale Package)
  - 1.2.6 Others
- 1.3 Global Wafer Level Package Dielectrics Market by Application/End Users
  - 1.3.1 Global Wafer Level Package Dielectrics Sales (Volume) and Market Share Comparison by Application (2013-2025)
  - 1.3.2 Consumer Electronics
  - 1.3.3 Automotive
  - 1.3.4 Healthcare
  - 1.3.5 Aerospace & Defense
- 1.4 Global Wafer Level Package Dielectrics Market by Region
  - 1.4.1 Global Wafer Level Package Dielectrics Market Size (Value) Comparison by Region (2013-2025)
  - 1.4.2 United States Wafer Level Package Dielectrics Status and Prospect (2013-2025)
  - 1.4.3 China Wafer Level Package Dielectrics Status and Prospect (2013-2025)
  - 1.4.4 Europe Wafer Level Package Dielectrics Status and Prospect (2013-2025)
  - 1.4.5 Japan Wafer Level Package Dielectrics Status and Prospect (2013-2025)
  - 1.4.6 Southeast Asia Wafer Level Package Dielectrics Status and Prospect (2013-2025)
  - 1.4.7 India Wafer Level Package Dielectrics Status and Prospect (2013-2025)
- 1.5 Global Market Size (Value and Volume) of Wafer Level Package Dielectrics (2013-2025)
  - 1.5.1 Global Wafer Level Package Dielectrics Sales and Growth Rate (2013-2025)
  - 1.5.2 Global Wafer Level Package Dielectrics Revenue and Growth Rate (2013-2025)

## 2 GLOBAL WAFER LEVEL PACKAGE DIELECTRICS COMPETITION BY

## **PLAYERS/SUPPLIERS, TYPE AND APPLICATION**

### 2.1 Global Wafer Level Package Dielectrics Market Competition by Players/Suppliers

2.1.1 Global Wafer Level Package Dielectrics Sales and Market Share of Key Players/Suppliers (2013-2018)

2.1.2 Global Wafer Level Package Dielectrics Revenue and Share by Players/Suppliers (2013-2018)

### 2.2 Global Wafer Level Package Dielectrics (Volume and Value) by Type

2.2.1 Global Wafer Level Package Dielectrics Sales and Market Share by Type (2013-2018)

2.2.2 Global Wafer Level Package Dielectrics Revenue and Market Share by Type (2013-2018)

### 2.3 Global Wafer Level Package Dielectrics (Volume and Value) by Region

2.3.1 Global Wafer Level Package Dielectrics Sales and Market Share by Region (2013-2018)

2.3.2 Global Wafer Level Package Dielectrics Revenue and Market Share by Region (2013-2018)

### 2.4 Global Wafer Level Package Dielectrics (Volume) by Application

## **3 UNITED STATES WAFER LEVEL PACKAGE DIELECTRICS (VOLUME, VALUE AND SALES PRICE)**

### 3.1 United States Wafer Level Package Dielectrics Sales and Value (2013-2018)

3.1.1 United States Wafer Level Package Dielectrics Sales and Growth Rate (2013-2018)

3.1.2 United States Wafer Level Package Dielectrics Revenue and Growth Rate (2013-2018)

3.1.3 United States Wafer Level Package Dielectrics Sales Price Trend (2013-2018)

3.2 United States Wafer Level Package Dielectrics Sales Volume and Market Share by Players (2013-2018)

3.3 United States Wafer Level Package Dielectrics Sales Volume and Market Share by Type (2013-2018)

3.4 United States Wafer Level Package Dielectrics Sales Volume and Market Share by Application (2013-2018)

## **4 CHINA WAFER LEVEL PACKAGE DIELECTRICS (VOLUME, VALUE AND SALES PRICE)**

4.1 China Wafer Level Package Dielectrics Sales and Value (2013-2018)

- 4.1.1 China Wafer Level Package Dielectrics Sales and Growth Rate (2013-2018)
- 4.1.2 China Wafer Level Package Dielectrics Revenue and Growth Rate (2013-2018)
- 4.1.3 China Wafer Level Package Dielectrics Sales Price Trend (2013-2018)
- 4.2 China Wafer Level Package Dielectrics Sales Volume and Market Share by Players (2013-2018)
- 4.3 China Wafer Level Package Dielectrics Sales Volume and Market Share by Type (2013-2018)
- 4.4 China Wafer Level Package Dielectrics Sales Volume and Market Share by Application (2013-2018)

## **5 EUROPE WAFER LEVEL PACKAGE DIELECTRICS (VOLUME, VALUE AND SALES PRICE)**

- 5.1 Europe Wafer Level Package Dielectrics Sales and Value (2013-2018)
  - 5.1.1 Europe Wafer Level Package Dielectrics Sales and Growth Rate (2013-2018)
  - 5.1.2 Europe Wafer Level Package Dielectrics Revenue and Growth Rate (2013-2018)
  - 5.1.3 Europe Wafer Level Package Dielectrics Sales Price Trend (2013-2018)
- 5.2 Europe Wafer Level Package Dielectrics Sales Volume and Market Share by Players (2013-2018)
- 5.3 Europe Wafer Level Package Dielectrics Sales Volume and Market Share by Type (2013-2018)
- 5.4 Europe Wafer Level Package Dielectrics Sales Volume and Market Share by Application (2013-2018)

## **6 JAPAN WAFER LEVEL PACKAGE DIELECTRICS (VOLUME, VALUE AND SALES PRICE)**

- 6.1 Japan Wafer Level Package Dielectrics Sales and Value (2013-2018)
  - 6.1.1 Japan Wafer Level Package Dielectrics Sales and Growth Rate (2013-2018)
  - 6.1.2 Japan Wafer Level Package Dielectrics Revenue and Growth Rate (2013-2018)
  - 6.1.3 Japan Wafer Level Package Dielectrics Sales Price Trend (2013-2018)
- 6.2 Japan Wafer Level Package Dielectrics Sales Volume and Market Share by Players (2013-2018)
- 6.3 Japan Wafer Level Package Dielectrics Sales Volume and Market Share by Type (2013-2018)
- 6.4 Japan Wafer Level Package Dielectrics Sales Volume and Market Share by Application (2013-2018)

## **7 SOUTHEAST ASIA WAFER LEVEL PACKAGE DIELECTRICS (VOLUME, VALUE**

## **AND SALES PRICE)**

### 7.1 Southeast Asia Wafer Level Package Dielectrics Sales and Value (2013-2018)

7.1.1 Southeast Asia Wafer Level Package Dielectrics Sales and Growth Rate (2013-2018)

7.1.2 Southeast Asia Wafer Level Package Dielectrics Revenue and Growth Rate (2013-2018)

7.1.3 Southeast Asia Wafer Level Package Dielectrics Sales Price Trend (2013-2018)

7.2 Southeast Asia Wafer Level Package Dielectrics Sales Volume and Market Share by Players (2013-2018)

7.3 Southeast Asia Wafer Level Package Dielectrics Sales Volume and Market Share by Type (2013-2018)

7.4 Southeast Asia Wafer Level Package Dielectrics Sales Volume and Market Share by Application (2013-2018)

## **8 INDIA WAFER LEVEL PACKAGE DIELECTRICS (VOLUME, VALUE AND SALES PRICE)**

### 8.1 India Wafer Level Package Dielectrics Sales and Value (2013-2018)

8.1.1 India Wafer Level Package Dielectrics Sales and Growth Rate (2013-2018)

8.1.2 India Wafer Level Package Dielectrics Revenue and Growth Rate (2013-2018)

8.1.3 India Wafer Level Package Dielectrics Sales Price Trend (2013-2018)

8.2 India Wafer Level Package Dielectrics Sales Volume and Market Share by Players (2013-2018)

8.3 India Wafer Level Package Dielectrics Sales Volume and Market Share by Type (2013-2018)

8.4 India Wafer Level Package Dielectrics Sales Volume and Market Share by Application (2013-2018)

## **9 GLOBAL WAFER LEVEL PACKAGE DIELECTRICS PLAYERS/SUPPLIERS PROFILES AND SALES DATA**

### 9.1 ChipMOS TECHNOLOGIES INC

9.1.1 Company Basic Information, Manufacturing Base and Competitors

9.1.2 Wafer Level Package Dielectrics Product Category, Application and Specification

9.1.2.1 Product A

9.1.2.2 Product B

9.1.3 ChipMOS TECHNOLOGIES INC Wafer Level Package Dielectrics Sales, Revenue, Price and Gross Margin (2013-2018)

- 9.1.4 Main Business/Business Overview
- 9.2 STATS ChipPAC Ltd
  - 9.2.1 Company Basic Information, Manufacturing Base and Competitors
  - 9.2.2 Wafer Level Package Dielectrics Product Category, Application and Specification
    - 9.2.2.1 Product A
    - 9.2.2.2 Product B
  - 9.2.3 STATS ChipPAC Ltd Wafer Level Package Dielectrics Sales, Revenue, Price and Gross Margin (2013-2018)
  - 9.2.4 Main Business/Business Overview
- 9.3 IQE PLC
  - 9.3.1 Company Basic Information, Manufacturing Base and Competitors
  - 9.3.2 Wafer Level Package Dielectrics Product Category, Application and Specification
    - 9.3.2.1 Product A
    - 9.3.2.2 Product B
  - 9.3.3 IQE PLC Wafer Level Package Dielectrics Sales, Revenue, Price and Gross Margin (2013-2018)
  - 9.3.4 Main Business/Business Overview
- 9.4 Amkor Technology Inc
  - 9.4.1 Company Basic Information, Manufacturing Base and Competitors
  - 9.4.2 Wafer Level Package Dielectrics Product Category, Application and Specification
    - 9.4.2.1 Product A
    - 9.4.2.2 Product B
  - 9.4.3 Amkor Technology Inc Wafer Level Package Dielectrics Sales, Revenue, Price and Gross Margin (2013-2018)
  - 9.4.4 Main Business/Business Overview
- 9.5 TriQuint Semiconductor Inc
  - 9.5.1 Company Basic Information, Manufacturing Base and Competitors
  - 9.5.2 Wafer Level Package Dielectrics Product Category, Application and Specification
    - 9.5.2.1 Product A
    - 9.5.2.2 Product B
  - 9.5.3 TriQuint Semiconductor Inc Wafer Level Package Dielectrics Sales, Revenue, Price and Gross Margin (2013-2018)
  - 9.5.4 Main Business/Business Overview
- 9.6 Deca Technologies
  - 9.6.1 Company Basic Information, Manufacturing Base and Competitors
  - 9.6.2 Wafer Level Package Dielectrics Product Category, Application and Specification
    - 9.6.2.1 Product A
    - 9.6.2.2 Product B
  - 9.6.3 Deca Technologies Wafer Level Package Dielectrics Sales, Revenue, Price and



## Gross Margin (2013-2018)

### 9.6.4 Main Business/Business Overview

## 9.7 KLA-Tencor Corporation

### 9.7.1 Company Basic Information, Manufacturing Base and Competitors

### 9.7.2 Wafer Level Package Dielectrics Product Category, Application and Specification

#### 9.7.2.1 Product A

#### 9.7.2.2 Product B

### 9.7.3 KLA-Tencor Corporation Wafer Level Package Dielectrics Sales, Revenue, Price and Gross Margin (2013-2018)

### 9.7.4 Main Business/Business Overview

## 9.8 Siliconware Precision Industries Co. Ltd

### 9.8.1 Company Basic Information, Manufacturing Base and Competitors

### 9.8.2 Wafer Level Package Dielectrics Product Category, Application and Specification

#### 9.8.2.1 Product A

#### 9.8.2.2 Product B

### 9.8.3 Siliconware Precision Industries Co. Ltd Wafer Level Package Dielectrics Sales, Revenue, Price and Gross Margin (2013-2018)

### 9.8.4 Main Business/Business Overview

## 9.9 China Wafer Level CSP Co. Ltd

### 9.9.1 Company Basic Information, Manufacturing Base and Competitors

### 9.9.2 Wafer Level Package Dielectrics Product Category, Application and Specification

#### 9.9.2.1 Product A

#### 9.9.2.2 Product B

### 9.9.3 China Wafer Level CSP Co. Ltd Wafer Level Package Dielectrics Sales, Revenue, Price and Gross Margin (2013-2018)

### 9.9.4 Main Business/Business Overview

## 9.10 Jiangsu Changjiang Electronics Technology Co. Ltd

### 9.10.1 Company Basic Information, Manufacturing Base and Competitors

### 9.10.2 Wafer Level Package Dielectrics Product Category, Application and Specification

#### 9.10.2.1 Product A

#### 9.10.2.2 Product B

### 9.10.3 Jiangsu Changjiang Electronics Technology Co. Ltd Wafer Level Package Dielectrics Sales, Revenue, Price and Gross Margin (2013-2018)

### 9.10.4 Main Business/Business Overview

## **10 WAFER LEVEL PACKAGE DIELECTRICS MAUFACTURING COST ANALYSIS**

### 10.1 Wafer Level Package Dielectrics Key Raw Materials Analysis

- 10.1.1 Key Raw Materials
- 10.1.2 Price Trend of Key Raw Materials
- 10.1.3 Key Suppliers of Raw Materials
- 10.1.4 Market Concentration Rate of Raw Materials
- 10.2 Proportion of Manufacturing Cost Structure
  - 10.2.1 Raw Materials
  - 10.2.2 Labor Cost
  - 10.2.3 Manufacturing Process Analysis of Wafer Level Package Dielectrics
- 10.3 Manufacturing Process Analysis of Wafer Level Package Dielectrics

## **11 INDUSTRIAL CHAIN, SOURCING STRATEGY AND DOWNSTREAM BUYERS**

- 11.1 Wafer Level Package Dielectrics Industrial Chain Analysis
- 11.2 Upstream Raw Materials Sourcing
- 11.3 Raw Materials Sources of Wafer Level Package Dielectrics Major Manufacturers in 2017
- 11.4 Downstream Buyers

## **12 MARKETING STRATEGY ANALYSIS, DISTRIBUTORS/TRADERS**

- 12.1 Marketing Channel
  - 12.1.1 Direct Marketing
  - 12.1.2 Indirect Marketing
  - 12.1.3 Marketing Channel Development Trend
- 12.2 Market Positioning
  - 12.2.1 Pricing Strategy
  - 12.2.2 Brand Strategy
  - 12.2.3 Target Client
- 12.3 Distributors/Traders List

## **13 MARKET EFFECT FACTORS ANALYSIS**

- 13.1 Technology Progress/Risk
  - 13.1.1 Substitutes Threat
  - 13.1.2 Technology Progress in Related Industry
- 13.2 Consumer Needs/Customer Preference Change
- 13.3 Economic/Political Environmental Change

## **14 GLOBAL WAFER LEVEL PACKAGE DIELECTRICS MARKET FORECAST**

**(2018-2025)**

14.1 Global Wafer Level Package Dielectrics Sales Volume, Revenue and Price Forecast (2018-2025)

14.1.1 Global Wafer Level Package Dielectrics Sales Volume and Growth Rate Forecast (2018-2025)

14.1.2 Global Wafer Level Package Dielectrics Revenue and Growth Rate Forecast (2018-2025)

14.1.3 Global Wafer Level Package Dielectrics Price and Trend Forecast (2018-2025)

14.2 Global Wafer Level Package Dielectrics Sales Volume, Revenue and Growth Rate Forecast by Region (2018-2025)

14.2.1 Global Wafer Level Package Dielectrics Sales Volume and Growth Rate Forecast by Regions (2018-2025)

14.2.2 Global Wafer Level Package Dielectrics Revenue and Growth Rate Forecast by Regions (2018-2025)

14.2.3 United States Wafer Level Package Dielectrics Sales Volume, Revenue and Growth Rate Forecast (2018-2025)

14.2.4 China Wafer Level Package Dielectrics Sales Volume, Revenue and Growth Rate Forecast (2018-2025)

14.2.5 Europe Wafer Level Package Dielectrics Sales Volume, Revenue and Growth Rate Forecast (2018-2025)

14.2.6 Japan Wafer Level Package Dielectrics Sales Volume, Revenue and Growth Rate Forecast (2018-2025)

14.2.7 Southeast Asia Wafer Level Package Dielectrics Sales Volume, Revenue and Growth Rate Forecast (2018-2025)

14.2.8 India Wafer Level Package Dielectrics Sales Volume, Revenue and Growth Rate Forecast (2018-2025)

14.3 Global Wafer Level Package Dielectrics Sales Volume, Revenue and Price Forecast by Type (2018-2025)

14.3.1 Global Wafer Level Package Dielectrics Sales Forecast by Type (2018-2025)

14.3.2 Global Wafer Level Package Dielectrics Revenue Forecast by Type (2018-2025)

14.3.3 Global Wafer Level Package Dielectrics Price Forecast by Type (2018-2025)

14.4 Global Wafer Level Package Dielectrics Sales Volume Forecast by Application (2018-2025)

**15 RESEARCH FINDINGS AND CONCLUSION****16 APPENDIX**

## 16.1 Methodology/Research Approach

### 16.1.1 Research Programs/Design

### 16.1.2 Market Size Estimation

### 16.1.3 Market Breakdown and Data Triangulation

## 16.2 Data Source

### 16.2.1 Secondary Sources

### 16.2.2 Primary Sources

## 16.3 Disclaimer

The report requires updating with new data and is sent in 2-3 business days after order is placed.

## List Of Tables

### LIST OF TABLES AND FIGURES

Figure Product Picture of Wafer Level Package Dielectrics

Figure Global Wafer Level Package Dielectrics Sales Volume Comparison (K Units) by Type (2013-2025)

Figure Global Wafer Level Package Dielectrics Sales Volume Market Share by Type (Product Category) in 2017

Figure FOWLP (Fan-Out Wafer Level Package) Product Picture

Figure FIWLP (Fan-in Wafer Level Package) Product Picture

Figure FIWLCSP (Fan-in Wafer Level Chip Scale Package) Product Picture

Figure Others Product Picture

Figure Global Wafer Level Package Dielectrics Sales Comparison (K Units) by Application (2013-2025)

Figure Global Sales Market Share of Wafer Level Package Dielectrics by Application in 2017

Figure Consumer Electronics Examples

Table Key Downstream Customer in Consumer Electronics

Figure Automotive Examples

Table Key Downstream Customer in Automotive

Figure Healthcare Examples

Table Key Downstream Customer in Healthcare

Figure Aerospace & Defense Examples

Table Key Downstream Customer in Aerospace & Defense

Figure Global Wafer Level Package Dielectrics Market Size (Million USD) by Regions (2013-2025)

Figure United States Wafer Level Package Dielectrics Revenue (Million USD) and Growth Rate (2013-2025)

Figure China Wafer Level Package Dielectrics Revenue (Million USD) and Growth Rate (2013-2025)

Figure Europe Wafer Level Package Dielectrics Revenue (Million USD) and Growth Rate (2013-2025)

Figure Japan Wafer Level Package Dielectrics Revenue (Million USD) and Growth Rate (2013-2025)

Figure Southeast Asia Wafer Level Package Dielectrics Revenue (Million USD) and Growth Rate (2013-2025)

Figure India Wafer Level Package Dielectrics Revenue (Million USD) and Growth Rate (2013-2025)

Figure Global Wafer Level Package Dielectrics Sales Volume (K Units) and Growth Rate (2013-2025)

Figure Global Wafer Level Package Dielectrics Revenue (Million USD) and Growth Rate (2013-2025)

Figure Global Market Major Players Wafer Level Package Dielectrics Sales Volume (K Units) (2013-2018)

Table Global Wafer Level Package Dielectrics Sales (K Units) of Key Players/Suppliers (2013-2018)

Table Global Wafer Level Package Dielectrics Sales Share by Players/Suppliers (2013-2018)

Figure 2017 Wafer Level Package Dielectrics Sales Share by Players/Suppliers

Figure 2017 Wafer Level Package Dielectrics Sales Share by Players/Suppliers

Figure Global Wafer Level Package Dielectrics Revenue (Million USD) by Players/Suppliers (2013-2018)

Table Global Wafer Level Package Dielectrics Revenue (Million USD) by Players/Suppliers (2013-2018)

Table Global Wafer Level Package Dielectrics Revenue Share by Players/Suppliers (2013-2018)

Table 2017 Global Wafer Level Package Dielectrics Revenue Share by Players

Table 2017 Global Wafer Level Package Dielectrics Revenue Share by Players

Table Global Wafer Level Package Dielectrics Sales (K Units) and Market Share by Type (2013-2018)

Table Global Wafer Level Package Dielectrics Sales Share (K Units) by Type (2013-2018)

Figure Sales Market Share of Wafer Level Package Dielectrics by Type (2013-2018)

Figure Global Wafer Level Package Dielectrics Sales Growth Rate by Type (2013-2018)

Table Global Wafer Level Package Dielectrics Revenue (Million USD) and Market Share by Type (2013-2018)

Table Global Wafer Level Package Dielectrics Revenue Share by Type (2013-2018)

Figure Revenue Market Share of Wafer Level Package Dielectrics by Type (2013-2018)

Figure Global Wafer Level Package Dielectrics Revenue Growth Rate by Type (2013-2018)

Table Global Wafer Level Package Dielectrics Sales Volume (K Units) and Market Share by Region (2013-2018)

Table Global Wafer Level Package Dielectrics Sales Share by Region (2013-2018)

Figure Sales Market Share of Wafer Level Package Dielectrics by Region (2013-2018)

Figure Global Wafer Level Package Dielectrics Sales Growth Rate by Region in 2017

Table Global Wafer Level Package Dielectrics Revenue (Million USD) and Market Share by Region (2013-2018)

Table Global Wafer Level Package Dielectrics Revenue Share (%) by Region (2013-2018)

Figure Revenue Market Share of Wafer Level Package Dielectrics by Region (2013-2018)

Figure Global Wafer Level Package Dielectrics Revenue Growth Rate by Region in 2017

Table Global Wafer Level Package Dielectrics Revenue (Million USD) and Market Share by Region (2013-2018)

Table Global Wafer Level Package Dielectrics Revenue Share (%) by Region (2013-2018)

Figure Revenue Market Share of Wafer Level Package Dielectrics by Region (2013-2018)

Figure Global Wafer Level Package Dielectrics Revenue Market Share by Region in 2017

Table Global Wafer Level Package Dielectrics Sales Volume (K Units) and Market Share by Application (2013-2018)

Table Global Wafer Level Package Dielectrics Sales Share (%) by Application (2013-2018)

Figure Sales Market Share of Wafer Level Package Dielectrics by Application (2013-2018)

Figure Global Wafer Level Package Dielectrics Sales Market Share by Application (2013-2018)

Figure United States Wafer Level Package Dielectrics Sales (K Units) and Growth Rate (2013-2018)

Figure United States Wafer Level Package Dielectrics Revenue (Million USD) and Growth Rate (2013-2018)

Figure United States Wafer Level Package Dielectrics Sales Price (USD/Unit) Trend (2013-2018)

Table United States Wafer Level Package Dielectrics Sales Volume (K Units) by Players (2013-2018)

Table United States Wafer Level Package Dielectrics Sales Volume Market Share by Players (2013-2018)

Figure United States Wafer Level Package Dielectrics Sales Volume Market Share by Players in 2017

Table United States Wafer Level Package Dielectrics Sales Volume (K Units) by Type (2013-2018)

Table United States Wafer Level Package Dielectrics Sales Volume Market Share by Type (2013-2018)

Figure United States Wafer Level Package Dielectrics Sales Volume Market Share by



Type in 2017

Table United States Wafer Level Package Dielectrics Sales Volume (K Units) by Application (2013-2018)

Table United States Wafer Level Package Dielectrics Sales Volume Market Share by Application (2013-2018)

Figure United States Wafer Level Package Dielectrics Sales Volume Market Share by Application in 2017

Figure China Wafer Level Package Dielectrics Sales (K Units) and Growth Rate (2013-2018)

Figure China Wafer Level Package Dielectrics Revenue (Million USD) and Growth Rate (2013-2018)

Figure China Wafer Level Package Dielectrics Sales Price (USD/Unit) Trend (2013-2018)

Table China Wafer Level Package Dielectrics Sales Volume (K Units) by Players (2013-2018)

Table China Wafer Level Package Dielectrics Sales Volume Market Share by Players (2013-2018)

Figure China Wafer Level Package Dielectrics Sales Volume Market Share by Players in 2017

Table China Wafer Level Package Dielectrics Sales Volume (K Units) by Type (2013-2018)

Table China Wafer Level Package Dielectrics Sales Volume Market Share by Type (2013-2018)

Figure China Wafer Level Package Dielectrics Sales Volume Market Share by Type in 2017

Table China Wafer Level Package Dielectrics Sales Volume (K Units) by Application (2013-2018)

Table China Wafer Level Package Dielectrics Sales Volume Market Share by Application (2013-2018)

Figure China Wafer Level Package Dielectrics Sales Volume Market Share by Application in 2017

Figure Europe Wafer Level Package Dielectrics Sales (K Units) and Growth Rate (2013-2018)

Figure Europe Wafer Level Package Dielectrics Revenue (Million USD) and Growth Rate (2013-2018)

Figure Europe Wafer Level Package Dielectrics Sales Price (USD/Unit) Trend (2013-2018)

Table Europe Wafer Level Package Dielectrics Sales Volume (K Units) by Players (2013-2018)



Table Europe Wafer Level Package Dielectrics Sales Volume Market Share by Players (2013-2018)

Figure Europe Wafer Level Package Dielectrics Sales Volume Market Share by Players in 2017

Table Europe Wafer Level Package Dielectrics Sales Volume (K Units) by Type (2013-2018)

Table Europe Wafer Level Package Dielectrics Sales Volume Market Share by Type (2013-2018)

Figure Europe Wafer Level Package Dielectrics Sales Volume Market Share by Type in 2017

Table Europe Wafer Level Package Dielectrics Sales Volume (K Units) by Application (2013-2018)

Table Europe Wafer Level Package Dielectrics Sales Volume Market Share by Application (2013-2018)

Figure Europe Wafer Level Package Dielectrics Sales Volume Market Share by Application in 2017

Figure Japan Wafer Level Package Dielectrics Sales (K Units) and Growth Rate (2013-2018)

Figure Japan Wafer Level Package Dielectrics Revenue (Million USD) and Growth Rate (2013-2018)

Figure Japan Wafer Level Package Dielectrics Sales Price (USD/Unit) Trend (2013-2018)

Table Japan Wafer Level Package Dielectrics Sales Volume (K Units) by Players (2013-2018)

Table Japan Wafer Level Package Dielectrics Sales Volume Market Share by Players (2013-2018)

Figure Japan Wafer Level Package Dielectrics Sales Volume Market Share by Players in 2017

Table Japan Wafer Level Package Dielectrics Sales Volume (K Units) by Type (2013-2018)

Table Japan Wafer Level Package Dielectrics Sales Volume Market Share by Type (2013-2018)

Figure Japan Wafer Level Package Dielectrics Sales Volume Market Share by Type in 2017

Table Japan Wafer Level Package Dielectrics Sales Volume (K Units) by Application (2013-2018)

Table Japan Wafer Level Package Dielectrics Sales Volume Market Share by Application (2013-2018)

Figure Japan Wafer Level Package Dielectrics Sales Volume Market Share by

Application in 2017

Figure Southeast Asia Wafer Level Package Dielectrics Sales (K Units) and Growth Rate (2013-2018)

Figure Southeast Asia Wafer Level Package Dielectrics Revenue (Million USD) and Growth Rate (2013-2018)

Figure Southeast Asia Wafer Level Package Dielectrics Sales Price (USD/Unit) Trend (2013-2018)

Table Southeast Asia Wafer Level Package Dielectrics Sales Volume (K Units) by Players (2013-2018)

Table Southeast Asia Wafer Level Package Dielectrics Sales Volume Market Share by Players (2013-2018)

Figure Southeast Asia Wafer Level Package Dielectrics Sales Volume Market Share by Players in 2017

Table Southeast Asia Wafer Level Package Dielectrics Sales Volume (K Units) by Type (2013-2018)

Table Southeast Asia Wafer Level Package Dielectrics Sales Volume Market Share by Type (2013-2018)

Figure Southeast Asia Wafer Level Package Dielectrics Sales Volume Market Share by Type in 2017

Table Southeast Asia Wafer Level Package Dielectrics Sales Volume (K Units) by Application (2013-2018)

Table Southeast Asia Wafer Level Package Dielectrics Sales Volume Market Share by Application (2013-2018)

Figure Southeast Asia Wafer Level Package Dielectrics Sales Volume Market Share by Application in 2017

Figure India Wafer Level Package Dielectrics Sales (K Units) and Growth Rate (2013-2018)

Figure India Wafer Level Package Dielectrics Revenue (Million USD) and Growth Rate (2013-2018)

Figure India Wafer Level Package Dielectrics Sales Price (USD/Unit) Trend (2013-2018)

Table India Wafer Level Package Dielectrics Sales Volume (K Units) by Players (2013-2018)

Table India Wafer Level Package Dielectrics Sales Volume Market Share by Players (2013-2018)

Figure India Wafer Level Package Dielectrics Sales Volume Market Share by Players in 2017

Table India Wafer Level Package Dielectrics Sales Volume (K Units) by Type (2013-2018)

Table India Wafer Level Package Dielectrics Sales Volume Market Share by Type (2013-2018)

Figure India Wafer Level Package Dielectrics Sales Volume Market Share by Type in 2017

Table India Wafer Level Package Dielectrics Sales Volume (K Units) by Application (2013-2018)

Table India Wafer Level Package Dielectrics Sales Volume Market Share by Application (2013-2018)

Figure India Wafer Level Package Dielectrics Sales Volume Market Share by Application in 2017

Table ChipMOS TECHNOLOGIES INC Basic Information List

Table ChipMOS TECHNOLOGIES INC Wafer Level Package Dielectrics Sales (K Units), Revenue (Million USD), Price (USD/Unit) and Gross Margin (2013-2018)

Figure ChipMOS TECHNOLOGIES INC Wafer Level Package Dielectrics Sales Growth Rate (2013-2018)

Figure ChipMOS TECHNOLOGIES INC Wafer Level Package Dielectrics Sales Global Market Share (2013-2018)

Figure ChipMOS TECHNOLOGIES INC Wafer Level Package Dielectrics Revenue Global Market Share (2013-2018)

Table STATS ChipPAC Ltd Basic Information List

Table STATS ChipPAC Ltd Wafer Level Package Dielectrics Sales (K Units), Revenue (Million USD), Price (USD/Unit) and Gross Margin (2013-2018)

Figure STATS ChipPAC Ltd Wafer Level Package Dielectrics Sales Growth Rate (2013-2018)

Figure STATS ChipPAC Ltd Wafer Level Package Dielectrics Sales Global Market Share (2013-2018)

Figure STATS ChipPAC Ltd Wafer Level Package Dielectrics Revenue Global Market Share (2013-2018)

Table IQE PLC Basic Information List

Table IQE PLC Wafer Level Package Dielectrics Sales (K Units), Revenue (Million USD), Price (USD/Unit) and Gross Margin (2013-2018)

Figure IQE PLC Wafer Level Package Dielectrics Sales Growth Rate (2013-2018)

Figure IQE PLC Wafer Level Package Dielectrics Sales Global Market Share (2013-2018)

Figure IQE PLC Wafer Level Package Dielectrics Revenue Global Market Share (2013-2018)

Table Amkor Technology Inc Basic Information List

Table Amkor Technology Inc Wafer Level Package Dielectrics Sales (K Units), Revenue (Million USD), Price (USD/Unit) and Gross Margin (2013-2018)

Figure Amkor Technology Inc Wafer Level Package Dielectrics Sales Growth Rate (2013-2018)

Figure Amkor Technology Inc Wafer Level Package Dielectrics Sales Global Market Share (2013-2018)

Figure Amkor Technology Inc Wafer Level Package Dielectrics Revenue Global Market Share (2013-2018)

Table TriQuint Semiconductor Inc Basic Information List

Table TriQuint Semiconductor Inc Wafer Level Package Dielectrics Sales (K Units), Revenue (Million USD), Price (USD/Unit) and Gross Margin (2013-2018)

Figure TriQuint Semiconductor Inc Wafer Level Package Dielectrics Sales Growth Rate (2013-2018)

Figure TriQuint Semiconductor Inc Wafer Level Package Dielectrics Sales Global Market Share (2013-2018)

Figure TriQuint Semiconductor Inc Wafer Level Package Dielectrics Revenue Global Market Share (2013-2018)

Table Deca Technologies Basic Information List

Table Deca Technologies Wafer Level Package Dielectrics Sales (K Units), Revenue (Million USD), Price (USD/Unit) and Gross Margin (2013-2018)

Figure Deca Technologies Wafer Level Package Dielectrics Sales Growth Rate (2013-2018)

Figure Deca Technologies Wafer Level Package Dielectrics Sales Global Market Share (2013-2018)

Figure Deca Technologies Wafer Level Package Dielectrics Revenue Global Market Share (2013-2018)

Table KLA-Tencor Corporation Basic Information List

Table KLA-Tencor Corporation Wafer Level Package Dielectrics Sales (K Units), Revenue (Million USD), Price (USD/Unit) and Gross Margin (2013-2018)

Figure KLA-Tencor Corporation Wafer Level Package Dielectrics Sales Growth Rate (2013-2018)

Figure KLA-Tencor Corporation Wafer Level Package Dielectrics Sales Global Market Share (2013-2018)

Figure KLA-Tencor Corporation Wafer Level Package Dielectrics Revenue Global Market Share (2013-2018)

Table Siliconware Precision Industries Co. Ltd Basic Information List

Table Siliconware Precision Industries Co. Ltd Wafer Level Package Dielectrics Sales (K Units), Revenue (Million USD), Price (USD/Unit) and Gross Margin (2013-2018)

Figure Siliconware Precision Industries Co. Ltd Wafer Level Package Dielectrics Sales Growth Rate (2013-2018)

Figure Siliconware Precision Industries Co. Ltd Wafer Level Package Dielectrics Sales

Global Market Share (2013-2018)

Figure Siliconware Precision Industries Co. Ltd Wafer Level Package Dielectrics

Revenue Global Market Share (2013-2018)

Table China Wafer Level CSP Co. Ltd Basic Information List

Table China Wafer Level CSP Co. Ltd Wafer Level Package Dielectrics Sales (K Units), Revenue (Million USD), Price (USD/Unit) and Gross Margin (2013-2018)

Figure China Wafer Level CSP Co. Ltd Wafer Level Package Dielectrics Sales Growth Rate (2013-2018)

Figure China Wafer Level CSP Co. Ltd Wafer Level Package Dielectrics Sales Global Market Share (2013-2018)

Figure China Wafer Level CSP Co. Ltd Wafer Level Package Dielectrics Revenue Global Market Share (2013-2018)

Table Jiangsu Changjiang Electronics Technology Co. Ltd Basic Information List

Table Jiangsu Changjiang Electronics Technology Co. Ltd Wafer Level Package Dielectrics Sales (K Units), Revenue (Million USD), Price (USD/Unit) and Gross Margin (2013-2018)

Figure Jiangsu Changjiang Electronics Technology Co. Ltd Wafer Level Package Dielectrics Sales Growth Rate (2013-2018)

Figure Jiangsu Changjiang Electronics Technology Co. Ltd Wafer Level Package Dielectrics Sales Global Market Share (2013-2018)

Figure Jiangsu Changjiang Electronics Technology Co. Ltd Wafer Level Package Dielectrics Revenue Global Market Share (2013-2018)

Table Production Base and Market Concentration Rate of Raw Material

Figure Price Trend of Key Raw Materials

Table Key Suppliers of Raw Materials

Figure Manufacturing Cost Structure of Wafer Level Package Dielectrics

Figure Manufacturing Process Analysis of Wafer Level Package Dielectrics

Figure Wafer Level Package Dielectrics Industrial Chain Analysis

Table Raw Materials Sources of Wafer Level Package Dielectrics Major Players in 2017

Table Major Buyers of Wafer Level Package Dielectrics

Table Distributors/Traders List

Figure Global Wafer Level Package Dielectrics Sales Volume (K Units) and Growth Rate Forecast (2018-2025)

Figure Global Wafer Level Package Dielectrics Revenue (Million USD) and Growth Rate Forecast (2018-2025)

Figure Global Wafer Level Package Dielectrics Price (USD/Unit) and Trend Forecast (2018-2025)

Table Global Wafer Level Package Dielectrics Sales Volume (K Units) Forecast by Regions (2018-2025)



Figure Global Wafer Level Package Dielectrics Sales Volume Market Share Forecast by Regions (2018-2025)

Figure Global Wafer Level Package Dielectrics Sales Volume Market Share Forecast by Regions in 2025

Table Global Wafer Level Package Dielectrics Revenue (Million USD) Forecast by Regions (2018-2025)

Figure Global Wafer Level Package Dielectrics Revenue Market Share Forecast by Regions (2018-2025)

Figure Global Wafer Level Package Dielectrics Revenue Market Share Forecast by Regions in 2025

Figure United States Wafer Level Package Dielectrics Sales Volume (K Units) and Growth Rate Forecast (2018-2025)

Figure United States Wafer Level Package Dielectrics Revenue (Million USD) and Growth Rate Forecast (2018-2025)

Figure China Wafer Level Package Dielectrics Sales Volume (K Units) and Growth Rate Forecast (2018-2025)

Figure China Wafer Level Package Dielectrics Revenue and Growth Rate Forecast (2018-2025)

Figure Europe Wafer Level Package Dielectrics Sales Volume (K Units) and Growth Rate Forecast (2018-2025)

Figure Europe Wafer Level Package Dielectrics Revenue (Million USD) and Growth Rate Forecast (2018-2025)

Figure Japan Wafer Level Package Dielectrics Sales Volume (K Units) and Growth Rate Forecast (2018-2025)

Figure Japan Wafer Level Package Dielectrics Revenue (Million USD) and Growth Rate Forecast (2018-2025)

Figure Southeast Asia Wafer Level Package Dielectrics Sales Volume (K Units) and Growth Rate Forecast (2018-2025)

Figure Southeast Asia Wafer Level Package Dielectrics Revenue (Million USD) and Growth Rate Forecast (2018-2025)

Figure India Wafer Level Package Dielectrics Sales Volume (K Units) and Growth Rate Forecast (2018-2025)

Figure India Wafer Level Package Dielectrics Revenue (Million USD) and Growth Rate Forecast (2018-2025)

Table Global Wafer Level Package Dielectrics Sales (K Units) Forecast by Type (2018-2025)

Figure Global Wafer Level Package Dielectrics Sales Volume Market Share Forecast by Type (2018-2025)

Table Global Wafer Level Package Dielectrics Revenue (Million USD) Forecast by Type

(2018-2025)

Figure Global Wafer Level Package Dielectrics Revenue Market Share Forecast by Type (2018-2025)

Table Global Wafer Level Package Dielectrics Price (USD/Unit) Forecast by Type (2018-2025)

Table Global Wafer Level Package Dielectrics Sales (K Units) Forecast by Application (2018-2025)

Figure Global Wafer Level Package Dielectrics Sales Market Share Forecast by Application (2018-2025)

Table Research Programs/Design for This Report

Figure Bottom-up and Top-down Approaches for This Report

Figure Data Triangulation

Table Key Data Information from Secondary Sources

Table Key Data Information from Primary Sources

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