

Global Polished Silicon Wafer Market Research Report 2023

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Abstracts

Polished silicon wafer is a wafer of semiconducting material made by polished growth for use in photonics, microelectronics, spintronics, or photovoltaics.

According to QYResearch's new survey, global Polished Silicon Wafer market is projected to reach US\$ million in 2029, increasing from US\$ million in 2022, with the CAGR of % during the period of 2023 to 2029. Influencing issues, such as economy environments, COVID-19 and Russia-Ukraine War, have led to great market fluctuations in the past few years and are considered comprehensively in the whole Polished Silicon Wafer market research.

Following a strong growth of 26.2 percent in the year 2021, WSTS revised it down to a single digit growth for the worldwide semiconductor market in 2022 with a total size of US\$580 billion, up 4.4 percent. WSTS lowered growth estimation as inflation rises and end markets seeing weaker demand, especially those exposed to consumer spending. While some major categories are still double-digit year-over-year growth in 2022, led by Analog with 20.8 percent, Sensors with 16.3 percent, and Logic with 14.5 percent growth. Memory declined with 12.6 percent year over year. In 2022, all geographical regions showed double-digit growth except Asia Pacific. The largest region, Asia Pacific, declined 2.0 percent. Sales in the Americas were US\$142.1 billion, up 17.0% year-on-year, sales in Europe were US\$53.8 billion, up 12.6% year-on-year, and sales in Japan were US\$48.1 billion, up 10.0% year-on-year. However, sales in the largest Asia-Pacific region were US\$336.2 billion, down 2.0% year-on-year.

Report Scope

This report, based on historical analysis (2018-2022) and forecast calculation

(2023-2029), aims to help readers to get a comprehensive understanding of global Polished Silicon Wafer market with multiple angles, which provides sufficient supports to readers' strategy and decision making.

By Company

Shin-Etsu Chemical

SUMCO CORPORATION

GlobalWafers

SK Siltron Co., Ltd.

Siltronic

Okmetic

Ferrotec

JRH

Wafer Works

Simgui

Poshing

GRITEK

Zhonghuan Huanou

MCL

Segment by Type

300 mm

200 mm

150 mm

125 mm

Others

Segment by Application

Memory

Logic/MPU

Others

Production by Region

North America

Europe

China

Japan

South Korea

Consumption by Region

North America

U.S.

Canada

Europe

Germany

France

U.K.

Italy

Russia

Asia-Pacific

China

Japan

South Korea

China Taiwan

Southeast Asia

India

Latin America, Middle East & Africa

Mexico

Brazil

Turkey

GCC Countries

The Polished Silicon Wafer report covers below items:

Chapter 1: Product Basic Information (Definition, type and application)

Chapter 2: Manufacturers' Competition Patterns

Chapter 3: Production Region Distribution and Analysis

Chapter 4: Country Level Sales Analysis

Chapter 5: Product Type Analysis

Chapter 6: Product Application Analysis

Chapter 7: Manufacturers' Outline

Chapter 8: Industry Chain, Market Channel and Customer Analysis

Chapter 9: Market Opportunities and Challenges

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