

Global E-Chuck for Wafer Market Research Report 2023

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Abstracts

E-Chuck for Wafer is a tool that clamps an object with the force generated between the electrode and the object by applying a voltage to the electrode. There are two different types of electrostatic clamping methods. One is Coulomb force type that utilizes an insulator as a dielectric material, and the other is Johnson-Rahbek force type that utilizes an attractive force induced by dielectric polarization caused by minute electric current flow across the boundary between an object and a dielectric material. ESCs which are widely used for wafer processing including etching, CVD, PVD, Ashing etc.

According to QYResearch's new survey, global E-Chuck for Wafer market is projected to reach US\$ 2411.5 million in 2029, increasing from US\$ 1793.6 million in 2022, with the CAGR of 4.7% during the period of 2023 to 2029. Influencing issues, such as economy environments, COVID-19 and Russia-Ukraine War, have led to great market fluctuations in the past few years and are considered comprehensively in the whole E-Chuck for Wafer market research.

Following a strong growth of 26.2 percent in the year 2021, WSTS revised it down to a single digit growth for the worldwide semiconductor market in 2022 with a total size of US\$580 billion, up 4.4 percent. WSTS lowered growth estimation as inflation rises and end markets seeing weaker demand, especially those exposed to consumer spending. While some major categories are still double-digit year-over-year growth in 2022, led by Analog with 20.8 percent, Sensors with 16.3 percent, and Logic with 14.5 percent growth. Memory declined with 12.6 percent year over year. In 2022, all geographical regions showed double-digit growth except Asia Pacific. The largest region, Asia Pacific, declined 2.0 percent. Sales in the Americas were US\$142.1 billion, up 17.0% year-on-year, sales in Europe were US\$53.8 billion, up 12.6% year-on-year, and sales in Japan were US\$48.1 billion, up 10.0% year-on-year. However, sales in the largest

Asia-Pacific region were US\$336.2 billion, down 2.0% year-on-year.

Report Scope

This report, based on historical analysis (2018-2022) and forecast calculation (2023-2029), aims to help readers to get a comprehensive understanding of global E-Chuck for Wafer market with multiple angles, which provides sufficient supports to readers' strategy and decision making.

By Company

Applied Materials

Lam Research

SHINKO

TOTO

Sumitomo Osaka Cement

Creative Technology Corporation

Kyocera

Entegris

NTK CERATEC

NGK Insulators, Ltd.

II-VI M Cubed

Tsukuba Seiko

Calitech

Beijing U-PRECISION TECH CO., LTD.

Segment by Type

Coulomb Type

Johnsen-Rahbek (JR) Type

Segment by Application

300 mm Wafer

200 mm Wafer

Others

Production by Region

North America

Europe

China

Japan

South Korea

Consumption by Region

North America

U.S.

Canada

Europe

Germany

France

U.K.

Italy

Russia

Asia-Pacific

China

Japan

South Korea

China Taiwan

Southeast Asia

India

Latin America, Middle East & Africa

Mexico

Brazil

Turkey

GCC Countries

The E-Chuck for Wafer report covers below items:

Chapter 1: Product Basic Information (Definition, type and application)

Chapter 2: Manufacturers' Competition Patterns

Chapter 3: Production Region Distribution and Analysis

Chapter 4: Country Level Sales Analysis

Chapter 5: Product Type Analysis

Chapter 6: Product Application Analysis

Chapter 7: Manufacturers' Outline

Chapter 8: Industry Chain, Market Channel and Customer Analysis

Chapter 9: Market Opportunities and Challenges

Chapter 10: Market Conclusions

Chapter 11: Research Methodology and Data Source

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