

# Global Chip Handler in Semiconductor Market Research Report 2017

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## Abstracts

In this report, the global Chip Handler in Semiconductor market is valued at USD XX million in 2016 and is expected to reach USD XX million by the end of 2022, growing at a CAGR of XX% between 2016 and 2022.

Geographically, this report is segmented into several key Regions, with production, consumption, revenue (million USD), market share and growth rate of Chip Handler in Semiconductor in these regions, from 2012 to 2022 (forecast), covering

North America

Europe

China

Japan

Southeast Asia

India

Global Chip Handler in Semiconductor market competition by top manufacturers, with production, price, revenue (value) and market share for each manufacturer; the top players including

Multitest

Seiko Epson Corporation

Multitest

ASM Pacific Technology

Cohu

Advantest

Boston Semi Equipment

Hon Technologies

SRM Integration

SYNAX

CST

Exatron

Xeltek

On the basis of product, this report displays the production, revenue, price, market share and growth rate of each type, primarily split into

High Parallel Memory Test

High Parallel Strip Test

On the basis of the end users/applications, this report focuses on the status and outlook for major applications/end users, consumption (sales), market share and growth rate for each application, including

Outsourced Semiconductor Assembly and Test Providers (OSATs)

## Integrated Device Manufacturers (IDMs)

If you have any special requirements, please let us know and we will offer you the report as you want.

## Contents

### Global Chip Handler in Semiconductor Market Research Report 2017

## 1 CHIP HANDLER IN SEMICONDUCTOR MARKET OVERVIEW

- 1.1 Product Overview and Scope of Chip Handler in Semiconductor
- 1.2 Chip Handler in Semiconductor Segment by Type (Product Category)
  - 1.2.1 Global Chip Handler in Semiconductor Production and CAGR (%) Comparison by Type (Product Category)(2012-2022)
  - 1.2.2 Global Chip Handler in Semiconductor Production Market Share by Type (Product Category) in 2016
  - 1.2.3 High Parallel Memory Test
  - 1.2.4 High Parallel Strip Test
- 1.3 Global Chip Handler in Semiconductor Segment by Application
  - 1.3.1 Chip Handler in Semiconductor Consumption (Sales) Comparison by Application (2012-2022)
  - 1.3.2 Outsourced Semiconductor Assembly and Test Providers (OSATs)
  - 1.3.3 Integrated Device Manufacturers (IDMs)
- 1.4 Global Chip Handler in Semiconductor Market by Region (2012-2022)
  - 1.4.1 Global Chip Handler in Semiconductor Market Size (Value) and CAGR (%) Comparison by Region (2012-2022)
  - 1.4.2 North America Status and Prospect (2012-2022)
  - 1.4.3 Europe Status and Prospect (2012-2022)
  - 1.4.4 China Status and Prospect (2012-2022)
  - 1.4.5 Japan Status and Prospect (2012-2022)
  - 1.4.6 Southeast Asia Status and Prospect (2012-2022)
  - 1.4.7 India Status and Prospect (2012-2022)
- 1.5 Global Market Size (Value) of Chip Handler in Semiconductor (2012-2022)
  - 1.5.1 Global Chip Handler in Semiconductor Revenue Status and Outlook (2012-2022)
  - 1.5.2 Global Chip Handler in Semiconductor Capacity, Production Status and Outlook (2012-2022)

## 2 GLOBAL CHIP HANDLER IN SEMICONDUCTOR MARKET COMPETITION BY MANUFACTURERS

- 2.1 Global Chip Handler in Semiconductor Capacity, Production and Share by Manufacturers (2012-2017)
  - 2.1.1 Global Chip Handler in Semiconductor Capacity and Share by Manufacturers

(2012-2017)

2.1.2 Global Chip Handler in Semiconductor Production and Share by Manufacturers

(2012-2017)

2.2 Global Chip Handler in Semiconductor Revenue and Share by Manufacturers

(2012-2017)

2.3 Global Chip Handler in Semiconductor Average Price by Manufacturers (2012-2017)

2.4 Manufacturers Chip Handler in Semiconductor Manufacturing Base Distribution, Sales Area and Product Type

2.5 Chip Handler in Semiconductor Market Competitive Situation and Trends

2.5.1 Chip Handler in Semiconductor Market Concentration Rate

2.5.2 Chip Handler in Semiconductor Market Share of Top 3 and Top 5 Manufacturers

2.5.3 Mergers & Acquisitions, Expansion

### **3 GLOBAL CHIP HANDLER IN SEMICONDUCTOR CAPACITY, PRODUCTION, REVENUE (VALUE) BY REGION (2012-2017)**

3.1 Global Chip Handler in Semiconductor Capacity and Market Share by Region (2012-2017)

3.2 Global Chip Handler in Semiconductor Production and Market Share by Region (2012-2017)

3.3 Global Chip Handler in Semiconductor Revenue (Value) and Market Share by Region (2012-2017)

3.4 Global Chip Handler in Semiconductor Capacity, Production, Revenue, Price and Gross Margin (2012-2017)

3.5 North America Chip Handler in Semiconductor Capacity, Production, Revenue, Price and Gross Margin (2012-2017)

3.6 Europe Chip Handler in Semiconductor Capacity, Production, Revenue, Price and Gross Margin (2012-2017)

3.7 China Chip Handler in Semiconductor Capacity, Production, Revenue, Price and Gross Margin (2012-2017)

3.8 Japan Chip Handler in Semiconductor Capacity, Production, Revenue, Price and Gross Margin (2012-2017)

3.9 Southeast Asia Chip Handler in Semiconductor Capacity, Production, Revenue, Price and Gross Margin (2012-2017)

3.10 India Chip Handler in Semiconductor Capacity, Production, Revenue, Price and Gross Margin (2012-2017)

### **4 GLOBAL CHIP HANDLER IN SEMICONDUCTOR SUPPLY (PRODUCTION), CONSUMPTION, EXPORT, IMPORT BY REGION (2012-2017)**

- 4.1 Global Chip Handler in Semiconductor Consumption by Region (2012-2017)
- 4.2 North America Chip Handler in Semiconductor Production, Consumption, Export, Import (2012-2017)
- 4.3 Europe Chip Handler in Semiconductor Production, Consumption, Export, Import (2012-2017)
- 4.4 China Chip Handler in Semiconductor Production, Consumption, Export, Import (2012-2017)
- 4.5 Japan Chip Handler in Semiconductor Production, Consumption, Export, Import (2012-2017)
- 4.6 Southeast Asia Chip Handler in Semiconductor Production, Consumption, Export, Import (2012-2017)
- 4.7 India Chip Handler in Semiconductor Production, Consumption, Export, Import (2012-2017)

## **5 GLOBAL CHIP HANDLER IN SEMICONDUCTOR PRODUCTION, REVENUE (VALUE), PRICE TREND BY TYPE**

- 5.1 Global Chip Handler in Semiconductor Production and Market Share by Type (2012-2017)
- 5.2 Global Chip Handler in Semiconductor Revenue and Market Share by Type (2012-2017)
- 5.3 Global Chip Handler in Semiconductor Price by Type (2012-2017)
- 5.4 Global Chip Handler in Semiconductor Production Growth by Type (2012-2017)

## **6 GLOBAL CHIP HANDLER IN SEMICONDUCTOR MARKET ANALYSIS BY APPLICATION**

- 6.1 Global Chip Handler in Semiconductor Consumption and Market Share by Application (2012-2017)
- 6.2 Global Chip Handler in Semiconductor Consumption Growth Rate by Application (2012-2017)
- 6.3 Market Drivers and Opportunities
  - 6.3.1 Potential Applications
  - 6.3.2 Emerging Markets/Countries

## **7 GLOBAL CHIP HANDLER IN SEMICONDUCTOR MANUFACTURERS PROFILES/ANALYSIS**

## 7.1 Multitest

7.1.1 Company Basic Information, Manufacturing Base, Sales Area and Its Competitors

7.1.2 Chip Handler in Semiconductor Product Category, Application and Specification

7.1.2.1 Product A

7.1.2.2 Product B

7.1.3 Multitest Chip Handler in Semiconductor Capacity, Production, Revenue, Price and Gross Margin (2012-2017)

7.1.4 Main Business/Business Overview

## 7.2 Seiko Epson Corporation

7.2.1 Company Basic Information, Manufacturing Base, Sales Area and Its Competitors

7.2.2 Chip Handler in Semiconductor Product Category, Application and Specification

7.2.2.1 Product A

7.2.2.2 Product B

7.2.3 Seiko Epson Corporation Chip Handler in Semiconductor Capacity, Production, Revenue, Price and Gross Margin (2012-2017)

7.2.4 Main Business/Business Overview

## 7.3 Multitest

7.3.1 Company Basic Information, Manufacturing Base, Sales Area and Its Competitors

7.3.2 Chip Handler in Semiconductor Product Category, Application and Specification

7.3.2.1 Product A

7.3.2.2 Product B

7.3.3 Multitest Chip Handler in Semiconductor Capacity, Production, Revenue, Price and Gross Margin (2012-2017)

7.3.4 Main Business/Business Overview

## 7.4 ASM Pacific Technology

7.4.1 Company Basic Information, Manufacturing Base, Sales Area and Its Competitors

7.4.2 Chip Handler in Semiconductor Product Category, Application and Specification

7.4.2.1 Product A

7.4.2.2 Product B

7.4.3 ASM Pacific Technology Chip Handler in Semiconductor Capacity, Production, Revenue, Price and Gross Margin (2012-2017)

7.4.4 Main Business/Business Overview

## 7.5 CoHu

7.5.1 Company Basic Information, Manufacturing Base, Sales Area and Its Competitors

- 7.5.2 Chip Handler in Semiconductor Product Category, Application and Specification
  - 7.5.2.1 Product A
  - 7.5.2.2 Product B
- 7.5.3 Cohu Chip Handler in Semiconductor Capacity, Production, Revenue, Price and Gross Margin (2012-2017)
- 7.5.4 Main Business/Business Overview
- 7.6 Advantest
  - 7.6.1 Company Basic Information, Manufacturing Base, Sales Area and Its Competitors
  - 7.6.2 Chip Handler in Semiconductor Product Category, Application and Specification
    - 7.6.2.1 Product A
    - 7.6.2.2 Product B
  - 7.6.3 Advantest Chip Handler in Semiconductor Capacity, Production, Revenue, Price and Gross Margin (2012-2017)
  - 7.6.4 Main Business/Business Overview
- 7.7 Boston Semi Equipment
  - 7.7.1 Company Basic Information, Manufacturing Base, Sales Area and Its Competitors
  - 7.7.2 Chip Handler in Semiconductor Product Category, Application and Specification
    - 7.7.2.1 Product A
    - 7.7.2.2 Product B
  - 7.7.3 Boston Semi Equipment Chip Handler in Semiconductor Capacity, Production, Revenue, Price and Gross Margin (2012-2017)
  - 7.7.4 Main Business/Business Overview
- 7.8 Hon Technologies
  - 7.8.1 Company Basic Information, Manufacturing Base, Sales Area and Its Competitors
  - 7.8.2 Chip Handler in Semiconductor Product Category, Application and Specification
    - 7.8.2.1 Product A
    - 7.8.2.2 Product B
  - 7.8.3 Hon Technologies Chip Handler in Semiconductor Capacity, Production, Revenue, Price and Gross Margin (2012-2017)
  - 7.8.4 Main Business/Business Overview
- 7.9 SRM Integration
  - 7.9.1 Company Basic Information, Manufacturing Base, Sales Area and Its Competitors
  - 7.9.2 Chip Handler in Semiconductor Product Category, Application and Specification
    - 7.9.2.1 Product A
    - 7.9.2.2 Product B



7.9.3 SRM Integration Chip Handler in Semiconductor Capacity, Production, Revenue, Price and Gross Margin (2012-2017)

7.9.4 Main Business/Business Overview

7.10 SYNAX

7.10.1 Company Basic Information, Manufacturing Base, Sales Area and Its Competitors

7.10.2 Chip Handler in Semiconductor Product Category, Application and Specification

7.10.2.1 Product A

7.10.2.2 Product B

7.10.3 SYNAX Chip Handler in Semiconductor Capacity, Production, Revenue, Price and Gross Margin (2012-2017)

7.10.4 Main Business/Business Overview

7.11 CST

7.12 Exatron

7.13 Xeltek

## **8 CHIP HANDLER IN SEMICONDUCTOR MANUFACTURING COST ANALYSIS**

8.1 Chip Handler in Semiconductor Key Raw Materials Analysis

8.1.1 Key Raw Materials

8.1.2 Price Trend of Key Raw Materials

8.1.3 Key Suppliers of Raw Materials

8.1.4 Market Concentration Rate of Raw Materials

8.2 Proportion of Manufacturing Cost Structure

8.2.1 Raw Materials

8.2.2 Labor Cost

8.2.3 Manufacturing Expenses

8.3 Manufacturing Process Analysis of Chip Handler in Semiconductor

## **9 INDUSTRIAL CHAIN, SOURCING STRATEGY AND DOWNSTREAM BUYERS**

9.1 Chip Handler in Semiconductor Industrial Chain Analysis

9.2 Upstream Raw Materials Sourcing

9.3 Raw Materials Sources of Chip Handler in Semiconductor Major Manufacturers in 2015

9.4 Downstream Buyers

## **10 MARKETING STRATEGY ANALYSIS, DISTRIBUTORS/TRADERS**

- 10.1 Marketing Channel
  - 10.1.1 Direct Marketing
  - 10.1.2 Indirect Marketing
  - 10.1.3 Marketing Channel Development Trend
- 10.2 Market Positioning
  - 10.2.1 Pricing Strategy
  - 10.2.2 Brand Strategy
  - 10.2.3 Target Client
- 10.3 Distributors/Traders List

## **11 MARKET EFFECT FACTORS ANALYSIS**

- 11.1 Technology Progress/Risk
  - 11.1.1 Substitutes Threat
  - 11.1.2 Technology Progress in Related Industry
- 11.2 Consumer Needs/Customer Preference Change
- 11.3 Economic/Political Environmental Change

## **12 GLOBAL CHIP HANDLER IN SEMICONDUCTOR MARKET FORECAST (2017-2022)**

- 12.1 Global Chip Handler in Semiconductor Capacity, Production, Revenue Forecast (2017-2022)
  - 12.1.1 Global Chip Handler in Semiconductor Capacity, Production and Growth Rate Forecast (2017-2022)
  - 12.1.2 Global Chip Handler in Semiconductor Revenue and Growth Rate Forecast (2017-2022)
  - 12.1.3 Global Chip Handler in Semiconductor Price and Trend Forecast (2017-2022)
- 12.2 Global Chip Handler in Semiconductor Production, Consumption, Import and Export Forecast by Region (2017-2022)
  - 12.2.1 North America Chip Handler in Semiconductor Production, Revenue, Consumption, Export and Import Forecast (2017-2022)
  - 12.2.2 Europe Chip Handler in Semiconductor Production, Revenue, Consumption, Export and Import Forecast (2017-2022)
  - 12.2.3 China Chip Handler in Semiconductor Production, Revenue, Consumption, Export and Import Forecast (2017-2022)
  - 12.2.4 Japan Chip Handler in Semiconductor Production, Revenue, Consumption, Export and Import Forecast (2017-2022)
  - 12.2.5 Southeast Asia Chip Handler in Semiconductor Production, Revenue,

Consumption, Export and Import Forecast (2017-2022)

12.2.6 India Chip Handler in Semiconductor Production, Revenue, Consumption, Export and Import Forecast (2017-2022)

12.3 Global Chip Handler in Semiconductor Production, Revenue and Price Forecast by Type (2017-2022)

12.4 Global Chip Handler in Semiconductor Consumption Forecast by Application (2017-2022)

## **13 RESEARCH FINDINGS AND CONCLUSION**

## **14 APPENDIX**

14.1 Methodology/Research Approach

14.1.1 Research Programs/Design

14.1.2 Market Size Estimation

14.1.3 Market Breakdown and Data Triangulation

14.2 Data Source

14.2.1 Secondary Sources

14.2.2 Primary Sources

14.3 Disclaimer

The report requires updating with new data and is sent in 2-3 business days after order is placed.

## List Of Tables

### LIST OF TABLES AND FIGURES

Figure Picture of Chip Handler in Semiconductor

Figure Global Chip Handler in Semiconductor Production (K Units) and CAGR (%) Comparison by Types (Product Category) (2012-2022)

Figure Global Chip Handler in Semiconductor Production Market Share by Types (Product Category) in 2016

Figure Product Picture of High Parallel Memory Test

Table Major Manufacturers of High Parallel Memory Test

Figure Product Picture of High Parallel Strip Test

Table Major Manufacturers of High Parallel Strip Test

Figure Global Chip Handler in Semiconductor Consumption (K Units) by Applications (2012-2022)

Figure Global Chip Handler in Semiconductor Consumption Market Share by Applications in 2016

Figure Outsourced Semiconductor Assembly and Test Providers (OSATs) Examples

Table Key Downstream Customer in Outsourced Semiconductor Assembly and Test Providers (OSATs)

Figure Integrated Device Manufacturers (IDMs) Examples

Table Key Downstream Customer in Integrated Device Manufacturers (IDMs)

Figure Global Chip Handler in Semiconductor Market Size (Million USD), Comparison (K Units) and CAGR (%) by Regions (2012-2022)

Figure North America Chip Handler in Semiconductor Revenue (Million USD) and Growth Rate (2012-2022)

Figure Europe Chip Handler in Semiconductor Revenue (Million USD) and Growth Rate (2012-2022)

Figure China Chip Handler in Semiconductor Revenue (Million USD) and Growth Rate (2012-2022)

Figure Japan Chip Handler in Semiconductor Revenue (Million USD) and Growth Rate (2012-2022)

Figure Southeast Asia Chip Handler in Semiconductor Revenue (Million USD) and Growth Rate (2012-2022)

Figure India Chip Handler in Semiconductor Revenue (Million USD) and Growth Rate (2012-2022)

Figure Global Chip Handler in Semiconductor Revenue (Million USD) Status and Outlook (2012-2022)

Figure Global Chip Handler in Semiconductor Capacity, Production (K Units) Status and

Outlook (2012-2022)

Figure Global Chip Handler in Semiconductor Major Players Product Capacity (K Units) (2012-2017)

Table Global Chip Handler in Semiconductor Capacity (K Units) of Key Manufacturers (2012-2017)

Table Global Chip Handler in Semiconductor Capacity Market Share of Key Manufacturers (2012-2017)

Figure Global Chip Handler in Semiconductor Capacity (K Units) of Key Manufacturers in 2016

Figure Global Chip Handler in Semiconductor Capacity (K Units) of Key Manufacturers in 2017

Figure Global Chip Handler in Semiconductor Major Players Product Production (K Units) (2012-2017)

Table Global Chip Handler in Semiconductor Production (K Units) of Key Manufacturers (2012-2017)

Table Global Chip Handler in Semiconductor Production Share by Manufacturers (2012-2017)

Figure 2016 Chip Handler in Semiconductor Production Share by Manufacturers

Figure 2017 Chip Handler in Semiconductor Production Share by Manufacturers

Figure Global Chip Handler in Semiconductor Major Players Product Revenue (Million USD) (2012-2017)

Table Global Chip Handler in Semiconductor Revenue (Million USD) by Manufacturers (2012-2017)

Table Global Chip Handler in Semiconductor Revenue Share by Manufacturers (2012-2017)

Table 2016 Global Chip Handler in Semiconductor Revenue Share by Manufacturers

Table 2017 Global Chip Handler in Semiconductor Revenue Share by Manufacturers

Table Global Market Chip Handler in Semiconductor Average Price (USD/Unit) of Key Manufacturers (2012-2017)

Figure Global Market Chip Handler in Semiconductor Average Price (USD/Unit) of Key Manufacturers in 2016

Table Manufacturers Chip Handler in Semiconductor Manufacturing Base Distribution and Sales Area

Table Manufacturers Chip Handler in Semiconductor Product Category

Figure Chip Handler in Semiconductor Market Share of Top 3 Manufacturers

Figure Chip Handler in Semiconductor Market Share of Top 5 Manufacturers

Table Global Chip Handler in Semiconductor Capacity (K Units) by Region (2012-2017)

Figure Global Chip Handler in Semiconductor Capacity Market Share by Region (2012-2017)

Figure Global Chip Handler in Semiconductor Capacity Market Share by Region (2012-2017)

Figure 2016 Global Chip Handler in Semiconductor Capacity Market Share by Region

Table Global Chip Handler in Semiconductor Production by Region (2012-2017)

Figure Global Chip Handler in Semiconductor Production (K Units) by Region (2012-2017)

Figure Global Chip Handler in Semiconductor Production Market Share by Region (2012-2017)

Figure 2016 Global Chip Handler in Semiconductor Production Market Share by Region

Table Global Chip Handler in Semiconductor Revenue (Million USD) by Region (2012-2017)

Table Global Chip Handler in Semiconductor Revenue Market Share by Region (2012-2017)

Figure Global Chip Handler in Semiconductor Revenue Market Share by Region (2012-2017)

Table 2016 Global Chip Handler in Semiconductor Revenue Market Share by Region

Figure Global Chip Handler in Semiconductor Capacity, Production (K Units) and Growth Rate (2012-2017)

Table Global Chip Handler in Semiconductor Capacity, Production (K Units), Revenue (Million USD), Price (USD/Unit) and Gross Margin (2012-2017)

Table North America Chip Handler in Semiconductor Capacity, Production (K Units), Revenue (Million USD), Price (USD/Unit) and Gross Margin (2012-2017)

Table Europe Chip Handler in Semiconductor Capacity, Production (K Units), Revenue (Million USD), Price (USD/Unit) and Gross Margin (2012-2017)

Table China Chip Handler in Semiconductor Capacity, Production (K Units), Revenue (Million USD), Price (USD/Unit) and Gross Margin (2012-2017)

Table Japan Chip Handler in Semiconductor Capacity, Production (K Units), Revenue (Million USD), Price (USD/Unit) and Gross Margin (2012-2017)

Table Southeast Asia Chip Handler in Semiconductor Capacity, Production (K Units), Revenue (Million USD), Price (USD/Unit) and Gross Margin (2012-2017)

Table India Chip Handler in Semiconductor Capacity, Production (K Units), Revenue (Million USD), Price (USD/Unit) and Gross Margin (2012-2017)

Table Global Chip Handler in Semiconductor Consumption (K Units) Market by Region (2012-2017)

Table Global Chip Handler in Semiconductor Consumption Market Share by Region (2012-2017)

Figure Global Chip Handler in Semiconductor Consumption Market Share by Region (2012-2017)

Figure 2016 Global Chip Handler in Semiconductor Consumption (K Units) Market



## Share by Region

Table North America Chip Handler in Semiconductor Production, Consumption, Import & Export (K Units) (2012-2017)

Table Europe Chip Handler in Semiconductor Production, Consumption, Import & Export (K Units) (2012-2017)

Table China Chip Handler in Semiconductor Production, Consumption, Import & Export (K Units) (2012-2017)

Table Japan Chip Handler in Semiconductor Production, Consumption, Import & Export (K Units) (2012-2017)

Table Southeast Asia Chip Handler in Semiconductor Production, Consumption, Import & Export (K Units) (2012-2017)

Table India Chip Handler in Semiconductor Production, Consumption, Import & Export (K Units) (2012-2017)

Table Global Chip Handler in Semiconductor Production (K Units) by Type (2012-2017)

Table Global Chip Handler in Semiconductor Production Share by Type (2012-2017)

Figure Production Market Share of Chip Handler in Semiconductor by Type (2012-2017)

Figure 2016 Production Market Share of Chip Handler in Semiconductor by Type

Table Global Chip Handler in Semiconductor Revenue (Million USD) by Type (2012-2017)

Table Global Chip Handler in Semiconductor Revenue Share by Type (2012-2017)

Figure Production Revenue Share of Chip Handler in Semiconductor by Type (2012-2017)

Figure 2016 Revenue Market Share of Chip Handler in Semiconductor by Type

Table Global Chip Handler in Semiconductor Price (USD/Unit) by Type (2012-2017)

Figure Global Chip Handler in Semiconductor Production Growth by Type (2012-2017)

Table Global Chip Handler in Semiconductor Consumption (K Units) by Application (2012-2017)

Table Global Chip Handler in Semiconductor Consumption Market Share by Application (2012-2017)

Figure Global Chip Handler in Semiconductor Consumption Market Share by Applications (2012-2017)

Figure Global Chip Handler in Semiconductor Consumption Market Share by Application in 2016

Table Global Chip Handler in Semiconductor Consumption Growth Rate by Application (2012-2017)

Figure Global Chip Handler in Semiconductor Consumption Growth Rate by Application (2012-2017)

Table Multitest Basic Information, Manufacturing Base, Sales Area and Its Competitors

Table Multitest Chip Handler in Semiconductor Capacity, Production (K Units), Revenue

(Million USD), Price (USD/Unit) and Gross Margin (2012-2017)

Figure Multitest Chip Handler in Semiconductor Production Growth Rate (2012-2017)

Figure Multitest Chip Handler in Semiconductor Production Market Share (2012-2017)

Figure Multitest Chip Handler in Semiconductor Revenue Market Share (2012-2017)

Table Seiko Epson Corporation Basic Information, Manufacturing Base, Sales Area and Its Competitors

Table Seiko Epson Corporation Chip Handler in Semiconductor Capacity, Production (K Units), Revenue (Million USD), Price (USD/Unit) and Gross Margin (2012-2017)

Figure Seiko Epson Corporation Chip Handler in Semiconductor Production Growth Rate (2012-2017)

Figure Seiko Epson Corporation Chip Handler in Semiconductor Production Market Share (2012-2017)

Figure Seiko Epson Corporation Chip Handler in Semiconductor Revenue Market Share (2012-2017)

Table Multitest Basic Information, Manufacturing Base, Sales Area and Its Competitors

Table Multitest Chip Handler in Semiconductor Capacity, Production (K Units), Revenue (Million USD), Price (USD/Unit) and Gross Margin (2012-2017)

Figure Multitest Chip Handler in Semiconductor Production Growth Rate (2012-2017)

Figure Multitest Chip Handler in Semiconductor Production Market Share (2012-2017)

Figure Multitest Chip Handler in Semiconductor Revenue Market Share (2012-2017)

Table ASM Pacific Technology Basic Information, Manufacturing Base, Sales Area and Its Competitors

Table ASM Pacific Technology Chip Handler in Semiconductor Capacity, Production (K Units), Revenue (Million USD), Price (USD/Unit) and Gross Margin (2012-2017)

Figure ASM Pacific Technology Chip Handler in Semiconductor Production Growth Rate (2012-2017)

Figure ASM Pacific Technology Chip Handler in Semiconductor Production Market Share (2012-2017)

Figure ASM Pacific Technology Chip Handler in Semiconductor Revenue Market Share (2012-2017)

Table Cohu Basic Information, Manufacturing Base, Sales Area and Its Competitors

Table Cohu Chip Handler in Semiconductor Capacity, Production (K Units), Revenue (Million USD), Price (USD/Unit) and Gross Margin (2012-2017)

Figure Cohu Chip Handler in Semiconductor Production Growth Rate (2012-2017)

Figure Cohu Chip Handler in Semiconductor Production Market Share (2012-2017)

Figure Cohu Chip Handler in Semiconductor Revenue Market Share (2012-2017)

Table Advantest Basic Information, Manufacturing Base, Sales Area and Its Competitors

Table Advantest Chip Handler in Semiconductor Capacity, Production (K Units),



Revenue (Million USD), Price (USD/Unit) and Gross Margin (2012-2017)

Figure Advantest Chip Handler in Semiconductor Production Growth Rate (2012-2017)

Figure Advantest Chip Handler in Semiconductor Production Market Share (2012-2017)

Figure Advantest Chip Handler in Semiconductor Revenue Market Share (2012-2017)

Table Boston Semi Equipment Basic Information, Manufacturing Base, Sales Area and Its Competitors

Table Boston Semi Equipment Chip Handler in Semiconductor Capacity, Production (K Units), Revenue (Million USD), Price (USD/Unit) and Gross Margin (2012-2017)

Figure Boston Semi Equipment Chip Handler in Semiconductor Production Growth Rate (2012-2017)

Figure Boston Semi Equipment Chip Handler in Semiconductor Production Market Share (2012-2017)

Figure Boston Semi Equipment Chip Handler in Semiconductor Revenue Market Share (2012-2017)

Table Hon Technologies Basic Information, Manufacturing Base, Sales Area and Its Competitors

Table Hon Technologies Chip Handler in Semiconductor Capacity, Production (K Units), Revenue (Million USD), Price (USD/Unit) and Gross Margin (2012-2017)

Figure Hon Technologies Chip Handler in Semiconductor Production Growth Rate (2012-2017)

Figure Hon Technologies Chip Handler in Semiconductor Production Market Share (2012-2017)

Figure Hon Technologies Chip Handler in Semiconductor Revenue Market Share (2012-2017)

Table SRM Integration Basic Information, Manufacturing Base, Sales Area and Its Competitors

Table SRM Integration Chip Handler in Semiconductor Capacity, Production (K Units), Revenue (Million USD), Price (USD/Unit) and Gross Margin (2012-2017)

Figure SRM Integration Chip Handler in Semiconductor Production Growth Rate (2012-2017)

Figure SRM Integration Chip Handler in Semiconductor Production Market Share (2012-2017)

Figure SRM Integration Chip Handler in Semiconductor Revenue Market Share (2012-2017)

Table SYNAX Basic Information, Manufacturing Base, Sales Area and Its Competitors

Table SYNAX Chip Handler in Semiconductor Capacity, Production (K Units), Revenue (Million USD), Price (USD/Unit) and Gross Margin (2012-2017)

Figure SYNAX Chip Handler in Semiconductor Production Growth Rate (2012-2017)

Figure SYNAX Chip Handler in Semiconductor Production Market Share (2012-2017)

Figure SYNAX Chip Handler in Semiconductor Revenue Market Share (2012-2017)

Table Production Base and Market Concentration Rate of Raw Material

Figure Price Trend of Key Raw Materials

Table Key Suppliers of Raw Materials

Figure Manufacturing Cost Structure of Chip Handler in Semiconductor

Figure Manufacturing Process Analysis of Chip Handler in Semiconductor

Figure Chip Handler in Semiconductor Industrial Chain Analysis

Table Raw Materials Sources of Chip Handler in Semiconductor Major Manufacturers in 2016

Table Major Buyers of Chip Handler in Semiconductor

Table Distributors/Traders List

Figure Global Chip Handler in Semiconductor Capacity, Production (K Units) and Growth Rate Forecast (2017-2022)

Figure Global Chip Handler in Semiconductor Revenue (Million USD) and Growth Rate Forecast (2017-2022)

Figure Global Chip Handler in Semiconductor Price (Million USD) and Trend Forecast (2017-2022)

Table Global Chip Handler in Semiconductor Production (K Units) Forecast by Region (2017-2022)

Figure Global Chip Handler in Semiconductor Production Market Share Forecast by Region (2017-2022)

Table Global Chip Handler in Semiconductor Consumption (K Units) Forecast by Region (2017-2022)

Figure Global Chip Handler in Semiconductor Consumption Market Share Forecast by Region (2017-2022)

Figure North America Chip Handler in Semiconductor Production (K Units) and Growth Rate Forecast (2017-2022)

Figure North America Chip Handler in Semiconductor Revenue (Million USD) and Growth Rate Forecast (2017-2022)

Table North America Chip Handler in Semiconductor Production, Consumption, Export and Import (K Units) Forecast (2017-2022)

Figure Europe Chip Handler in Semiconductor Production (K Units) and Growth Rate Forecast (2017-2022)

Figure Europe Chip Handler in Semiconductor Revenue (Million USD) and Growth Rate Forecast (2017-2022)

Table Europe Chip Handler in Semiconductor Production, Consumption, Export and Import (K Units) Forecast (2017-2022)

Figure China Chip Handler in Semiconductor Production (K Units) and Growth Rate Forecast (2017-2022)

Figure China Chip Handler in Semiconductor Revenue (Million USD) and Growth Rate Forecast (2017-2022)

Table China Chip Handler in Semiconductor Production, Consumption, Export and Import (K Units) Forecast (2017-2022)

Figure Japan Chip Handler in Semiconductor Production (K Units) and Growth Rate Forecast (2017-2022)

Figure Japan Chip Handler in Semiconductor Revenue (Million USD) and Growth Rate Forecast (2017-2022)

Table Japan Chip Handler in Semiconductor Production, Consumption, Export and Import (K Units) Forecast (2017-2022)

Figure Southeast Asia Chip Handler in Semiconductor Production (K Units) and Growth Rate Forecast (2017-2022)

Figure Southeast Asia Chip Handler in Semiconductor Revenue (Million USD) and Growth Rate Forecast (2017-2022)

Table Southeast Asia Chip Handler in Semiconductor Production, Consumption, Export and Import (K Units) Forecast (2017-2022)

Figure India Chip Handler in Semiconductor Production (K Units) and Growth Rate Forecast (2017-2022)

Figure India Chip Handler in Semiconductor Revenue (Million USD) and Growth Rate Forecast (2017-2022)

Table India Chip Handler in Semiconductor Production, Consumption, Export and Import (K Units) Forecast (2017-2022)

Table Global Chip Handler in Semiconductor Production (K Units) Forecast by Type (2017-2022)

Figure Global Chip Handler in Semiconductor Production (K Units) Forecast by Type (2017-2022)

Table Global Chip Handler in Semiconductor Revenue (Million USD) Forecast by Type (2017-2022)

Figure Global Chip Handler in Semiconductor Revenue Market Share Forecast by Type (2017-2022)

Table Global Chip Handler in Semiconductor Price Forecast by Type (2017-2022)

Table Global Chip Handler in Semiconductor Consumption (K Units) Forecast by Application (2017-2022)

Figure Global Chip Handler in Semiconductor Consumption (K Units) Forecast by Application (2017-2022)

Table Research Programs/Design for This Report

Figure Bottom-up and Top-down Approaches for This Report

Figure Data Triangulation

Table Key Data Information from Secondary Sources

## Table Key Data Information from Primary Source

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