

# Covid-19 Impact on Global SRAM Chip Market Insights, Forecast to 2026

<https://marketpublishers.com/r/C2766F693753EN.html>

Date: June 2020

Pages: 112

Price: US\$ 4,900.00 (Single User License)

ID: C2766F693753EN

## Abstracts

Static random access memory (SRAM) is a form of memory in a computer system. SRAM provides lowlatency, high speed data access. It is a volatile memory technology, meaning that its data is lost when power is turned off. Because of the relatively large SRAM cell size, it is not economically feasible to implement large capacity memories as SRAM.

Historically, SRAM provided memory to the computer system in discrete form. That role has since been supplanted by DRAM. However, SRAM continues to play a very important role integrated into silicon alongside CPUs, ASICs, and SoCs. Processor cache is almost exclusively implemented as SRAM. Firmware registers and FIFOs within the digital logic also utilize SRAM.

The core of a singlebit SRAM cell is just a latch made of two crosscoupled inverters. There are also two transistors, which act as pass gates to control access to the cell from the bitline. The feedback loop inherent in the latch means that periodic refresh cycles (such as in DRAM) are unnecessary. To read from the SRAM cell, the pass gates are activated and the latch is allowed to drive the bitlines high or low. Writing to the SRAM cell is more involved: the internal feedback of the latch must be overpowered by the input circuit, which is providing the new data.

Since the COVID-19 virus outbreak in December 2019, the disease has spread to almost 100 countries around the globe with the World Health Organization declaring it a public health emergency. The global impacts of the coronavirus disease 2019 (COVID-19) are already starting to be felt, and will significantly affect the SRAM Chip market in 2020.

COVID-19 can affect the global economy in three main ways: by directly affecting production and demand, by creating supply chain and market disruption, and by its financial impact on firms and financial markets.

The outbreak of COVID-19 has brought effects on many aspects, like flight

cancellations; travel bans and quarantines; restaurants closed; all indoor events restricted; over forty countries state of emergency declared; massive slowing of the supply chain; stock market volatility; falling business confidence, growing panic among the population, and uncertainty about future.

This report also analyses the impact of Coronavirus COVID-19 on the SRAM Chip industry.

Based on our recent survey, we have several different scenarios about the SRAM Chip YoY growth rate for 2020. The probable scenario is expected to grow by a xx% in 2020 and the revenue will be xx in 2020 from US\$ xx million in 2019. The market size of SRAM Chip will reach xx in 2026, with a CAGR of xx% from 2020 to 2026.

With industry-standard accuracy in analysis and high data integrity, the report makes a brilliant attempt to unveil key opportunities available in the global SRAM Chip market to help players in achieving a strong market position. Buyers of the report can access verified and reliable market forecasts, including those for the overall size of the global SRAM Chip market in terms of both revenue and volume.

Players, stakeholders, and other participants in the global SRAM Chip market will be able to gain the upper hand as they use the report as a powerful resource. For this version of the report, the segmental analysis focuses on sales (volume), revenue and forecast by each application segment in terms of sales and revenue and forecast by each type segment in terms of revenue for the period 2015-2026.

### Production and Pricing Analyses

Readers are provided with deeper production analysis, import and export analysis, and pricing analysis for the global SRAM Chip market. As part of production analysis, the report offers accurate statistics and figures for production capacity, production volume by region, and global production and production by each type segment for the period 2015-2026.

In the pricing analysis section of the report, readers are provided with validated statistics and figures for price by manufacturer and price by region for the period 2015-2020 and price by each type segment for the period 2015-2026. The import and export analysis for the global SRAM Chip market has been provided based on region.

### Regional and Country-level Analysis

The report offers an exhaustive geographical analysis of the global SRAM Chip market, covering important regions, viz, North America, Europe, China, Japan and South Korea. It also covers key countries (regions), viz, U.S., Canada, Germany, France, U.K., Italy, Russia, China, Japan, South Korea, India, Australia, Taiwan, Indonesia, Thailand,

Malaysia, Philippines, Vietnam, Mexico, Brazil, Turkey, Saudi Arabia, U.A.E, etc. The report includes country-wise and region-wise market size for the period 2015-2026. It also includes market size and forecast by each application segment in terms of volume for the period 2015-2026.

### Competition Analysis

In the competitive analysis section of the report, leading as well as prominent players of the global SRAM Chip market are broadly studied on the basis of key factors. The report offers comprehensive analysis and accurate statistics on sales by the player for the period 2015-2020. It also offers detailed analysis supported by reliable statistics on price and revenue (global level) by player for the period 2015-2020.

On the whole, the report proves to be an effective tool that players can use to gain a competitive edge over their competitors and ensure lasting success in the global SRAM Chip market. All of the findings, data, and information provided in the report are validated and revalidated with the help of trustworthy sources. The analysts who have authored the report took a unique and industry-best research and analysis approach for an in-depth study of the global SRAM Chip market.

The following manufacturers are covered in this report:

ISSI

Cypress Semiconductor

Alliance Memory

Renesas Electronics Corporation

Microchip

GSI Technology

Phoenix Contact

Renesas Electronics

VORAGO Technologies

## SRAM Chip Breakdown Data by Type

Below 1 Mbit

2-128 Mbit

Above 128 Mbit

## SRAM Chip Breakdown Data by Application

Parallel

Serial

SPI

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