

# Asia-Pacific Wafer Sorter Market Report 2017

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## Abstracts

In this report, the Asia-Pacific Wafer Sorter market is valued at USD XX million in 2016 and is expected to reach USD XX million by the end of 2022, growing at a CAGR of XX% between 2016 and 2022.

Geographically, this report split Asia-Pacific into several key Regions, with sales (K Units), revenue (Million USD), market share and growth rate of Wafer Sorter for these regions, from 2012 to 2022 (forecast), including

China

Japan

South Korea

Taiwan

India

Southeast Asia

Australia

Asia-Pacific Wafer Sorter market competition by top manufacturers/players, with Wafer Sorter sales volume, price, revenue (Million USD) and market share for each manufacturer/player; the top players including

Brooks(US)

C&D Semiconductor Services(US)

Tokyo Electron(JP)

Mechatronic Systemtechnik(DE)

Dou Yee Enterprises(CN)

GL Automation(US)

Genmark Automation(US)

ZS-Handling(DE)

Nadatech(US)

Waf-tech(MY)

OAI(US)

On the basis of product, this report displays the sales volume (K Units), revenue (Million USD), product price (USD/Unit), market share and growth rate of each type, primarily split into

Sort 0 mm – 50 mm

Sort 50 mm to 200 mm

Sort 200 mm to 300 mm

Sort More than 300 mm

On the basis on the end users/applications, this report focuses on the status and outlook for major applications/end users, sales volume (K Units), market share and growth rate of Wafer Sorter for each application, includin

Wafer Sorters for Si

Wafer Sorters for GaAs

Wafer Sorters for Sapphire

Wafer Sorters for SiC?

Wafer Sorters for Other Materials

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