

Wafer Level Packaging Market - Global Outlook and Forecast 2022-2028

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Abstracts

Wafer-level packaging (WLP) is the technology of packaging an integrated circuit while still part of the wafer, in contrast to the more conventional method of slicing the wafer into individual circuits (dice) and then packaging them. WLP is essentially a true chip-scale package (CSP) technology, since the resulting package is practically of the same size as the die.[1] Wafer-level packaging allows integration of wafer fab, packaging, test, and burn-in at wafer level in order to streamline the manufacturing process undergone by a device from silicon start to customer shipment.

Wafer-level packaging consists of extending the wafer fab processes to include device interconnection and device protection processes. Most other kinds of packaging do wafer dicing first, and then put the individual die in a plastic package and attach the solder bumps. Wafer-level packaging involves attaching the top and bottom outer layers of packaging, and the solder bumps, to integrated circuits while still in the wafer, and then wafer dicing.

There is no single industry-standard method of wafer-level packaging at present.

A major application area of WLPs are smartphones due to the size constraints.

This report contains market size and forecasts of Wafer Level Packaging in global, including the following market information:

Global Wafer Level Packaging Market Revenue, 2017-2022, 2023-2028, (\$ millions)

Global Wafer Level Packaging Market Sales, 2017-2022, 2023-2028, (K Units)

Global top five Wafer Level Packaging companies in 2021 (%)

The global Wafer Level Packaging market was valued at 3714.5 million in 2021 and is projected to reach US\$ 7439.5 million by 2028, at a CAGR of 10.4% during the forecast period.

The U.S. Market is Estimated at \$ Million in 2021, While China is Forecast to Reach \$ Million by 2028.

3D TSV WLP Segment to Reach \$ Million by 2028, with a % CAGR in next six years.

The global key manufacturers of Wafer Level Packaging include Amkor Technology Inc, Fujitsu Ltd, Jiangsu Changjiang Electronics, Deca Technologies, Qualcomm Inc, Toshiba Corp, Tokyo Electron Ltd, Applied Materials, Inc and ASML Holding NV, etc. In 2021, the global top five players have a share approximately % in terms of revenue.

MARKET MONITOR GLOBAL, INC (MMG) has surveyed the Wafer Level Packaging manufacturers, suppliers, distributors and industry experts on this industry, involving the sales, revenue, demand, price change, product type, recent development and plan, industry trends, drivers, challenges, obstacles, and potential risks.

Total Market by Segment:

Global Wafer Level Packaging Market, by Type, 2017-2022, 2023-2028 (\$ Millions) & (K Units)

Global Wafer Level Packaging Market Segment Percentages, by Type, 2021 (%)

3D TSV WLP

2.5D TSV WLP

WLCSP

Nano WLP

Others (2D TSV WLP and Compliant WLP)

Global Wafer Level Packaging Market, by Application, 2017-2022, 2023-2028 (\$ Millions) & (K Units)

Global Wafer Level Packaging Market Segment Percentages, by Application, 2021 (%)

Electronics

IT & Telecommunication

Industrial

Automotive

Aerospace & Defense

Healthcare

Others (Media & Entertainment and Non-Conventional Energy Resources)

Global Wafer Level Packaging Market, By Region and Country, 2017-2022, 2023-2028 (\$ Millions) & (K Units)

Global Wafer Level Packaging Market Segment Percentages, By Region and Country, 2021 (%)

North America

US

Canada

Mexico

Europe

Germany

France

U.K.

Italy

Russia

Nordic Countries

Benelux

Rest of Europe

Asia

China

Japan

South Korea

Southeast Asia

India

Rest of Asia

South America

Brazil

Argentina

Rest of South America

Middle East & Africa

Turkey

Israel

Saudi Arabia

UAE

Rest of Middle East & Africa

Competitor Analysis

The report also provides analysis of leading market participants including:

Key companies Wafer Level Packaging revenues in global market, 2017-2022
(Estimated), (\$ millions)

Key companies Wafer Level Packaging revenues share in global market, 2021 (%)

Key companies Wafer Level Packaging sales in global market, 2017-2022 (Estimated),
(K Units)

Key companies Wafer Level Packaging sales share in global market, 2021 (%)

Further, the report presents profiles of competitors in the market, key players include:

Amkor Technology Inc

Fujitsu Ltd

Jiangsu Changjiang Electronics

Deca Technologies

Qualcomm Inc

Toshiba Corp

Tokyo Electron Ltd

Applied Materials, Inc

ASML Holding NV

Lam Research Corp

KLA-Tencor Corration

China Wafer Level CSP Co. Ltd

Marvell Technology Group Ltd

Siliconware Precision Industries

Nanium SA

STATS Chip

PAC Ltd

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