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RISC-V Market By Offering [(Hardware, RISC-V IP (IP Royalties, IP Licensing), Software], Bit Architecture (32-bit, 64-bit, 128-bit), Application (Industrial, Automotive, Networking, Computer, Consumer, Aerospace & Defense) – Global Forecast to 2032

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Abstracts

The RISC-V market is anticipated to grow from USD 1.31 billion in 2026 to USD 4.85 billion by 2032, at a CAGR of 24.3%. The strongest driver is the accelerating shift toward open-standard processor architectures, where companies prefer RISC-V to avoid high licensing costs and vendor lock-in associated with proprietary Instruction Set Architectures (ISAs). Rapid growth in artificial intelligence (AI), machine learning (ML), and edge computing workloads is another key factor, as RISC-V enables highly customizable accelerators and heterogeneous compute designs optimized for performance and power efficiency.

Consumer applications held largest market share in 2025

Consumer applications held the largest share of the RISC-V market in 2025 due to the massive deployment of RISC-V solutions across smartphones, laptops, wearables, smart home devices, and personal electronics. High-volume adoption of low-power RISC-V MCUs and SoCs in cost-sensitive consumer products significantly boosted shipment scale and IP consumption. Manufacturers in this segment prioritize cost efficiency, design flexibility, and power optimization, all of which align strongly with RISC-V's open and customizable architecture. Additionally, the growing integration of AI-enabled features in consumer devices, such as voice assistants, on-device inference tools, and edge processing tools, has increased demand for heterogeneous computing architectures where RISC-V cores are used for control and auxiliary processing tasks. The presence of large ODMs and OEMs rapidly integrating RISC-V architecture into

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next-generation consumer platforms further strengthened its dominance in terms of volume contribution in 2025.

Software offerings to record highest CAGR during forecast period

The software segment is expected to register the highest CAGR in the RISC-V market from 2026 to 2032 due to rapid ecosystem expansion and increasing complexity of RISC-V-based system development. As hardware adoption accelerates, demand for design and verification tools, compiler toolchains, integrated development environments (IDEs), operating systems, middleware, and security software is rising sharply to support scalable development environments. The transition from early-stage adoption to mainstream commercialization is driving strong investment in software maturity, including optimization for AI workloads, cloud-native development, and embedded applications. Growing contributions from open-source communities and commercial vendors are improving compatibility, reducing time-to-market, and enhancing performance portability across platforms. Furthermore, the need for standardized software stacks across fragmented hardware implementations is accelerating enterprise adoption. As RISC-V moves into data centers, automotive systems, and edge AI platforms, software becomes the critical enabler layer, leading to a faster growth rate compared to IP and hardware segments.

Asia Pacific to exhibit highest CAGR during forecast period

Asia Pacific is expected to register the highest CAGR in the RISC-V market owing to the rapid expansion of AI, edge computing, industrial IoT, and automotive electronics applications across the region. The growing deployment of AI-enabled devices, smart factories, robotics systems, autonomous vehicles, and intelligent transportation infrastructure is creating strong demand for customizable and energy-efficient processor architectures. RISC-V processors provide regional semiconductor companies with the flexibility to integrate application-specific instructions, AI accelerators, vector processing capabilities, and low-power computing features optimized for edge and AI workloads. Additionally, hyperscalers and cloud service providers in the region are increasingly evaluating RISC-V for data center, networking, and AI infrastructure applications to improve workload optimization and reduce total development costs. The strong presence of consumer electronics manufacturing hubs across China, Taiwan, South Korea, and Japan is further accelerating the adoption of RISC-V architectures in next-generation connected devices. Moreover, increasing investments by companies such as Alibaba, Andes Technology, and StarFive in advanced RISC-V processor development and ecosystem expansion are strengthening the region’s technological leadership and

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supporting long-term market growth across commercial and industrial computing platforms.

Extensive primary interviews were conducted with key experts in the RIS-V industry to determine and verify the market size for various segments and subsegments gathered through secondary research. The breakdown of primary participants for the report is provided below:

The study contains insights from various industry experts, from component suppliers to Tier 1 companies and OEMs. The break-up of the primaries is as follows:

By Company Type: Tier 1–20%, Tier 2–25%, and Tier 3–55%

By Designation: C-level Executives–30%, Directors–30%, and Others–40%

By Region: North America–30%, Europe–15%, Asia Pacific–50%, and RoW–5%

The report profiles the key players in the RISC-V market with their respective ranking analysis. Prominent players profiled in this report are SiFive, Inc. (US), Andes Technology Corporation (Taiwan), Codasip (Germany), Synopsys, Inc. (US), Imagination Technologies (UK), MIPS (US), Tenstorrent (US), Shanghai StarFive Semiconductor Co., Ltd. (China), Nuclei System (China), and Efinix, Inc. (US), among others.

Apart from this, Semidynamics (Spain), Alibaba (China), CORTUS (France), Syntacore (Cyprus), InCore (India), Bluespec (US), Lattice Semiconductor (US), SEGGER (Germany), IAR (Sweden), Wind River (US), Green Hills Software (US), Lauterbach GmbH (Germany), emproof GmbH (Netherlands), Canonical (UK), Cadence Design Systems, Inc. (US), and Spanidea (US) are among a few other players in the RISC-V market.

Research Coverage:

This research report categorizes the RISC-V market based on offering, bit architecture, application, and region. The report describes the major drivers, restraints, challenges, and opportunities pertaining to the RISC-V market and forecasts the same till 2032. Apart from these, the report also consists of leadership mapping and analysis of all the companies included in the RISC-V ecosystem.

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Key Benefits of Buying the Report

The report will help the market leaders/new entrants in this market with information on the closest approximations of the numbers for the overall RISC-V market and the subsegments. This report will help stakeholders understand the competitive landscape and gain more insights to position their businesses better and plan suitable go-to-market strategies. The report also helps stakeholders understand the pulse of the market and provides them with information on key market drivers, restraints, challenges, and opportunities.

The report provides insights into the following pointers:

Analysis of key drivers (Cost Efficiency & Licensing Freedom), restraints (Ecosystem Maturity Gap), opportunities (Expansion of Edge AI and AIoT infrastructure), and challenges (Competition from Established Architectures)

Product Development/Innovation: Detailed insights on upcoming technologies, research & development activities, and new product launches in the RISC-V market

Market Development: Comprehensive information about lucrative markets by analyzing the RISC-V market across varied regions

Market Diversification: Exhaustive information about new products, untapped geographies, recent developments, and investments in the RISC-V market

Competitive Assessment: In-depth assessment of market shares, growth strategies, and service offerings of leading market players such as SiFive, Inc. (US), Andes Technology Corporation (Taiwan), Codasip (Germany), Synopsys, Inc. (US), and Imagination Technologies (UK)

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+357 96 030922
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+357 96 030922
info@marketpublishers.com

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info@marketpublishers.com

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