

# AI EDA Market by Product Category (Computer-aided Engineering, Integrated Circuit Physical Design Verification, Printed Circuit Board & Multi-chip Module, Services), Deployment Mode (On-premises, Cloud-based, Hybrid) - Global Forecast to 2032

<https://marketpublishers.com/r/A1ECC89AC35FEN.html>

Date: March 2026

Pages: 242

Price: US\$ 4,950.00 (Single User License)

ID: A1ECC89AC35FEN

## Abstracts

The AI EDA market is anticipated to grow from USD 4.27 billion in 2026 to USD 15.85 billion by 2032, at a CAGR of 24.4% from 2026 to 2032. The market growth is supported by the increasing use of data-driven design workflows and intelligent automation across complex semiconductor projects, where AI helps improve design exploration speed, reduce manual intervention, and enhance overall design quality. In addition, AI-based tools support better management of large design datasets, faster convergence during implementation stages, and more stable results across repeated design programs. These attributes make them crucial for maintaining efficiency and reliability as chip designs continue to increase in scale and complexity.

“Consumer electronics segment is anticipated to capture the largest market share in 2026”

In the AI EDA market, the consumer electronics segment is expected to hold the largest market share in 2026. This is mainly because the industry produces high volumes of chips with short product life cycles and strong pressure to launch new products quickly. Smartphones, wearables, personal computing devices, and smart home products require frequent design updates, feature integration, and cost optimization. These factors increase the demand for efficient and intelligent design tools. In addition, consumer electronics companies focus heavily on performance, power efficiency, and compact form factors, which drives the wider use of AI-based design optimization and verification solutions across their chip development programs. This results in higher

overall spending on AI EDA compared with other end use segments.

“Computer-aided engineering (CAE) is projected to record the highest CAGR from 2026 to 2032”

In the AI EDA market, the computer-aided engineering (CAE) segment is expected to record the highest CAGR during the forecast period as future chip programs will rely more on early-stage, system-level analysis to avoid costly downstream changes. As designs increasingly require simultaneous evaluation of electrical, thermal, and reliability behaviour, AI-enabled CAE tools become critical for fast scenario testing and virtual prototyping. This shift moves more decision-making to the front of the design cycle, expands the use of simulation-driven optimization, and accelerates the adoption of AI within CAE workflows.

“Asia Pacific is likely to exhibit the highest CAGR in the AI EDA market during the forecast period”

In the AI EDA market, Asia Pacific is expected to register the highest CAGR during the forecast period, driven by the rapid expansion in semiconductor manufacturing, local chip design activity, and electronics production capacity. Countries across the region are increasing the investment in advanced nodes, packaging, and domestic design ecosystems, which boosts the demand for modern AI-driven design and verification tools. In addition, the growth of automotive electronics, consumer devices, and data infrastructure creates a larger pipeline of new chip projects, making the adoption of AI EDA solutions accelerate faster than in more mature markets.

Extensive primary interviews were conducted with key industry experts offering AI EDA solutions to determine and verify the market size for various segments and subsegments gathered through secondary research. The breakdown of primary participants for the report is provided below.

The study contains insights from various industry experts, from component suppliers to Tier 1 companies and OEMs. The break-up of the primaries is as follows:

By Company Type: Tier 1–40%, Tier 2–35%, and Tier 3–25%

By Designation: C-level Executives–40%, Directors–45%, and Others–15%

By Region: North America–26%, Europe–28%, Asia Pacific–41%, and RoW–5%

The report profiles key players in the AI EDA market with their respective market ranking analysis. Prominent players profiled in this report are Synopsys, Inc. (US), Cadence Design Systems, Inc. (US), Siemens (Germany), Keysight Technologies (US), PrimisAI (US), Circuit Mind Limited (UK), Quilter AI (US), Zuken (Japan), Doide Computers, Inc. (US), and Celus GmbH (Germany), among others.

Apart from these, Flux (US), JITX (US), Silimate, Inc. (US), AMIQ EDA (Romania), Bronco AI, Inc. (US), ChipAgents (US), MooresLab AI (US), Rise Design Automation (US), Silogy Technologies, Inc. (US), Chipmind AG (Switzerland), Cognichip, Inc. (US), Astrus (Canada), ChipStack, Inc. (US), Maieutic Semiconductors (India), and AllSpice.io (US) also operate in the AI EDA market.

### **Research Coverage:**

This research report categorizes the AI EDA market based on product category, deployment mode, application, end use, and region. The report describes the major drivers, restraints, challenges, and opportunities pertaining to the AI EDA market and forecasts the same till 2032. Apart from these, the report also consists of leadership mapping and analysis of all the companies included in the AI EDA market ecosystem.

### **Key Benefits of Buying the Report**

The report will help the market leaders/new entrants in this market with information on the closest approximations of the numbers for the overall AI EDA market and the subsegments. This report will help stakeholders understand the competitive landscape and gain more insights to position their businesses better and plan suitable go-to-market strategies. The report also helps stakeholders understand the pulse of the market and provides them with information on key market drivers, restraints, challenges, and opportunities.

### **The report provides insights into the following pointers:**

Analysis of key drivers (Rising complexity in semiconductor design at advanced technology nodes, High pressure to reduce chip design cycles, Growing emphasis on reducing chip design and verification costs), restraints (Concerns over protection of sensitive design intellectual property, Workflow reconfiguration and integration issues in AI-enabled EDA), opportunities (Integration of

generative AI into early-stage design exploration, Development of AI copilots embedded directly within EDA tools), and challenges (Evolving design constraints and frequent updates to EDA tools, Shortage of AI EDA expertise) of the AI EDA market

**Product Development/Innovation:** Detailed insights into upcoming technologies, research & development activities, and new product/service launches in the AI EDA market

**Market Development:** Comprehensive information about lucrative markets—the report analyzes the AI EDA market across varied regions

**Market Diversification:** Exhaustive information about new products & services, untapped geographies, recent developments, and investments in the AI EDA market

**Competitive Assessment:** In-depth assessment of market shares, growth strategies, and service offerings of leading players, such as Synopsys, Inc. (US), Cadence Design Systems, Inc. (US), Siemens (Germany), Keysight Technologies (US), and Zuken (Japan) in the AI EDA market

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