

2020-2025 Global System In Package Market Report - Production and Consumption Professional Analysis (Impact of COVID-19)

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Abstracts

SiP is a kind of package concept, which is a package method in which all or most of the electronic functions of a system or subsystem are arranged in an integrated substrate, and the chip is bonded to the integrated substrate in a 2D or 3D manner. The SIP package does not have a certain type. In terms of the arrangement of the chips, the SIP can be a planar 2D package of a multi-chip module, and the structure of the 3D package can be reused to effectively reduce the package area; and the internal bonding technology can be For simple wire bonding, flip chip bonding can also be used, but it can also be used in combination. In addition to the 2D and 3D package structure, another way to integrate components with versatile substrates can also be included in the scope of SIP. This technology is mainly to embed different components in the multi-function substrate, and can also be regarded as the concept of SIP to achieve the purpose of functional integration. Different chip arrangements, combined with different internal bonding technologies, enable a variety of combinations of SIP package types, and can be customized or flexibly produced according to customer or product requirements.

This report elaborates the market size, market characteristics, and market growth of the System In Package industry, and breaks down according to the type, application, and consumption area of System In Package. The report also conducted a PESTEL analysis of the industry to study the main influencing factors and entry barriers of the industry.

In Chapter 3.4 of the report, the impact of the COVID-19 outbreak on the industry was fully assessed. Fully risk assessment and industry recommendations were made for System In Package in a special period. This chapter also compares the markets of Pre COVID-19 and Post COVID-19.

In addition, chapters 8-12 consider the impact of COVID-19 on the regional economy.

Key players in the global System In Package market covered in Chapter 13:

Intel

Chipmos Technologies

ASE

Jiangsu Changjiang Electronics

Renesas Electronics

Toshiba

Fujitsu

Texas Instruments

Signetics

KYEC

Samsung

Amkor

In Chapter 6, on the basis of types, the System In Package market from 2015 to 2025 is primarily split into:

2-D IC Packaging

2.5-D IC Packaging

3-D IC Packaging

In Chapter 7, on the basis of applications, the System In Package market from 2015 to 2025 covers:

Consumer Electronics

Automotive

Telecommunication

Industrial System

Aerospace & Defense

Others

Geographically, the detailed analysis of production, trade of the following countries is covered in Chapter 4.2, 5:

United States

Europe

China

Japan

India

Geographically, the detailed analysis of consumption, revenue, market share and

growth rate of the following regions are covered in Chapter 8, 9, 10, 11, 12:

North America (Covered in Chapter 8)

United States

Canada

Mexico

Europe (Covered in Chapter 9)

Germany

UK

France

Italy

Spain

Others

Asia-Pacific (Covered in Chapter 10)

China

Japan

India

South Korea

Southeast Asia

Others

Middle East and Africa (Covered in Chapter 11)

Saudi Arabia

UAE

South Africa

Others

South America (Covered in Chapter 12)

Brazil

Others

Years considered for this report:

Historical Years: 2015-2019

Base Year: 2019

Estimated Year: 2020

Forecast Period: 2020-2025

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