

Global Wired Interface IP Market Growth (Status and Outlook) 2026-2032

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Abstracts

The global Wired Interface IP market size is predicted to grow from US\$ 1705 million in 2025 to US\$ 2659 million in 2032; it is expected to grow at a CAGR of 7.1% from 2026 to 2032.

Wired Interface IP is a key semiconductor IP category used in chip designs to implement peripheral connectivity, memory access, network communication, display transmission, chip-to-chip interconnects and on-chip or in-package data paths. Its core value lies in packaging complex protocol control logic, PHY analog circuits, high-speed SerDes, clocking and signal-integrity design, power management, protocol compliance and cross-node portability into reusable modules, helping chip designers reduce development complexity, shorten tape-out cycles and improve system interoperability. With rising demand for high-bandwidth, low-latency and low-power connectivity in AI computing, chiplets, automotive electronics, smart devices, industrial control and data-center equipment, Wired Interface IP has become one of the most important foundational IP categories in advanced SoCs and heterogeneous integrated chips.

The Wired Interface IP market is driven by AI computing, data-center bandwidth upgrades, chiplet and advanced packaging architectures, the evolution of intelligent automotive electronics, high-speed consumer connectivity and rising chip design complexity. AI accelerators, CPUs, GPUs, DPUs, switch chips and storage controllers require PCIe, CXL, UCIe, HBM, DDR and high-speed SerDes to enable higher-bandwidth data movement. Chiplet architectures are shifting die-to-die connectivity from proprietary interfaces toward standardized solutions, creating incremental demand for UCIe and die-to-die IP. Automotive electronics, industrial control and smart devices continue to increase demand for MIPI, USB, Ethernet, CAN/LIN and low-power interface IP. At advanced process nodes, PHY design, signal integrity, clock recovery, power

optimization and protocol compliance verification are becoming more difficult, encouraging chip companies to license mature silicon-proven IP to reduce tape-out risk and accelerate time to market.

LPI (LP Information)' newest research report, the 'Wired Interface IP Industry Forecast' looks at past sales and reviews total world Wired Interface IP sales in 2025, providing a comprehensive analysis by region and market sector of projected Wired Interface IP sales for 2026 through 2032. With Wired Interface IP sales broken down by region, market sector and sub-sector, this report provides a detailed analysis in US\$ millions of the world Wired Interface IP industry.

This Insight Report provides a comprehensive analysis of the global Wired Interface IP landscape and highlights key trends related to product segmentation, company formation, revenue, and market share, latest development, and M&A activity. This report also analyses the strategies of leading global companies with a focus on Wired Interface IP portfolios and capabilities, market entry strategies, market positions, and geographic footprints, to better understand these firms' unique position in an accelerating global Wired Interface IP market.

This Insight Report evaluates the key market trends, drivers, and affecting factors shaping the global outlook for Wired Interface IP and breaks down the forecast by Type, by Application, geography, and market size to highlight emerging pockets of opportunity. With a transparent methodology based on hundreds of bottom-up qualitative and quantitative market inputs, this study forecast offers a highly nuanced view of the current state and future trajectory in the global Wired Interface IP.

This report presents a comprehensive overview, market shares, and growth opportunities of Wired Interface IP market by product type, application, key players and key regions and countries.

Segmentation by Type:

PCIe/CXL/UCIe IP

USB/Ethernet/SATA IP

DDR/LPDDR/HBM IP

Others

Segmentation by Data Rate:

Low and Medium-speed Interface IP

High-speed Interface IP

Segmentation by Application:

Data Center and AI Computing

Automotive Electronics

Consumer Electronics

Industrial and IoT

Others

This report also splits the market by region:

Americas

United States

Canada

Mexico

Brazil

APAC

China

Japan

Korea

Southeast Asia

India

Australia

Europe

Germany

France

UK

Italy

Russia

Middle East & Africa

Egypt

South Africa

Israel

Turkey

GCC Countries

The below companies that are profiled have been selected based on inputs gathered from primary experts and analyzing the company's coverage, product portfolio, its market penetration.

Synopsys

Cadence Design Systems

Siemens EDA

Qualcomm

Renesas Electronics

Arasan Chip Systems

Comcores ApS

CAST, Inc.

T2M IP

Analog Bits, Inc.

Silicon Creations

Credo Technology Group Holding Ltd

Mobiveil, Inc.

Kandou

Dolphin Technology

VeriSilicon Microelectronics (Shanghai)

Innosilicon Technology

Brite Semiconductor (Shanghai)

Socionext

Faraday Technology Corporation

M31 Technology Corporation

Rambus Inc.

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