

# Global Wafer Bump Inspection and Measurement System Market Growth 2024-2030

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## Abstracts

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Wafer Bump Inspection and Measurement System is a specialized equipment used in the semiconductor manufacturing process to inspect and measure the quality and accuracy of the bumps or solder balls present on a wafer. These systems utilize advanced imaging, metrology, and analysis techniques to detect defects, measure dimensions, and ensure the overall quality of the wafer bumps. Wafer bumps are crucial for enabling electrical connections between the silicon die and the packaging substrate. The inspection and measurement system ensures that the bumps meet the required specifications, thereby ensuring proper functionality and reliability of the final packaged semiconductor devices.

The global Wafer Bump Inspection and Measurement System market size is projected to grow from US\$ 846 million in 2024 to US\$ 1682 million in 2030; it is expected to grow at a CAGR of 12.1% from 2024 to 2030.

LP Information, Inc. (LPI) ' newest research report, the "Wafer Bump Inspection and Measurement System Industry Forecast" looks at past sales and reviews total world Wafer Bump Inspection and Measurement System sales in 2023, providing a comprehensive analysis by region and market sector of projected Wafer Bump Inspection and Measurement System sales for 2024 through 2030. With Wafer Bump Inspection and Measurement System sales broken down by region, market sector and sub-sector, this report provides a detailed analysis in US\$ millions of the world Wafer Bump Inspection and Measurement System industry.

This Insight Report provides a comprehensive analysis of the global Wafer Bump

Inspection and Measurement System landscape and highlights key trends related to product segmentation, company formation, revenue, and market share, latest development, and M&A activity. This report also analyzes the strategies of leading global companies with a focus on Wafer Bump Inspection and Measurement System portfolios and capabilities, market entry strategies, market positions, and geographic footprints, to better understand these firms' unique position in an accelerating global Wafer Bump Inspection and Measurement System market.

This Insight Report evaluates the key market trends, drivers, and affecting factors shaping the global outlook for Wafer Bump Inspection and Measurement System and breaks down the forecast by Type, by Application, geography, and market size to highlight emerging pockets of opportunity. With a transparent methodology based on hundreds of bottom-up qualitative and quantitative market inputs, this study forecast offers a highly nuanced view of the current state and future trajectory in the global Wafer Bump Inspection and Measurement System.

The industry trend for Wafer Bump Inspection and Measurement Systems is driven by several factors. Firstly, the demand for higher-performance and miniaturized semiconductor devices necessitates finer and more precise bump structures. As a result, manufacturers require advanced inspection and measurement systems to ensure the quality and consistency of these bumps. Secondly, the increasing adoption of advanced packaging technologies, such as flip-chip and wafer-level packaging, further drives the need for accurate inspection and measurement solutions. Moreover, the rapid development of 3D integration techniques and heterogeneous integration in semiconductor manufacturing introduces complex bump structures that require specialized inspection and measurement capabilities. The industry trend is focused on enhancing the speed, accuracy, and automation of these systems to support high-volume production and ensure the quality of wafer bumps for advanced semiconductor devices.

This report presents a comprehensive overview, market shares, and growth opportunities of Wafer Bump Inspection and Measurement System market by product type, application, key manufacturers and key regions and countries.

Segmentation by Type:

200mm Wafer Bump Inspection Device

300mm Wafer Bump Inspection Device

Others

Segmentation by Application:

Convex Detection and Measurement

Wafer Surface Particle Observation and Measurement

Observation and Measurement of Grinding Marks

Others

This report also splits the market by region:

Americas

United States

Canada

Mexico

Brazil

APAC

China

Japan

Korea

Southeast Asia

India

Australia

Europe

Germany

France

UK

Italy

Russia

Middle East & Africa

Egypt

South Africa

Israel

Turkey

GCC Countries

The below companies that are profiled have been selected based on inputs gathered from primary experts and analysing the company's coverage, product portfolio, its market penetration.

Lasertec Korea Corporation

Confovis

Takano

TAKAOKA TOKO

Nordson Corporation

Micro-Epsilon

KLA Corporation

Nidec Advance Technology Corporation

QES Mechatronic Sdn Bhd

Nextec Technologies

Cyber optics

ENGITIST CORPORATION

AK Optics Technology

#### Key Questions Addressed in this Report

What is the 10-year outlook for the global Wafer Bump Inspection and Measurement System market?

What factors are driving Wafer Bump Inspection and Measurement System market growth, globally and by region?

Which technologies are poised for the fastest growth by market and region?

How do Wafer Bump Inspection and Measurement System market opportunities vary by end market size?

How does Wafer Bump Inspection and Measurement System break out by Type, by Application?

## Contents

### 1 SCOPE OF THE REPORT

- 1.1 Market Introduction
- 1.2 Years Considered
- 1.3 Research Objectives
- 1.4 Market Research Methodology
- 1.5 Research Process and Data Source
- 1.6 Economic Indicators
- 1.7 Currency Considered
- 1.8 Market Estimation Caveats

### 2 EXECUTIVE SUMMARY

#### 2.1 World Market Overview

- 2.1.1 Global Wafer Bump Inspection and Measurement System Annual Sales 2019-2030

- 2.1.2 World Current & Future Analysis for Wafer Bump Inspection and Measurement System by Geographic Region, 2019, 2023 & 2030

- 2.1.3 World Current & Future Analysis for Wafer Bump Inspection and Measurement System by Country/Region, 2019, 2023 & 2030

#### 2.2 Wafer Bump Inspection and Measurement System Segment by Type

- 2.2.1 200mm Wafer Bump Inspection Device

- 2.2.2 300mm Wafer Bump Inspection Device

- 2.2.3 Others

#### 2.3 Wafer Bump Inspection and Measurement System Sales by Type

- 2.3.1 Global Wafer Bump Inspection and Measurement System Sales Market Share by Type (2019-2024)

- 2.3.2 Global Wafer Bump Inspection and Measurement System Revenue and Market Share by Type (2019-2024)

- 2.3.3 Global Wafer Bump Inspection and Measurement System Sale Price by Type (2019-2024)

#### 2.4 Wafer Bump Inspection and Measurement System Segment by Application

- 2.4.1 Convex Detection and Measurement

- 2.4.2 Wafer Surface Particle Observation and Measurement

- 2.4.3 Observation and Measurement of Grinding Marks

- 2.4.4 Others

#### 2.5 Wafer Bump Inspection and Measurement System Sales by Application

2.5.1 Global Wafer Bump Inspection and Measurement System Sale Market Share by Application (2019-2024)

2.5.2 Global Wafer Bump Inspection and Measurement System Revenue and Market Share by Application (2019-2024)

2.5.3 Global Wafer Bump Inspection and Measurement System Sale Price by Application (2019-2024)

### **3 GLOBAL BY COMPANY**

3.1 Global Wafer Bump Inspection and Measurement System Breakdown Data by Company

3.1.1 Global Wafer Bump Inspection and Measurement System Annual Sales by Company (2019-2024)

3.1.2 Global Wafer Bump Inspection and Measurement System Sales Market Share by Company (2019-2024)

3.2 Global Wafer Bump Inspection and Measurement System Annual Revenue by Company (2019-2024)

3.2.1 Global Wafer Bump Inspection and Measurement System Revenue by Company (2019-2024)

3.2.2 Global Wafer Bump Inspection and Measurement System Revenue Market Share by Company (2019-2024)

3.3 Global Wafer Bump Inspection and Measurement System Sale Price by Company

3.4 Key Manufacturers Wafer Bump Inspection and Measurement System Producing Area Distribution, Sales Area, Product Type

3.4.1 Key Manufacturers Wafer Bump Inspection and Measurement System Product Location Distribution

3.4.2 Players Wafer Bump Inspection and Measurement System Products Offered

3.5 Market Concentration Rate Analysis

3.5.1 Competition Landscape Analysis

3.5.2 Concentration Ratio (CR3, CR5 and CR10) & (2019-2024)

3.6 New Products and Potential Entrants

3.7 Market M&A Activity & Strategy

### **4 WORLD HISTORIC REVIEW FOR WAFER BUMP INSPECTION AND MEASUREMENT SYSTEM BY GEOGRAPHIC REGION**

4.1 World Historic Wafer Bump Inspection and Measurement System Market Size by Geographic Region (2019-2024)

4.1.1 Global Wafer Bump Inspection and Measurement System Annual Sales by

Geographic Region (2019-2024)

4.1.2 Global Wafer Bump Inspection and Measurement System Annual Revenue by Geographic Region (2019-2024)

4.2 World Historic Wafer Bump Inspection and Measurement System Market Size by Country/Region (2019-2024)

4.2.1 Global Wafer Bump Inspection and Measurement System Annual Sales by Country/Region (2019-2024)

4.2.2 Global Wafer Bump Inspection and Measurement System Annual Revenue by Country/Region (2019-2024)

4.3 Americas Wafer Bump Inspection and Measurement System Sales Growth

4.4 APAC Wafer Bump Inspection and Measurement System Sales Growth

4.5 Europe Wafer Bump Inspection and Measurement System Sales Growth

4.6 Middle East & Africa Wafer Bump Inspection and Measurement System Sales Growth

## **5 AMERICAS**

5.1 Americas Wafer Bump Inspection and Measurement System Sales by Country

5.1.1 Americas Wafer Bump Inspection and Measurement System Sales by Country (2019-2024)

5.1.2 Americas Wafer Bump Inspection and Measurement System Revenue by Country (2019-2024)

5.2 Americas Wafer Bump Inspection and Measurement System Sales by Type (2019-2024)

5.3 Americas Wafer Bump Inspection and Measurement System Sales by Application (2019-2024)

5.4 United States

5.5 Canada

5.6 Mexico

5.7 Brazil

## **6 APAC**

6.1 APAC Wafer Bump Inspection and Measurement System Sales by Region

6.1.1 APAC Wafer Bump Inspection and Measurement System Sales by Region (2019-2024)

6.1.2 APAC Wafer Bump Inspection and Measurement System Revenue by Region (2019-2024)

6.2 APAC Wafer Bump Inspection and Measurement System Sales by Type



(2019-2024)

6.3 APAC Wafer Bump Inspection and Measurement System Sales by Application

(2019-2024)

6.4 China

6.5 Japan

6.6 South Korea

6.7 Southeast Asia

6.8 India

6.9 Australia

6.10 China Taiwan

## **7 EUROPE**

7.1 Europe Wafer Bump Inspection and Measurement System by Country

7.1.1 Europe Wafer Bump Inspection and Measurement System Sales by Country

(2019-2024)

7.1.2 Europe Wafer Bump Inspection and Measurement System Revenue by Country

(2019-2024)

7.2 Europe Wafer Bump Inspection and Measurement System Sales by Type

(2019-2024)

7.3 Europe Wafer Bump Inspection and Measurement System Sales by Application

(2019-2024)

7.4 Germany

7.5 France

7.6 UK

7.7 Italy

7.8 Russia

## **8 MIDDLE EAST & AFRICA**

8.1 Middle East & Africa Wafer Bump Inspection and Measurement System by Country

8.1.1 Middle East & Africa Wafer Bump Inspection and Measurement System Sales by Country (2019-2024)

8.1.2 Middle East & Africa Wafer Bump Inspection and Measurement System Revenue by Country (2019-2024)

8.2 Middle East & Africa Wafer Bump Inspection and Measurement System Sales by Type (2019-2024)

8.3 Middle East & Africa Wafer Bump Inspection and Measurement System Sales by Application (2019-2024)

- 8.4 Egypt
- 8.5 South Africa
- 8.6 Israel
- 8.7 Turkey
- 8.8 GCC Countries

## **9 MARKET DRIVERS, CHALLENGES AND TRENDS**

- 9.1 Market Drivers & Growth Opportunities
- 9.2 Market Challenges & Risks
- 9.3 Industry Trends

## **10 MANUFACTURING COST STRUCTURE ANALYSIS**

- 10.1 Raw Material and Suppliers
- 10.2 Manufacturing Cost Structure Analysis of Wafer Bump Inspection and Measurement System
- 10.3 Manufacturing Process Analysis of Wafer Bump Inspection and Measurement System
- 10.4 Industry Chain Structure of Wafer Bump Inspection and Measurement System

## **11 MARKETING, DISTRIBUTORS AND CUSTOMER**

- 11.1 Sales Channel
  - 11.1.1 Direct Channels
  - 11.1.2 Indirect Channels
- 11.2 Wafer Bump Inspection and Measurement System Distributors
- 11.3 Wafer Bump Inspection and Measurement System Customer

## **12 WORLD FORECAST REVIEW FOR WAFER BUMP INSPECTION AND MEASUREMENT SYSTEM BY GEOGRAPHIC REGION**

- 12.1 Global Wafer Bump Inspection and Measurement System Market Size Forecast by Region
  - 12.1.1 Global Wafer Bump Inspection and Measurement System Forecast by Region (2025-2030)
  - 12.1.2 Global Wafer Bump Inspection and Measurement System Annual Revenue Forecast by Region (2025-2030)
- 12.2 Americas Forecast by Country (2025-2030)

12.3 APAC Forecast by Region (2025-2030)

12.4 Europe Forecast by Country (2025-2030)

12.5 Middle East & Africa Forecast by Country (2025-2030)

12.6 Global Wafer Bump Inspection and Measurement System Forecast by Type (2025-2030)

12.7 Global Wafer Bump Inspection and Measurement System Forecast by Application (2025-2030)

## **13 KEY PLAYERS ANALYSIS**

13.1 Lasertec Korea Corporation

13.1.1 Lasertec Korea Corporation Company Information

13.1.2 Lasertec Korea Corporation Wafer Bump Inspection and Measurement System Product Portfolios and Specifications

13.1.3 Lasertec Korea Corporation Wafer Bump Inspection and Measurement System Sales, Revenue, Price and Gross Margin (2019-2024)

13.1.4 Lasertec Korea Corporation Main Business Overview

13.1.5 Lasertec Korea Corporation Latest Developments

13.2 Confovis

13.2.1 Confovis Company Information

13.2.2 Confovis Wafer Bump Inspection and Measurement System Product Portfolios and Specifications

13.2.3 Confovis Wafer Bump Inspection and Measurement System Sales, Revenue, Price and Gross Margin (2019-2024)

13.2.4 Confovis Main Business Overview

13.2.5 Confovis Latest Developments

13.3 Takano

13.3.1 Takano Company Information

13.3.2 Takano Wafer Bump Inspection and Measurement System Product Portfolios and Specifications

13.3.3 Takano Wafer Bump Inspection and Measurement System Sales, Revenue, Price and Gross Margin (2019-2024)

13.3.4 Takano Main Business Overview

13.3.5 Takano Latest Developments

13.4 TAKAOKA TOKO

13.4.1 TAKAOKA TOKO Company Information

13.4.2 TAKAOKA TOKO Wafer Bump Inspection and Measurement System Product Portfolios and Specifications

13.4.3 TAKAOKA TOKO Wafer Bump Inspection and Measurement System Sales,

## Revenue, Price and Gross Margin (2019-2024)

### 13.4.4 TAKAOKA TOKO Main Business Overview

### 13.4.5 TAKAOKA TOKO Latest Developments

## 13.5 Nordson Corporation

### 13.5.1 Nordson Corporation Company Information

### 13.5.2 Nordson Corporation Wafer Bump Inspection and Measurement System

## Product Portfolios and Specifications

### 13.5.3 Nordson Corporation Wafer Bump Inspection and Measurement System Sales, Revenue, Price and Gross Margin (2019-2024)

### 13.5.4 Nordson Corporation Main Business Overview

### 13.5.5 Nordson Corporation Latest Developments

## 13.6 Micro-Epsilon

### 13.6.1 Micro-Epsilon Company Information

### 13.6.2 Micro-Epsilon Wafer Bump Inspection and Measurement System Product

## Portfolios and Specifications

### 13.6.3 Micro-Epsilon Wafer Bump Inspection and Measurement System Sales, Revenue, Price and Gross Margin (2019-2024)

### 13.6.4 Micro-Epsilon Main Business Overview

### 13.6.5 Micro-Epsilon Latest Developments

## 13.7 KLA Corporation

### 13.7.1 KLA Corporation Company Information

### 13.7.2 KLA Corporation Wafer Bump Inspection and Measurement System Product

## Portfolios and Specifications

### 13.7.3 KLA Corporation Wafer Bump Inspection and Measurement System Sales, Revenue, Price and Gross Margin (2019-2024)

### 13.7.4 KLA Corporation Main Business Overview

### 13.7.5 KLA Corporation Latest Developments

## 13.8 Nidec Advance Technology Corporation

### 13.8.1 Nidec Advance Technology Corporation Company Information

### 13.8.2 Nidec Advance Technology Corporation Wafer Bump Inspection and Measurement System Product Portfolios and Specifications

### 13.8.3 Nidec Advance Technology Corporation Wafer Bump Inspection and Measurement System Sales, Revenue, Price and Gross Margin (2019-2024)

### 13.8.4 Nidec Advance Technology Corporation Main Business Overview

### 13.8.5 Nidec Advance Technology Corporation Latest Developments

## 13.9 QES Mechatronic Sdn Bhd

### 13.9.1 QES Mechatronic Sdn Bhd Company Information

### 13.9.2 QES Mechatronic Sdn Bhd Wafer Bump Inspection and Measurement System Product Portfolios and Specifications

13.9.3 QES Mechatronic Sdn Bhd Wafer Bump Inspection and Measurement System Sales, Revenue, Price and Gross Margin (2019-2024)

13.9.4 QES Mechatronic Sdn Bhd Main Business Overview

13.9.5 QES Mechatronic Sdn Bhd Latest Developments

13.10 Nextec Technologies

13.10.1 Nextec Technologies Company Information

13.10.2 Nextec Technologies Wafer Bump Inspection and Measurement System Product Portfolios and Specifications

13.10.3 Nextec Technologies Wafer Bump Inspection and Measurement System Sales, Revenue, Price and Gross Margin (2019-2024)

13.10.4 Nextec Technologies Main Business Overview

13.10.5 Nextec Technologies Latest Developments

13.11 Cyber optics

13.11.1 Cyber optics Company Information

13.11.2 Cyber optics Wafer Bump Inspection and Measurement System Product Portfolios and Specifications

13.11.3 Cyber optics Wafer Bump Inspection and Measurement System Sales, Revenue, Price and Gross Margin (2019-2024)

13.11.4 Cyber optics Main Business Overview

13.11.5 Cyber optics Latest Developments

13.12 ENGITIST CORPORATION

13.12.1 ENGITIST CORPORATION Company Information

13.12.2 ENGITIST CORPORATION Wafer Bump Inspection and Measurement System Product Portfolios and Specifications

13.12.3 ENGITIST CORPORATION Wafer Bump Inspection and Measurement System Sales, Revenue, Price and Gross Margin (2019-2024)

13.12.4 ENGITIST CORPORATION Main Business Overview

13.12.5 ENGITIST CORPORATION Latest Developments

13.13 AK Optics Technology

13.13.1 AK Optics Technology Company Information

13.13.2 AK Optics Technology Wafer Bump Inspection and Measurement System Product Portfolios and Specifications

13.13.3 AK Optics Technology Wafer Bump Inspection and Measurement System Sales, Revenue, Price and Gross Margin (2019-2024)

13.13.4 AK Optics Technology Main Business Overview

13.13.5 AK Optics Technology Latest Developments

## **14 RESEARCH FINDINGS AND CONCLUSION**



## List Of Tables

### LIST OF TABLES

Table 1. Wafer Bump Inspection and Measurement System Annual Sales CAGR by Geographic Region (2019, 2023 & 2030) & (\$ millions)

Table 2. Wafer Bump Inspection and Measurement System Annual Sales CAGR by Country/Region (2019, 2023 & 2030) & (\$ millions)

Table 3. Major Players of 200mm Wafer Bump Inspection Device

Table 4. Major Players of 300mm Wafer Bump Inspection Device

Table 5. Major Players of Others

Table 6. Global Wafer Bump Inspection and Measurement System Sales by Type (2019-2024) & (Units)

Table 7. Global Wafer Bump Inspection and Measurement System Sales Market Share by Type (2019-2024)

Table 8. Global Wafer Bump Inspection and Measurement System Revenue by Type (2019-2024) & (\$ million)

Table 9. Global Wafer Bump Inspection and Measurement System Revenue Market Share by Type (2019-2024)

Table 10. Global Wafer Bump Inspection and Measurement System Sale Price by Type (2019-2024) & (US\$/Unit)

Table 11. Global Wafer Bump Inspection and Measurement System Sale by Application (2019-2024) & (Units)

Table 12. Global Wafer Bump Inspection and Measurement System Sale Market Share by Application (2019-2024)

Table 13. Global Wafer Bump Inspection and Measurement System Revenue by Application (2019-2024) & (\$ million)

Table 14. Global Wafer Bump Inspection and Measurement System Revenue Market Share by Application (2019-2024)

Table 15. Global Wafer Bump Inspection and Measurement System Sale Price by Application (2019-2024) & (US\$/Unit)

Table 16. Global Wafer Bump Inspection and Measurement System Sales by Company (2019-2024) & (Units)

Table 17. Global Wafer Bump Inspection and Measurement System Sales Market Share by Company (2019-2024)

Table 18. Global Wafer Bump Inspection and Measurement System Revenue by Company (2019-2024) & (\$ millions)

Table 19. Global Wafer Bump Inspection and Measurement System Revenue Market Share by Company (2019-2024)

Table 20. Global Wafer Bump Inspection and Measurement System Sale Price by Company (2019-2024) & (US\$/Unit)

Table 21. Key Manufacturers Wafer Bump Inspection and Measurement System Producing Area Distribution and Sales Area

Table 22. Players Wafer Bump Inspection and Measurement System Products Offered

Table 23. Wafer Bump Inspection and Measurement System Concentration Ratio (CR3, CR5 and CR10) & (2019-2024)

Table 24. New Products and Potential Entrants

Table 25. Market M&A Activity & Strategy

Table 26. Global Wafer Bump Inspection and Measurement System Sales by Geographic Region (2019-2024) & (Units)

Table 27. Global Wafer Bump Inspection and Measurement System Sales Market Share Geographic Region (2019-2024)

Table 28. Global Wafer Bump Inspection and Measurement System Revenue by Geographic Region (2019-2024) & (\$ millions)

Table 29. Global Wafer Bump Inspection and Measurement System Revenue Market Share by Geographic Region (2019-2024)

Table 30. Global Wafer Bump Inspection and Measurement System Sales by Country/Region (2019-2024) & (Units)

Table 31. Global Wafer Bump Inspection and Measurement System Sales Market Share by Country/Region (2019-2024)

Table 32. Global Wafer Bump Inspection and Measurement System Revenue by Country/Region (2019-2024) & (\$ millions)

Table 33. Global Wafer Bump Inspection and Measurement System Revenue Market Share by Country/Region (2019-2024)

Table 34. Americas Wafer Bump Inspection and Measurement System Sales by Country (2019-2024) & (Units)

Table 35. Americas Wafer Bump Inspection and Measurement System Sales Market Share by Country (2019-2024)

Table 36. Americas Wafer Bump Inspection and Measurement System Revenue by Country (2019-2024) & (\$ millions)

Table 37. Americas Wafer Bump Inspection and Measurement System Sales by Type (2019-2024) & (Units)

Table 38. Americas Wafer Bump Inspection and Measurement System Sales by Application (2019-2024) & (Units)

Table 39. APAC Wafer Bump Inspection and Measurement System Sales by Region (2019-2024) & (Units)

Table 40. APAC Wafer Bump Inspection and Measurement System Sales Market Share by Region (2019-2024)



Table 41. APAC Wafer Bump Inspection and Measurement System Revenue by Region (2019-2024) & (\$ millions)

Table 42. APAC Wafer Bump Inspection and Measurement System Sales by Type (2019-2024) & (Units)

Table 43. APAC Wafer Bump Inspection and Measurement System Sales by Application (2019-2024) & (Units)

Table 44. Europe Wafer Bump Inspection and Measurement System Sales by Country (2019-2024) & (Units)

Table 45. Europe Wafer Bump Inspection and Measurement System Revenue by Country (2019-2024) & (\$ millions)

Table 46. Europe Wafer Bump Inspection and Measurement System Sales by Type (2019-2024) & (Units)

Table 47. Europe Wafer Bump Inspection and Measurement System Sales by Application (2019-2024) & (Units)

Table 48. Middle East & Africa Wafer Bump Inspection and Measurement System Sales by Country (2019-2024) & (Units)

Table 49. Middle East & Africa Wafer Bump Inspection and Measurement System Revenue Market Share by Country (2019-2024)

Table 50. Middle East & Africa Wafer Bump Inspection and Measurement System Sales by Type (2019-2024) & (Units)

Table 51. Middle East & Africa Wafer Bump Inspection and Measurement System Sales by Application (2019-2024) & (Units)

Table 52. Key Market Drivers & Growth Opportunities of Wafer Bump Inspection and Measurement System

Table 53. Key Market Challenges & Risks of Wafer Bump Inspection and Measurement System

Table 54. Key Industry Trends of Wafer Bump Inspection and Measurement System

Table 55. Wafer Bump Inspection and Measurement System Raw Material

Table 56. Key Suppliers of Raw Materials

Table 57. Wafer Bump Inspection and Measurement System Distributors List

Table 58. Wafer Bump Inspection and Measurement System Customer List

Table 59. Global Wafer Bump Inspection and Measurement System Sales Forecast by Region (2025-2030) & (Units)

Table 60. Global Wafer Bump Inspection and Measurement System Revenue Forecast by Region (2025-2030) & (\$ millions)

Table 61. Americas Wafer Bump Inspection and Measurement System Sales Forecast by Country (2025-2030) & (Units)

Table 62. Americas Wafer Bump Inspection and Measurement System Annual Revenue Forecast by Country (2025-2030) & (\$ millions)

Table 63. APAC Wafer Bump Inspection and Measurement System Sales Forecast by Region (2025-2030) & (Units)

Table 64. APAC Wafer Bump Inspection and Measurement System Annual Revenue Forecast by Region (2025-2030) & (\$ millions)

Table 65. Europe Wafer Bump Inspection and Measurement System Sales Forecast by Country (2025-2030) & (Units)

Table 66. Europe Wafer Bump Inspection and Measurement System Revenue Forecast by Country (2025-2030) & (\$ millions)

Table 67. Middle East & Africa Wafer Bump Inspection and Measurement System Sales Forecast by Country (2025-2030) & (Units)

Table 68. Middle East & Africa Wafer Bump Inspection and Measurement System Revenue Forecast by Country (2025-2030) & (\$ millions)

Table 69. Global Wafer Bump Inspection and Measurement System Sales Forecast by Type (2025-2030) & (Units)

Table 70. Global Wafer Bump Inspection and Measurement System Revenue Forecast by Type (2025-2030) & (\$ millions)

Table 71. Global Wafer Bump Inspection and Measurement System Sales Forecast by Application (2025-2030) & (Units)

Table 72. Global Wafer Bump Inspection and Measurement System Revenue Forecast by Application (2025-2030) & (\$ millions)

Table 73. Lasertec Korea Corporation Basic Information, Wafer Bump Inspection and Measurement System Manufacturing Base, Sales Area and Its Competitors

Table 74. Lasertec Korea Corporation Wafer Bump Inspection and Measurement System Product Portfolios and Specifications

Table 75. Lasertec Korea Corporation Wafer Bump Inspection and Measurement System Sales (Units), Revenue (\$ Million), Price (US\$/Unit) and Gross Margin (2019-2024)

Table 76. Lasertec Korea Corporation Main Business

Table 77. Lasertec Korea Corporation Latest Developments

Table 78. Confovis Basic Information, Wafer Bump Inspection and Measurement System Manufacturing Base, Sales Area and Its Competitors

Table 79. Confovis Wafer Bump Inspection and Measurement System Product Portfolios and Specifications

Table 80. Confovis Wafer Bump Inspection and Measurement System Sales (Units), Revenue (\$ Million), Price (US\$/Unit) and Gross Margin (2019-2024)

Table 81. Confovis Main Business

Table 82. Confovis Latest Developments

Table 83. Takano Basic Information, Wafer Bump Inspection and Measurement System Manufacturing Base, Sales Area and Its Competitors

Table 84. Takano Wafer Bump Inspection and Measurement System Product Portfolios and Specifications

Table 85. Takano Wafer Bump Inspection and Measurement System Sales (Units), Revenue (\$ Million), Price (US\$/Unit) and Gross Margin (2019-2024)

Table 86. Takano Main Business

Table 87. Takano Latest Developments

Table 88. TAKAOKA TOKO Basic Information, Wafer Bump Inspection and Measurement System Manufacturing Base, Sales Area and Its Competitors

Table 89. TAKAOKA TOKO Wafer Bump Inspection and Measurement System Product Portfolios and Specifications

Table 90. TAKAOKA TOKO Wafer Bump Inspection and Measurement System Sales (Units), Revenue (\$ Million), Price (US\$/Unit) and Gross Margin (2019-2024)

Table 91. TAKAOKA TOKO Main Business

Table 92. TAKAOKA TOKO Latest Developments

Table 93. Nordson Corporation Basic Information, Wafer Bump Inspection and Measurement System Manufacturing Base, Sales Area and Its Competitors

Table 94. Nordson Corporation Wafer Bump Inspection and Measurement System Product Portfolios and Specifications

Table 95. Nordson Corporation Wafer Bump Inspection and Measurement System Sales (Units), Revenue (\$ Million), Price (US\$/Unit) and Gross Margin (2019-2024)

Table 96. Nordson Corporation Main Business

Table 97. Nordson Corporation Latest Developments

Table 98. Micro-Epsilon Basic Information, Wafer Bump Inspection and Measurement System Manufacturing Base, Sales Area and Its Competitors

Table 99. Micro-Epsilon Wafer Bump Inspection and Measurement System Product Portfolios and Specifications

Table 100. Micro-Epsilon Wafer Bump Inspection and Measurement System Sales (Units), Revenue (\$ Million), Price (US\$/Unit) and Gross Margin (2019-2024)

Table 101. Micro-Epsilon Main Business

Table 102. Micro-Epsilon Latest Developments

Table 103. KLA Corporation Basic Information, Wafer Bump Inspection and Measurement System Manufacturing Base, Sales Area and Its Competitors

Table 104. KLA Corporation Wafer Bump Inspection and Measurement System Product Portfolios and Specifications

Table 105. KLA Corporation Wafer Bump Inspection and Measurement System Sales (Units), Revenue (\$ Million), Price (US\$/Unit) and Gross Margin (2019-2024)

Table 106. KLA Corporation Main Business

Table 107. KLA Corporation Latest Developments

Table 108. Nidec Advance Technology Corporation Basic Information, Wafer Bump

## Inspection and Measurement System Manufacturing Base, Sales Area and Its Competitors

Table 109. Nidec Advance Technology Corporation Wafer Bump Inspection and Measurement System Product Portfolios and Specifications

Table 110. Nidec Advance Technology Corporation Wafer Bump Inspection and Measurement System Sales (Units), Revenue (\$ Million), Price (US\$/Unit) and Gross Margin (2019-2024)

Table 111. Nidec Advance Technology Corporation Main Business

Table 112. Nidec Advance Technology Corporation Latest Developments

Table 113. QES Mechatronic Sdn Bhd Basic Information, Wafer Bump Inspection and Measurement System Manufacturing Base, Sales Area and Its Competitors

Table 114. QES Mechatronic Sdn Bhd Wafer Bump Inspection and Measurement System Product Portfolios and Specifications

Table 115. QES Mechatronic Sdn Bhd Wafer Bump Inspection and Measurement System Sales (Units), Revenue (\$ Million), Price (US\$/Unit) and Gross Margin (2019-2024)

Table 116. QES Mechatronic Sdn Bhd Main Business

Table 117. QES Mechatronic Sdn Bhd Latest Developments

Table 118. Nextec Technologies Basic Information, Wafer Bump Inspection and Measurement System Manufacturing Base, Sales Area and Its Competitors

Table 119. Nextec Technologies Wafer Bump Inspection and Measurement System Product Portfolios and Specifications

Table 120. Nextec Technologies Wafer Bump Inspection and Measurement System Sales (Units), Revenue (\$ Million), Price (US\$/Unit) and Gross Margin (2019-2024)

Table 121. Nextec Technologies Main Business

Table 122. Nextec Technologies Latest Developments

Table 123. Cyber optics Basic Information, Wafer Bump Inspection and Measurement System Manufacturing Base, Sales Area and Its Competitors

Table 124. Cyber optics Wafer Bump Inspection and Measurement System Product Portfolios and Specifications

Table 125. Cyber optics Wafer Bump Inspection and Measurement System Sales (Units), Revenue (\$ Million), Price (US\$/Unit) and Gross Margin (2019-2024)

Table 126. Cyber optics Main Business

Table 127. Cyber optics Latest Developments

Table 128. ENGITIST CORPORATION Basic Information, Wafer Bump Inspection and Measurement System Manufacturing Base, Sales Area and Its Competitors

Table 129. ENGITIST CORPORATION Wafer Bump Inspection and Measurement System Product Portfolios and Specifications

Table 130. ENGITIST CORPORATION Wafer Bump Inspection and Measurement

System Sales (Units), Revenue (\$ Million), Price (US\$/Unit) and Gross Margin (2019-2024)

Table 131. ENGITIST CORPORATION Main Business

Table 132. ENGITIST CORPORATION Latest Developments

Table 133. AK Optics Technology Basic Information, Wafer Bump Inspection and Measurement System Manufacturing Base, Sales Area and Its Competitors

Table 134. AK Optics Technology Wafer Bump Inspection and Measurement System Product Portfolios and Specifications

Table 135. AK Optics Technology Wafer Bump Inspection and Measurement System Sales (Units), Revenue (\$ Million), Price (US\$/Unit) and Gross Margin (2019-2024)

Table 136. AK Optics Technology Main Business

Table 137. AK Optics Technology Latest Developments

## List Of Figures

### LIST OF FIGURES

- Figure 1. Picture of Wafer Bump Inspection and Measurement System
- Figure 2. Wafer Bump Inspection and Measurement System Report Years Considered
- Figure 3. Research Objectives
- Figure 4. Research Methodology
- Figure 5. Research Process and Data Source
- Figure 6. Global Wafer Bump Inspection and Measurement System Sales Growth Rate 2019-2030 (Units)
- Figure 7. Global Wafer Bump Inspection and Measurement System Revenue Growth Rate 2019-2030 (\$ millions)
- Figure 8. Wafer Bump Inspection and Measurement System Sales by Geographic Region (2019, 2023 & 2030) & (\$ millions)
- Figure 9. Wafer Bump Inspection and Measurement System Sales Market Share by Country/Region (2023)
- Figure 10. Wafer Bump Inspection and Measurement System Sales Market Share by Country/Region (2019, 2023 & 2030)
- Figure 11. Product Picture of 200mm Wafer Bump Inspection Device
- Figure 12. Product Picture of 300mm Wafer Bump Inspection Device
- Figure 13. Product Picture of Others
- Figure 14. Global Wafer Bump Inspection and Measurement System Sales Market Share by Type in 2023
- Figure 15. Global Wafer Bump Inspection and Measurement System Revenue Market Share by Type (2019-2024)
- Figure 16. Wafer Bump Inspection and Measurement System Consumed in Convex Detection and Measurement
- Figure 17. Global Wafer Bump Inspection and Measurement System Market: Convex Detection and Measurement (2019-2024) & (Units)
- Figure 18. Wafer Bump Inspection and Measurement System Consumed in Wafer Surface Particle Observation and Measurement
- Figure 19. Global Wafer Bump Inspection and Measurement System Market: Wafer Surface Particle Observation and Measurement (2019-2024) & (Units)
- Figure 20. Wafer Bump Inspection and Measurement System Consumed in Observation and Measurement of Grinding Marks
- Figure 21. Global Wafer Bump Inspection and Measurement System Market: Observation and Measurement of Grinding Marks (2019-2024) & (Units)
- Figure 22. Wafer Bump Inspection and Measurement System Consumed in Others

Figure 23. Global Wafer Bump Inspection and Measurement System Market: Others (2019-2024) & (Units)

Figure 24. Global Wafer Bump Inspection and Measurement System Sale Market Share by Application (2023)

Figure 25. Global Wafer Bump Inspection and Measurement System Revenue Market Share by Application in 2023

Figure 26. Wafer Bump Inspection and Measurement System Sales by Company in 2023 (Units)

Figure 27. Global Wafer Bump Inspection and Measurement System Sales Market Share by Company in 2023

Figure 28. Wafer Bump Inspection and Measurement System Revenue by Company in 2023 (\$ millions)

Figure 29. Global Wafer Bump Inspection and Measurement System Revenue Market Share by Company in 2023

Figure 30. Global Wafer Bump Inspection and Measurement System Sales Market Share by Geographic Region (2019-2024)

Figure 31. Global Wafer Bump Inspection and Measurement System Revenue Market Share by Geographic Region in 2023

Figure 32. Americas Wafer Bump Inspection and Measurement System Sales 2019-2024 (Units)

Figure 33. Americas Wafer Bump Inspection and Measurement System Revenue 2019-2024 (\$ millions)

Figure 34. APAC Wafer Bump Inspection and Measurement System Sales 2019-2024 (Units)

Figure 35. APAC Wafer Bump Inspection and Measurement System Revenue 2019-2024 (\$ millions)

Figure 36. Europe Wafer Bump Inspection and Measurement System Sales 2019-2024 (Units)

Figure 37. Europe Wafer Bump Inspection and Measurement System Revenue 2019-2024 (\$ millions)

Figure 38. Middle East & Africa Wafer Bump Inspection and Measurement System Sales 2019-2024 (Units)

Figure 39. Middle East & Africa Wafer Bump Inspection and Measurement System Revenue 2019-2024 (\$ millions)

Figure 40. Americas Wafer Bump Inspection and Measurement System Sales Market Share by Country in 2023

Figure 41. Americas Wafer Bump Inspection and Measurement System Revenue Market Share by Country (2019-2024)

Figure 42. Americas Wafer Bump Inspection and Measurement System Sales Market

Share by Type (2019-2024)

Figure 43. Americas Wafer Bump Inspection and Measurement System Sales Market Share by Application (2019-2024)

Figure 44. United States Wafer Bump Inspection and Measurement System Revenue Growth 2019-2024 (\$ millions)

Figure 45. Canada Wafer Bump Inspection and Measurement System Revenue Growth 2019-2024 (\$ millions)

Figure 46. Mexico Wafer Bump Inspection and Measurement System Revenue Growth 2019-2024 (\$ millions)

Figure 47. Brazil Wafer Bump Inspection and Measurement System Revenue Growth 2019-2024 (\$ millions)

Figure 48. APAC Wafer Bump Inspection and Measurement System Sales Market Share by Region in 2023

Figure 49. APAC Wafer Bump Inspection and Measurement System Revenue Market Share by Region (2019-2024)

Figure 50. APAC Wafer Bump Inspection and Measurement System Sales Market Share by Type (2019-2024)

Figure 51. APAC Wafer Bump Inspection and Measurement System Sales Market Share by Application (2019-2024)

Figure 52. China Wafer Bump Inspection and Measurement System Revenue Growth 2019-2024 (\$ millions)

Figure 53. Japan Wafer Bump Inspection and Measurement System Revenue Growth 2019-2024 (\$ millions)

Figure 54. South Korea Wafer Bump Inspection and Measurement System Revenue Growth 2019-2024 (\$ millions)

Figure 55. Southeast Asia Wafer Bump Inspection and Measurement System Revenue Growth 2019-2024 (\$ millions)

Figure 56. India Wafer Bump Inspection and Measurement System Revenue Growth 2019-2024 (\$ millions)

Figure 57. Australia Wafer Bump Inspection and Measurement System Revenue Growth 2019-2024 (\$ millions)

Figure 58. China Taiwan Wafer Bump Inspection and Measurement System Revenue Growth 2019-2024 (\$ millions)

Figure 59. Europe Wafer Bump Inspection and Measurement System Sales Market Share by Country in 2023

Figure 60. Europe Wafer Bump Inspection and Measurement System Revenue Market Share by Country (2019-2024)

Figure 61. Europe Wafer Bump Inspection and Measurement System Sales Market Share by Type (2019-2024)



Figure 62. Europe Wafer Bump Inspection and Measurement System Sales Market Share by Application (2019-2024)

Figure 63. Germany Wafer Bump Inspection and Measurement System Revenue Growth 2019-2024 (\$ millions)

Figure 64. France Wafer Bump Inspection and Measurement System Revenue Growth 2019-2024 (\$ millions)

Figure 65. UK Wafer Bump Inspection and Measurement System Revenue Growth 2019-2024 (\$ millions)

Figure 66. Italy Wafer Bump Inspection and Measurement System Revenue Growth 2019-2024 (\$ millions)

Figure 67. Russia Wafer Bump Inspection and Measurement System Revenue Growth 2019-2024 (\$ millions)

Figure 68. Middle East & Africa Wafer Bump Inspection and Measurement System Sales Market Share by Country (2019-2024)

Figure 69. Middle East & Africa Wafer Bump Inspection and Measurement System Sales Market Share by Type (2019-2024)

Figure 70. Middle East & Africa Wafer Bump Inspection and Measurement System Sales Market Share by Application (2019-2024)

Figure 71. Egypt Wafer Bump Inspection and Measurement System Revenue Growth 2019-2024 (\$ millions)

Figure 72. South Africa Wafer Bump Inspection and Measurement System Revenue Growth 2019-2024 (\$ millions)

Figure 73. Israel Wafer Bump Inspection and Measurement System Revenue Growth 2019-2024 (\$ millions)

Figure 74. Turkey Wafer Bump Inspection and Measurement System Revenue Growth 2019-2024 (\$ millions)

Figure 75. GCC Countries Wafer Bump Inspection and Measurement System Revenue Growth 2019-2024 (\$ millions)

Figure 76. Manufacturing Cost Structure Analysis of Wafer Bump Inspection and Measurement System in 2023

Figure 77. Manufacturing Process Analysis of Wafer Bump Inspection and Measurement System

Figure 78. Industry Chain Structure of Wafer Bump Inspection and Measurement System

Figure 79. Channels of Distribution

Figure 80. Global Wafer Bump Inspection and Measurement System Sales Market Forecast by Region (2025-2030)

Figure 81. Global Wafer Bump Inspection and Measurement System Revenue Market Share Forecast by Region (2025-2030)

Figure 82. Global Wafer Bump Inspection and Measurement System Sales Market Share Forecast by Type (2025-2030)

Figure 83. Global Wafer Bump Inspection and Measurement System Revenue Market Share Forecast by Type (2025-2030)

Figure 84. Global Wafer Bump Inspection and Measurement System Sales Market Share Forecast by Application (2025-2030)

Figure 85. Global Wafer Bump Inspection and Measurement System Revenue Market Share Forecast by Application (2025-2030)

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