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# Global Translational Chip Testing and Sorting Machine Market Growth 2026-2032

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## Abstracts

The global Translational Chip Testing and Sorting Machine market size is predicted to grow from US$ 1213 million in 2025 to US$ 2498 million in 2032; it is expected to grow at a CAGR of 11.1% from 2026 to 2032.

Translational chip testing and sorting machine usually refers to an automatic machine with a linear translation conveying structure as the core, which clamps packaged integrated circuits one by one or in rows on a carrier, jig or pallet, and is driven by a servo or linear motor to move along one or more linear tracks, and then goes through loading, preheating or temperature adjustment, electrical performance and functional testing, visual/appearance inspection, and graded unloading. Compared with gravity-type and turret-type sorters, automated test sorting equipment has more stable package attitude control, better adaptability to large-size packages and high-pin-count devices, and is easy to arrange multiple test positions and multiple processes. It is suitable for analog/digital-analog hybrid chips, power management ICs, power devices, small batches of multi-variety chips, and other medium-to-high-speed scenarios that require high test conditions and action flexibility. In 2024, global Translational Chip Testing and Sorting Machine production reached approximately 9,392 units, with an average global market price of around K US$ 117.64 per unit.

The annual production capacity of translational chip testing and sorting machines is 13,000 units. In terms of gross profit margin, mid-range general-purpose models usually range from 25% to 35%, and high-end platform models can reach 35% to 45%. The upstream mainly includes precision mechanical parts and sheet metal parts, linear motors and servo systems, motion control cards, industrial PCs and PLCs, power supplies and I/O modules, ATE test machine interface boards and test sockets, vision and sensors, temperature control and constant temperature chambers, pneumatic and

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vacuum components, as well as supporting processing such as cables, cabinets and spraying. The downstream is connected to IDM and OSAT packaging and testing plants, and is divided into logic IC, analog IC, power devices, memory, Different product lines, such as automotive and industrial chips, are deployed in the mid-test, final test and reliability screening sections, and serve end industries such as consumer electronics, automotive electronics, industrial power supplies, new energy and data centers through these customers. From a cost structure perspective, the material BOM (mechanism, servo and control, electrical, temperature control, fixtures and fixtures, etc.) generally accounts for 55% to 70% of the total machine cost, and assembly and joint debugging, on-site installation acceptance and training account for about 10% to 15%.

On the one hand, translational chip testing and sorting machines are evolving in the direction of "multi-temperature zones + high parallelism". More and more models support three-temperature (low temperature/normal temperature/high temperature) or at least high-temperature test chambers, as well as multi-socket parallel testing to meet the requirements for temperature stress and long-term stress screening of automotive-grade and industrial-grade chips. ; On the other hand, as the number of SKUs in packaging and testing plants increases and order batches become smaller, the translational platform is being strengthened as a "highly flexible Handler" because its tracks and fixtures are easy to change and program switching is convenient, covering a variety of packages and material types through quickly replaceable fixtures and universal carrier designs. At the same time, the equipment is more deeply integrated with ATE testing machines, MES systems, barcodes, and yield analysis platforms, requiring translation sorters to not only complete physical handling and test interfaces, but also undertake functions such as data collection, binning rule management, and traceability coding. Generally speaking, it has given way to turret sorters in the field of ultra-high-speed small packages. However, in the testing of power devices, automotive chips, high-end analog ICs, and multi-variety mixed lines, it continues to increase its volume and upgrade towards mid-to-high-end platforms and intelligence due to its flexible layout, large expansion space, and easy connection to form an entire automated test production line.

LP Information, Inc. (LPI) ' newest research report, the ?Translational Chip Testing and Sorting Machine Industry Forecast? looks at past sales and reviews total world Translational Chip Testing and Sorting Machine sales in 2025, providing a comprehensive analysis by region and market sector of projected Translational Chip Testing and Sorting Machine sales for 2026 through 2032. With Translational Chip Testing and Sorting Machine sales broken down by region, market sector and sub-

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sector, this report provides a detailed analysis in US$ millions of the world Translational Chip Testing and Sorting Machine industry.

This Insight Report provides a comprehensive analysis of the global Translational Chip Testing and Sorting Machine landscape and highlights key trends related to product segmentation, company formation, revenue, and market share, latest development, and M&A activity. This report also analyzes the strategies of leading global companies with a focus on Translational Chip Testing and Sorting Machine portfolios and capabilities, market entry strategies, market positions, and geographic footprints, to better understand these firms? unique position in an accelerating global Translational Chip Testing and Sorting Machine market.

This Insight Report evaluates the key market trends, drivers, and affecting factors shaping the global outlook for Translational Chip Testing and Sorting Machine and breaks down the forecast by Type, by Application, geography, and market size to highlight emerging pockets of opportunity. With a transparent methodology based on hundreds of bottom-up qualitative and quantitative market inputs, this study forecast offers a highly nuanced view of the current state and future trajectory in the global Translational Chip Testing and Sorting Machine.

This report presents a comprehensive overview, market shares, and growth opportunities of Translational Chip Testing and Sorting Machine market by product type, application, key manufacturers and key regions and countries.

Segmentation by Type:

Ambient/High-temp Pick and Place Handler

Tri-temp Pick and Place Handler

Segmentation by Automation:

Fully Automatic Test Handler

Semi-Automatic Test Handler

Segmentation by IC Type:

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Universal Chip Test Handler

Special Chip Test Handler

Segmentation by Application:

IDMs

OSATs

This report also splits the market by region:

Americas

United States

Canada

Mexico

Brazil

APAC

China

Japan

Korea

Southeast Asia

India

Australia

Europe

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Germany

France

UK

Italy

Russia

Middle East & Africa

Egypt

South Africa

Israel

Turkey

GCC Countries

The below companies that are profiled have been selected based on inputs gathered from primary experts and analysing the company's coverage, product portfolio, its market penetration.

Cohu, Inc. (Xcerra & MCT)

Changchuan Technology

Advantest

Hon Precision

Techwing

Tianjin JHT Design

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Kanematsu (Epson)

Boston Semi Equipment

Chroma ATE

EXIS TECH

SRM Integration

Shanghai Yingshuo

Ueno Seiki

YoungTek Electronics Corp (YTEC)

SYNAX

Pentamaster

ATECO

MIRAE

SEMES

JT Corp

Genesem

Fuzhou Palide

Shanghai Cascol

Shenzhen Biaopu Semiconductor

M?hlbauer

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MIT Semiconductor

Y.A.C. MECHATRONICS

HappyJapan

Key Questions Addressed in this Report
What is the 10-year outlook for the global Translational Chip Testing and Sorting Machine market?
What factors are driving Translational Chip Testing and Sorting Machine market growth, globally and by region?
Which technologies are poised for the fastest growth by market and region?
How do Translational Chip Testing and Sorting Machine market opportunities vary by end market size?
How does Translational Chip Testing and Sorting Machine break out by Type, by Application?

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