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Global SPI Interface I/O Expander Market Growth 2026-2032

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Abstracts

The global SPI Interface I/O Expander market size is predicted to grow from US$ 45 million in 2025 to US$ 71.88 million in 2032; it is expected to grow at a CAGR of 7.1% from 2026 to 2032.

SPI interface I/O expanders are integrated circuits that expand the number of general-purpose input/output ports (GPIOs) of a microcontroller or processor through a serial peripheral interface. They typically provide multiple configurable GPIO ports and feature interrupt, cascading, and level compatibility functions to save master control pins and simplify wiring and software drivers. Global shipments in 2024 were approximately 120 million units, with an average unit price of approximately US$0.35 per unit. A typical single-line annual capacity is approximately 25 million units. The upstream of the industry mainly consists of semiconductor design and IP licensing, wafer foundry, packaging and testing, passive components, and power management chip supply. Downstream customers and applications cover consumer electronics, IoT terminals, smart homes, and industrial control. The industry encompasses manufacturing, automotive electronics, medical equipment, network communication equipment and instruments, etc., with an average gross profit margin of approximately 42%. In terms of product cost structure, silicon wafers and wafer manufacturing account for approximately 50%, packaging and testing approximately 18%, R&D and IP licensing approximately 12%, quality testing and reliability verification approximately 5%, sales and marketing approximately 8%, and logistics and management approximately 7%. Product costs can be categorized by parameters, such as operating voltage (e.g., 1.8V, 3.3V, and 5V), GPIO port count (e.g., 8-channel, 16-channel, 32-channel and above), interface speed (e.g., low-speed control, medium-speed data, and high-speed real-time), and functional characteristics (e.g., interrupt-enabled, cascading/daisy-chain supported, level-flipping/push-pull output). The product is categorized by packaging and

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temperature rating, such as small thin packages, industrial-grade high-temperature extended types, and automotive-grade AEC-certified types. Demand analysis shows that the downstream demand list includes GPIO expansion modules that save main control resources, interface boards supporting multiple sensors and buttons, low-power wake-up and interrupt management solutions, integrated level and power management solutions, and multi-device daisy-chain communication and isolation solutions. The downstream customer list covers smartphone and wearable device manufacturers, smart home and security equipment manufacturers, IoT module and gateway manufacturers, industrial automation and PLC suppliers, Tier 1 automotive electronics suppliers, medical device manufacturers, network equipment and storage manufacturers, and test and measurement equipment manufacturers. In terms of business opportunities, policy drivers are reflected in various countries' commitment to semiconductor self-sufficiency. Controllable and localized supply chain support promotes local IC design and packaging demand. Technological innovation drives demand, including co-design with low-power microcontrollers, integrated power and signal conditioning, SerDes interfaces supporting higher SPI frequencies and lower latency, and the integration of security and programmable logic into expanders to meet edge computing needs. Changing consumer demands are reflected in end devices' need for more sensors, richer interactive interfaces, and lower power consumption experiences. Taking these trends into account, highly integrated, low-power, multi-channel I/O expanders, high-reliability models for automotive and industrial applications, localized packaging and rapid delivery capabilities, and reference designs and modular solutions that integrate hardware and software will become the main market opportunities.

United States market for SPI Interface I/O Expander is estimated to increase from US$ million in 2025 to US$ million by 2032, at a CAGR of % from 2026 through 2032.

China market for SPI Interface I/O Expander is estimated to increase from US$ million in 2025 to US$ million by 2032, at a CAGR of % from 2026 through 2032.

Europe market for SPI Interface I/O Expander is estimated to increase from US$ million in 2025 to US$ million by 2032, at a CAGR of % from 2026 through 2032.

Global key SPI Interface I/O Expander players cover ADI, TI, NXP, Microchip, Infineon, etc. In terms of revenue, the global two largest companies occupied for a share nearly % in 2025.

Report Scope

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LP Information, Inc. (LPI) ' newest research report, the “SPI Interface I/O Expander Industry Forecast” looks at past sales and reviews total world SPI Interface I/O Expander sales in 2025, providing a comprehensive analysis by region and market sector of projected SPI Interface I/O Expander sales for 2026 through 2032. With SPI Interface I/O Expander sales broken down by region, market sector and sub-sector, this report provides a detailed analysis in US$ millions of the world SPI Interface I/O Expander industry.

This Insight Report provides a comprehensive analysis of the global SPI Interface I/O Expander landscape and highlights key trends related to product segmentation, company formation, revenue, and market share, latest development, and M&A activity. This report also analyzes the strategies of leading global companies with a focus on SPI Interface I/O Expander portfolios and capabilities, market entry strategies, market positions, and geographic footprints, to better understand these firms’ unique position in an accelerating global SPI Interface I/O Expander market.

This Insight Report evaluates the key market trends, drivers, and affecting factors shaping the global outlook for SPI Interface I/O Expander and breaks down the forecast by Type, by Application, geography, and market size to highlight emerging pockets of opportunity. With a transparent methodology based on hundreds of bottom-up qualitative and quantitative market inputs, this study forecast offers a highly nuanced view of the current state and future trajectory in the global SPI Interface I/O Expander.

This report presents a comprehensive overview, market shares, and growth opportunities of SPI Interface I/O Expander market by product type, application, key manufacturers and key regions and countries.

Market Segmentation

Segmentation by Type:

4-Channel

8-Channel

16-Channel

Other

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Segmentation by bit:

20-Bit

40-Bit

60-Bit

Segmentation by Power Consumption:

Power Consumption: <2.5 ?a

Power Consumption: ?2.5 ?a

Segmentation by Application:

Servers And Routers

Automotive

Surveying

Consumer Electronics

Telecommunications Equipment

Other

This report also splits the market by region:

Americas

United States

Canada

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- Mexico
- Brazil

APAC

- China
- Japan
- Korea
- Southeast Asia
- India
- Australia

Europe

- Germany
- France
- UK
- Italy
- Russia

Middle East & Africa

- Egypt
- South Africa
- Israel

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Turkey

GCC Countries

The below companies that are profiled have been selected based on inputs gathered from primary experts and analysing the company's coverage, product portfolio, its market penetration.

ADI

TI

NXP

Microchip

Infineon

Semtech

Nanjing Qinheng Microelectronics

Key Questions Addressed in this Report

What is the 10-year outlook for the global SPI Interface I/O Expander market?

What factors are driving SPI Interface I/O Expander market growth, globally and by region?

Which technologies are poised for the fastest growth by market and region?

How do SPI Interface I/O Expander market opportunities vary by end market size?

How does SPI Interface I/O Expander break out by Type, by Application?

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