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Global Smart Metering Chip Market Growth 2026-2032

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Abstracts

The global Smart Metering Chip market size is predicted to grow from US$ 683 million in 2025 to US$ 1093 million in 2032; it is expected to grow at a CAGR of 6.9% from 2026 to 2032.

Smart Metering Chip refers to specialized semiconductor devices used in smart electricity meters and professional electrical metering terminals to acquire voltage and current signals and perform billing-grade or high-accuracy energy measurement. The study focuses on semiconductor devices that directly participate in the metrology chain, principally Metering AFE/ASIC, Metrology MCU and highly integrated Metering SoC architectures. Typical devices combine precision analog front ends, programmable-gain amplifiers, high-resolution Sigma-Delta ADCs, low-drift references, digital metrology engines and, depending on integration level, MCU/CPU, RTC, memory, security and display-control functions. Key measurement functions include active, reactive and apparent energy, RMS voltage and current, power factor, frequency, tamper monitoring and, for higher-performance devices, power-quality analysis. Current commercial platforms commonly employ high-resolution conversion architectures, with 24-bit Sigma-Delta ADCs widely used in precision metrology designs and support for shunt, current-transformer and Rogowski-coil sensing depending on the device. The research scope centers on utility smart electricity metering while also covering professional metering demand in commercial buildings, industrial energy management, renewable energy and energy storage systems, and EV charging infrastructure.

Key Findings

Metering AFE/ASIC, Metrology MCU and Metering SoC form the three principal product architectures

China has the broadest specialist supplier base among the confirmed core

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manufacturers

India’s RDSS has sanctioned 203.3 million smart meters, creating a substantial multi-year deployment pipeline

State Grid’s second 2025 metering procurement included 17.46 million electricity meters

Market Trends

The Smart Metering Chip market is moving toward higher integration, wider dynamic range and richer metrology capability rather than simply pursuing lower-cost basic energy measurement. In cost-sensitive designs, Metering SoC and Metrology MCU architectures increasingly combine precision measurement, processing, security, RTC and system-control functions to reduce component count and simplify meter design. At the same time, stand-alone Metering AFE/ASIC devices remain strategically important where higher analog performance, architecture flexibility or advanced polyphase measurement is required. Current commercial platforms illustrate the performance direction: Microchip supports metrology solutions reaching class 0.2 accuracy, 24-bit ADC architectures and dynamic ranges extending from approximately 3000:1 to 6000:1, while ST and Renesas maintain metrology products based on 24-bit Sigma-Delta conversion. Sensor flexibility is also becoming more important, with leading platforms supporting shunts, current transformers and Rogowski coils. Product differentiation is consequently expanding from basic active-energy accuracy toward temperature stability, tamper detection, firmware resilience, autocalibration, multi-parameter measurement and power-quality functions. This evolution supports a two-track product roadmap in which highly integrated devices gain relevance in volume applications while premium metering front ends retain value in high-accuracy and feature-intensive systems.

Market Dynamics

Drivers

Demand for Smart Metering Chip is primarily supported by continued smart-meter deployment, replacement of installed meter fleets, grid digitalization and the migration toward prepaid and advanced metering infrastructure. India represents one of the clearest multi-year deployment drivers: the Ministry of Power reported that RDSS had sanctioned smart metering for 197.9 million consumers together with distribution-

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transformer and feeder meters, taking the total sanctioned program to about 203.3 million units. China remains a major replacement and procurement market, with the China Instrument and Control Society/industry association information on State Grid's second 2025 metering procurement indicating approximately 17.46 million electricity meters in that batch alone. Beyond unit deployment, higher metrology accuracy, anti-tamper requirements, multi-parameter measurement, security and power-quality monitoring increase semiconductor content and favor products with stronger analog performance or higher integration.

Restraints

The principal restraint is persistent price pressure in high-volume single-phase metering, particularly where mature measurement functions have become standardized and multiple suppliers can satisfy mainstream accuracy requirements. Long product qualification cycles, meter certification requirements and the need for stable performance over extended operating lifetimes also slow supplier switching and extend commercialization periods for new devices. Architecture integration creates an additional structural constraint: when metrology, control and other functions migrate into a single Metering SoC or Metrology MCU, demand for discrete metering components can be reduced even though semiconductor value remains within the overall meter architecture. Suppliers therefore face a balance between increasing functional integration and preserving acceptable product economics, while fabless vendors must also manage foundry, packaging and test availability across long utility-product life cycles.

Opportunities

The strongest opportunities lie in markets where smart-meter penetration is still expanding and in applications requiring higher-value measurement functions. Large-scale smart-meter programs in India create opportunities not only for unit growth but also for localized semiconductor ecosystems and meter platforms adapted to domestic technical requirements. In mature utility markets, the opportunity increasingly shifts toward replacement, higher-accuracy polyphase metering, advanced tamper detection and power-quality functions. Beyond conventional utility meters, professional energy measurement is becoming more relevant in commercial buildings, industrial energy-management systems, distributed renewable generation, battery energy storage and EV charging equipment. Official product portfolios already show metering silicon being adapted to building-energy monitoring and EV supply equipment, indicating that revenue-grade or high-accuracy metrology technologies can extend into adjacent energy-management applications without changing the underlying measurement

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architecture.

Challenges

The industry must simultaneously manage accuracy, cost, certification, security and long-term supply requirements. Metering devices operate in electrically noisy environments and must maintain measurement stability across temperature, load range and multiple sensing technologies, making analog design, reference stability and calibration capability persistent technical barriers. The shift toward increasingly integrated SoCs raises software, cybersecurity and functional-validation requirements, while advanced power-quality functionality increases signal-processing complexity. Commercially, established suppliers benefit from long installed bases and customer qualification histories, making entry difficult even when a new chip is price competitive. Meanwhile, aggressive competition in standardized single-phase applications can compress ASPs faster than unit volumes expand, placing greater importance on product mix, polyphase exposure, differentiated metrology functions and disciplined manufacturing costs.

Industry Chain Analysis

The Smart Metering Chip industry chain begins with semiconductor design tools and IP, analog and mixed-signal process technologies, foundry or internal wafer fabrication, specialty packaging, test services and supporting materials. Precision metrology products place particular emphasis on ADC architecture, low-noise analog front ends, voltage references, mixed-signal process stability and production calibration. The midstream layer consists of IDM and fabless chip suppliers that integrate circuit design, metrology algorithms, firmware libraries, security functions, device testing and long-lifecycle product management. Value creation is concentrated not only in silicon area but also in measurement accuracy, dynamic range, temperature performance, certification support, calibration efficiency and the ability to maintain consistent performance over long utility deployment cycles. Current vendor platforms demonstrate that metrology devices increasingly combine 24-bit conversion, DSP or MCU processing and broader system functions within a single product family.

Downstream, Smart Metering Chip is integrated into electricity meters and professional energy-measurement equipment before reaching utility, building-energy, industrial and emerging energy applications. Semiconductor cost structures differ materially by architecture: high-volume single-phase ICs are highly sensitive to wafer, packaging and test costs, while higher-accuracy polyphase AFEs and integrated SoCs derive a larger

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portion of value from analog performance, embedded processing, calibration capability, security and application software. Consequently, profitability is generally more defensible where suppliers provide differentiated measurement capability or system integration rather than competing solely on standardized basic metrology.

Segment Insights

By product type, the established market structure consists of Metering AFE, dedicated Metering IC/ASIC, Metrology MCU and Metering SoC. Stand-alone AFE/ASIC solutions remain relevant where designers prioritize high analog performance, independent metrology architecture or flexible pairing with external processors. Metrology MCU and Metering SoC solutions are structurally gaining importance where BOM reduction, PCB simplification, security integration and firmware functionality are important purchasing criteria. Official platforms from Microchip, Renesas and NXP demonstrate the commercial viability of integrating precision metrology with processing, while ST and Analog Devices continue to support specialized high-performance metering IC architectures.

By phase configuration, single-phase devices represent the principal volume-oriented architecture for residential metering, whereas three-phase and polyphase devices generally carry greater semiconductor complexity and value per meter. By ADC resolution, products span lower-resolution architectures through 17–23-bit devices to 24-bit-and-above precision designs, with 24-bit Sigma-Delta conversion particularly prominent in current high-accuracy platforms. By measurement function, the market ranges from basic energy metering to multi-parameter metrology and advanced power-quality measurement. These dimensions are complementary rather than interchangeable and provide a practical basis for evaluating product mix, technology migration and relative value creation.

Downstream Market Opportunities

Utility Electricity Metering remains the primary application market for Smart Metering Chip because billing accuracy, long operating life and large installed meter fleets create recurring demand for dedicated metrology silicon. The adjacent opportunity set is expanding as commercial buildings and industrial facilities adopt more granular energy monitoring, while renewable generation, battery storage and EV charging increase the need for accurate bidirectional or transaction-related energy measurement. These markets are most attractive where applications require utility-grade or professional metrology rather than basic consumer power monitoring. EV charging is particularly

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relevant because energy measurement can become part of billing, equipment diagnostics and energy-management functions, while distributed energy systems increase demand for polyphase measurement, broader dynamic range and power-quality information.

Regional Insights

The regional structure combines mature semiconductor supply centers with rapidly expanding smart-meter deployment markets. North America, Europe and Japan are home to several established precision analog and metrology MCU platforms with long product histories, global qualification capabilities and broad customer coverage. China has the broadest specialist supplier base within the confirmed Core Formal List and combines local semiconductor design capability with a large domestic electricity-meter replacement market. State Grid procurement continues to provide material demand visibility, while competition among local metering-IC suppliers creates a comparatively cost-sensitive market structure. Taiwan contributes a smaller but established group of specialist metering IC and SoC suppliers.

India represents one of the most significant incremental deployment opportunities because its RDSS program has created a large sanctioned pipeline for consumer, feeder and distribution-transformer smart meters. The market is therefore increasingly important to both global semiconductor suppliers and emerging local supply-chain development. Southeast Asia plays a more important role in semiconductor manufacturing, assembly and test than as a headquarters base for dedicated Smart Metering Chip suppliers. Overall, regional competition is evolving toward a combination of global platform vendors, China-based specialist suppliers and increasingly localized semiconductor initiatives in large emerging smart-meter markets.

Competitive Landscape Analysis

The Smart Metering Chip market has a multi-tier competitive structure rather than a single-vendor-dominated model. The confirmed Core Formal List contains 15 manufacturers. Large diversified semiconductor groups such as Texas Instruments, NXP, STMicroelectronics, Analog Devices, Renesas and Microchip compete through precision analog capability, metrology MCU/SoC platforms, broad software ecosystems, certification support and long product lifecycles; Cirrus Logic participates through specialized high-accuracy energy-measurement technology. China has developed a comparatively broad specialist layer including Shanghai Belling, Hi-Trend Technology, Vango Technologies, Eastsoft, Chipsea Technologies and Sino Wealth, with

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competition centered more heavily on local meter specifications, cost efficiency, product customization and domestic support. Prolific Technology and Arch Meter represent specialized Taiwan-based suppliers. Competitive differentiation increasingly depends on architecture choice: high-performance AFE suppliers emphasize accuracy, dynamic range, sensing flexibility and power-quality functions, while MCU and SoC suppliers emphasize integration, security, reduced BOM and software reuse. This structure favors established suppliers in qualification-intensive utility programs but still leaves room for specialist vendors where local standards, cost structures or application-specific integration create clear advantages.

Report Scope

LP Information, Inc. (LPI) ' newest research report, the “Smart Metering Chip Industry Forecast” looks at past sales and reviews total world Smart Metering Chip sales in 2025, providing a comprehensive analysis by region and market sector of projected Smart Metering Chip sales for 2026 through 2032. With Smart Metering Chip sales broken down by region, market sector and sub-sector, this report provides a detailed analysis in US$ millions of the world Smart Metering Chip industry.

This Insight Report provides a comprehensive analysis of the global Smart Metering Chip landscape and highlights key trends related to product segmentation, company formation, revenue, and market share, latest development, and M&A activity. This report also analyzes the strategies of leading global companies with a focus on Smart Metering Chip portfolios and capabilities, market entry strategies, market positions, and geographic footprints, to better understand these firms’ unique position in an accelerating global Smart Metering Chip market.

This Insight Report evaluates the key market trends, drivers, and affecting factors shaping the global outlook for Smart Metering Chip and breaks down the forecast by Type, by Application, geography, and market size to highlight emerging pockets of opportunity. With a transparent methodology based on hundreds of bottom-up qualitative and quantitative market inputs, this study forecast offers a highly nuanced view of the current state and future trajectory in the global Smart Metering Chip.

This report presents a comprehensive overview, market shares, and growth opportunities of Smart Metering Chip market by product type, application, key manufacturers and key regions and countries.

Market Segmentation

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Segmentation by Type:

Metering AFE

Metering SoC

Other

Segmentation by Phase Configuration:

Single-phase Metering Chips

Three-phase Metering Chips

Multi-phase Compatible Metering Chips

Segmentation by ADC Resolution:

?16-bit

17–23-bit

?24-bit

Segmentation by Measurement Function:

Basic Energy Metering

Multi-parameter Metering

Advanced Power-quality Metering

Segmentation by Application:

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Utility Electricity Metering

Commercial Buildings

Industrial Energy Management

Other Applications

This report also splits the market by region:

Americas

United States

Canada

Mexico

Brazil

APAC

China

Japan

Korea

Southeast Asia

India

Australia

Europe

Germany

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France

UK

Italy

Russia

Middle East & Africa

Egypt

South Africa

Israel

Turkey

GCC Countries

The below companies that are profiled have been selected based on inputs gathered from primary experts and analysing the company's coverage, product portfolio, its market penetration.

Texas Instruments Incorporated

NXP Semiconductors N.V.

STMicroelectronics N.V.

Analog Devices, Inc.

Renesas Electronics Corporation

Microchip Technology Incorporated

Cirrus Logic, Inc.

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Shanghai Belling Co., Ltd.

Sino Wealth Electronic Ltd.

Qingdao Eastsoft Communication Technology Co., Ltd.

Chipsea Technologies (Shenzhen) Corp., Ltd.

Hi-Trend Technology (Shanghai) Co., Ltd.

Vango Technologies, Inc.

Prolific Technology Inc.

Arch Meter Corporation

Key Questions Addressed in this Report

What is the 10-year outlook for the global Smart Metering Chip market?

What factors are driving Smart Metering Chip market growth, globally and by region?

Which technologies are poised for the fastest growth by market and region?

How do Smart Metering Chip market opportunities vary by end market size?

How does Smart Metering Chip break out by Type, by Application?

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Contents

1 SCOPE OF THE REPORT

1.1 Market Introduction
1.2 Years Considered
1.3 Research Objectives
1.4 Market Research Methodology
1.5 Research Process and Data Source
1.6 Economic Indicators
1.7 Currency Considered
1.8 Market Estimation Caveats

2 EXECUTIVE SUMMARY

2.1 World Market Overview
  2.1.1 Global Smart Metering Chip Annual Sales 2021-2032
  2.1.2 World Current & Future Analysis for Smart Metering Chip by Geographic Region, 2021, 2025 & 2032
  2.1.3 World Current & Future Analysis for Smart Metering Chip by Country/Region, 2021, 2025 & 2032
2.2 Smart Metering Chip Segment by Type
  2.2.1 Metering AFE
  2.2.2 Metering SoC
  2.2.3 Other
  2.2.4 Smart Metering Chip Sales by Type
    2.2.4.1 Global Smart Metering Chip Sales Market Share by Type (2021-2026)
    2.2.4.2 Global Smart Metering Chip Revenue and Market Share by Type (2021-2026)
    2.2.4.3 Global Smart Metering Chip Sale Price by Type (2021-2026)
2.3 Smart Metering Chip Segment by Phase Configuration
  2.3.1 Single-phase Metering Chips
  2.3.2 Three-phase Metering Chips
  2.3.3 Multi-phase Compatible Metering Chips
  2.3.4 Smart Metering Chip Sales by Phase Configuration
    2.3.4.1 Global Smart Metering Chip Sales Market Share by Phase Configuration (2021-2026)
    2.3.4.2 Global Smart Metering Chip Revenue and Market Share by Phase Configuration (2021-2026)
    2.3.4.3 Global Smart Metering Chip Sale Price by Phase Configuration (2021-2026)

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2.4 Smart Metering Chip Segment by ADC Resolution
2.4.1 ?16-bit
2.4.2 17–23-bit
2.4.3 ?24-bit
2.4.4 Smart Metering Chip Sales by ADC Resolution
2.4.4.1 Global Smart Metering Chip Sales Market Share by ADC Resolution (2021-2026)
2.4.4.2 Global Smart Metering Chip Revenue and Market Share by ADC Resolution (2021-2026)
2.4.4.3 Global Smart Metering Chip Sale Price by ADC Resolution (2021-2026)
2.5 Smart Metering Chip Segment by Measurement Function
2.5.1 Basic Energy Metering
2.5.2 Multi-parameter Metering
2.5.3 Advanced Power-quality Metering
2.5.4 Smart Metering Chip Sales by Measurement Function
2.5.4.1 Global Smart Metering Chip Sales Market Share by Measurement Function (2021-2026)
2.5.4.2 Global Smart Metering Chip Revenue and Market Share by Measurement Function (2021-2026)
2.5.4.3 Global Smart Metering Chip Sale Price by Measurement Function (2021-2026)
2.6 Smart Metering Chip Segment by Application
2.6.1 Utility Electricity Metering
2.6.2 Commercial Buildings
2.6.3 Industrial Energy Management
2.6.4 Other Applications
2.6.5 Smart Metering Chip Sales by Application
2.6.5.1 Global Smart Metering Chip Sale Market Share by Application (2021-2026)
2.6.5.2 Global Smart Metering Chip Revenue and Market Share by Application (2021-2026)
2.6.5.3 Global Smart Metering Chip Sale Price by Application (2021-2026)

3 GLOBAL BY COMPANY

3.1 Global Smart Metering Chip Breakdown Data by Company
3.1.1 Global Smart Metering Chip Annual Sales by Company (2021-2026)
3.1.2 Global Smart Metering Chip Sales Market Share by Company (2021-2026)
3.2 Global Smart Metering Chip Annual Revenue by Company (2021-2026)
3.2.1 Global Smart Metering Chip Revenue by Company (2021-2026)

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info@marketpublishers.com

3.2.2 Global Smart Metering Chip Revenue Market Share by Company (2021-2026)
3.3 Global Smart Metering Chip Sale Price by Company
3.4 Key Manufacturers Smart Metering Chip Producing Area Distribution, Sales Area, Product Type
3.4.1 Key Manufacturers Smart Metering Chip Product Location Distribution
3.4.2 Players Smart Metering Chip Products Offered
3.5 Market Concentration Rate Analysis
3.5.1 Competition Landscape Analysis
3.5.2 Concentration Ratio (CR3, CR5 and CR10) & (2024-2026)
3.6 New Products and Potential Entrants
3.7 Market M&A Activity & Strategy

4 WORLD HISTORIC REVIEW FOR SMART METERING CHIP BY GEOGRAPHIC REGION

4.1 World Historic Smart Metering Chip Market Size by Geographic Region (2021-2026)
4.1.1 Global Smart Metering Chip Annual Sales by Geographic Region (2021-2026)
4.1.2 Global Smart Metering Chip Annual Revenue by Geographic Region (2021-2026)
4.2 World Historic Smart Metering Chip Market Size by Country/Region (2021-2026)
4.2.1 Global Smart Metering Chip Annual Sales by Country/Region (2021-2026)
4.2.2 Global Smart Metering Chip Annual Revenue by Country/Region (2021-2026)
4.3 Americas Smart Metering Chip Sales Growth
4.4 APAC Smart Metering Chip Sales Growth
4.5 Europe Smart Metering Chip Sales Growth
4.6 Middle East & Africa Smart Metering Chip Sales Growth

5 AMERICAS

5.1 Americas Smart Metering Chip Sales by Country
5.1.1 Americas Smart Metering Chip Sales by Country (2021-2026)
5.1.2 Americas Smart Metering Chip Revenue by Country (2021-2026)
5.2 Americas Smart Metering Chip Sales by Type (2021-2026)
5.3 Americas Smart Metering Chip Sales by Application (2021-2026)
5.4 United States
5.5 Canada
5.6 Mexico
5.7 Brazil

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6 APAC

6.1 APAC Smart Metering Chip Sales by Region
  6.1.1 APAC Smart Metering Chip Sales by Region (2021-2026)
  6.1.2 APAC Smart Metering Chip Revenue by Region (2021-2026)
6.2 APAC Smart Metering Chip Sales by Type (2021-2026)
6.3 APAC Smart Metering Chip Sales by Application (2021-2026)
6.4 China
6.5 Japan
6.6 South Korea
6.7 Southeast Asia
6.8 India
6.9 Australia
6.10 China Taiwan

7 EUROPE

7.1 Europe Smart Metering Chip by Country
  7.1.1 Europe Smart Metering Chip Sales by Country (2021-2026)
  7.1.2 Europe Smart Metering Chip Revenue by Country (2021-2026)
7.2 Europe Smart Metering Chip Sales by Type (2021-2026)
7.3 Europe Smart Metering Chip Sales by Application (2021-2026)
7.4 Germany
7.5 France
7.6 UK
7.7 Italy
7.8 Russia

8 MIDDLE EAST & AFRICA

8.1 Middle East & Africa Smart Metering Chip by Country
  8.1.1 Middle East & Africa Smart Metering Chip Sales by Country (2021-2026)
  8.1.2 Middle East & Africa Smart Metering Chip Revenue by Country (2021-2026)
8.2 Middle East & Africa Smart Metering Chip Sales by Type (2021-2026)
8.3 Middle East & Africa Smart Metering Chip Sales by Application (2021-2026)
8.4 Egypt
8.5 South Africa
8.6 Israel
8.7 Turkey

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info@marketpublishers.com

8.8 GCC Countries

9 MARKET DRIVERS, CHALLENGES AND TRENDS

9.1 Market Drivers & Growth Opportunities
9.2 Market Challenges & Risks
9.3 Industry Trends

10 MANUFACTURING COST STRUCTURE ANALYSIS

10.1 Raw Material and Suppliers
10.2 Manufacturing Cost Structure Analysis of Smart Metering Chip
10.3 Manufacturing Process Analysis of Smart Metering Chip
10.4 Industry Chain Structure of Smart Metering Chip

11 MARKETING, DISTRIBUTORS AND CUSTOMER

11.1 Sales Channel
  11.1.1 Direct Channels
  11.1.2 Indirect Channels
11.2 Smart Metering Chip Distributors
11.3 Smart Metering Chip Customer

12 WORLD FORECAST REVIEW FOR SMART METERING CHIP BY GEOGRAPHIC REGION

12.1 Global Smart Metering Chip Market Size Forecast by Region
  12.1.1 Global Smart Metering Chip Forecast by Region (2027-2032)
  12.1.2 Global Smart Metering Chip Annual Revenue Forecast by Region (2027-2032)
12.2 Americas Forecast by Country (2027-2032)
12.3 APAC Forecast by Region (2027-2032)
12.4 Europe Forecast by Country (2027-2032)
12.5 Middle East & Africa Forecast by Country (2027-2032)
12.6 Global Smart Metering Chip Forecast by Type (2027-2032)
12.7 Global Smart Metering Chip Forecast by Application (2027-2032)

13 KEY PLAYERS ANALYSIS

13.1 Texas Instruments Incorporated

+357 96 030922
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13.1.1 Texas Instruments Incorporated Company Information
13.1.2 Texas Instruments Incorporated Smart Metering Chip Product Portfolios and Specifications
13.1.3 Texas Instruments Incorporated Smart Metering Chip Sales, Revenue, Price and Gross Margin (2021-2026)
13.1.4 Texas Instruments Incorporated Main Business Overview
13.1.5 Texas Instruments Incorporated Latest Developments
13.2 NXP Semiconductors N.V.
13.2.1 NXP Semiconductors N.V. Company Information
13.2.2 NXP Semiconductors N.V. Smart Metering Chip Product Portfolios and Specifications
13.2.3 NXP Semiconductors N.V. Smart Metering Chip Sales, Revenue, Price and Gross Margin (2021-2026)
13.2.4 NXP Semiconductors N.V. Main Business Overview
13.2.5 NXP Semiconductors N.V. Latest Developments
13.3 STMicroelectronics N.V.
13.3.1 STMicroelectronics N.V. Company Information
13.3.2 STMicroelectronics N.V. Smart Metering Chip Product Portfolios and Specifications
13.3.3 STMicroelectronics N.V. Smart Metering Chip Sales, Revenue, Price and Gross Margin (2021-2026)
13.3.4 STMicroelectronics N.V. Main Business Overview
13.3.5 STMicroelectronics N.V. Latest Developments
13.4 Analog Devices, Inc.
13.4.1 Analog Devices, Inc. Company Information
13.4.2 Analog Devices, Inc. Smart Metering Chip Product Portfolios and Specifications
13.4.3 Analog Devices, Inc. Smart Metering Chip Sales, Revenue, Price and Gross Margin (2021-2026)
13.4.4 Analog Devices, Inc. Main Business Overview
13.4.5 Analog Devices, Inc. Latest Developments
13.5 Renesas Electronics Corporation
13.5.1 Renesas Electronics Corporation Company Information
13.5.2 Renesas Electronics Corporation Smart Metering Chip Product Portfolios and Specifications
13.5.3 Renesas Electronics Corporation Smart Metering Chip Sales, Revenue, Price and Gross Margin (2021-2026)
13.5.4 Renesas Electronics Corporation Main Business Overview
13.5.5 Renesas Electronics Corporation Latest Developments
13.6 Microchip Technology Incorporated

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13.6.1 Microchip Technology Incorporated Company Information
13.6.2 Microchip Technology Incorporated Smart Metering Chip Product Portfolios and Specifications
13.6.3 Microchip Technology Incorporated Smart Metering Chip Sales, Revenue, Price and Gross Margin (2021-2026)
13.6.4 Microchip Technology Incorporated Main Business Overview
13.6.5 Microchip Technology Incorporated Latest Developments
13.7 Cirrus Logic, Inc.
13.7.1 Cirrus Logic, Inc. Company Information
13.7.2 Cirrus Logic, Inc. Smart Metering Chip Product Portfolios and Specifications
13.7.3 Cirrus Logic, Inc. Smart Metering Chip Sales, Revenue, Price and Gross Margin (2021-2026)
13.7.4 Cirrus Logic, Inc. Main Business Overview
13.7.5 Cirrus Logic, Inc. Latest Developments
13.8 Shanghai Belling Co., Ltd.
13.8.1 Shanghai Belling Co., Ltd. Company Information
13.8.2 Shanghai Belling Co., Ltd. Smart Metering Chip Product Portfolios and Specifications
13.8.3 Shanghai Belling Co., Ltd. Smart Metering Chip Sales, Revenue, Price and Gross Margin (2021-2026)
13.8.4 Shanghai Belling Co., Ltd. Main Business Overview
13.8.5 Shanghai Belling Co., Ltd. Latest Developments
13.9 Sino Wealth Electronic Ltd.
13.9.1 Sino Wealth Electronic Ltd. Company Information
13.9.2 Sino Wealth Electronic Ltd. Smart Metering Chip Product Portfolios and Specifications
13.9.3 Sino Wealth Electronic Ltd. Smart Metering Chip Sales, Revenue, Price and Gross Margin (2021-2026)
13.9.4 Sino Wealth Electronic Ltd. Main Business Overview
13.9.5 Sino Wealth Electronic Ltd. Latest Developments
13.10 Qingdao Eastsoft Communication Technology Co., Ltd.
13.10.1 Qingdao Eastsoft Communication Technology Co., Ltd. Company Information
13.10.2 Qingdao Eastsoft Communication Technology Co., Ltd. Smart Metering Chip Product Portfolios and Specifications
13.10.3 Qingdao Eastsoft Communication Technology Co., Ltd. Smart Metering Chip Sales, Revenue, Price and Gross Margin (2021-2026)
13.10.4 Qingdao Eastsoft Communication Technology Co., Ltd. Main Business Overview
13.10.5 Qingdao Eastsoft Communication Technology Co., Ltd. Latest Developments

+357 96 030922
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13.11 Chipsea Technologies (Shenzhen) Corp., Ltd.
13.11.1 Chipsea Technologies (Shenzhen) Corp., Ltd. Company Information
13.11.2 Chipsea Technologies (Shenzhen) Corp., Ltd. Smart Metering Chip Product Portfolios and Specifications
13.11.3 Chipsea Technologies (Shenzhen) Corp., Ltd. Smart Metering Chip Sales, Revenue, Price and Gross Margin (2021-2026)
13.11.4 Chipsea Technologies (Shenzhen) Corp., Ltd. Main Business Overview
13.11.5 Chipsea Technologies (Shenzhen) Corp., Ltd. Latest Developments
13.12 Hi-Trend Technology (Shanghai) Co., Ltd.
13.12.1 Hi-Trend Technology (Shanghai) Co., Ltd. Company Information
13.12.2 Hi-Trend Technology (Shanghai) Co., Ltd. Smart Metering Chip Product Portfolios and Specifications
13.12.3 Hi-Trend Technology (Shanghai) Co., Ltd. Smart Metering Chip Sales, Revenue, Price and Gross Margin (2021-2026)
13.12.4 Hi-Trend Technology (Shanghai) Co., Ltd. Main Business Overview
13.12.5 Hi-Trend Technology (Shanghai) Co., Ltd. Latest Developments
13.13 Vango Technologies, Inc.
13.13.1 Vango Technologies, Inc. Company Information
13.13.2 Vango Technologies, Inc. Smart Metering Chip Product Portfolios and Specifications
13.13.3 Vango Technologies, Inc. Smart Metering Chip Sales, Revenue, Price and Gross Margin (2021-2026)
13.13.4 Vango Technologies, Inc. Main Business Overview
13.13.5 Vango Technologies, Inc. Latest Developments
13.14 Prolific Technology Inc.
13.14.1 Prolific Technology Inc. Company Information
13.14.2 Prolific Technology Inc. Smart Metering Chip Product Portfolios and Specifications
13.14.3 Prolific Technology Inc. Smart Metering Chip Sales, Revenue, Price and Gross Margin (2021-2026)
13.14.4 Prolific Technology Inc. Main Business Overview
13.14.5 Prolific Technology Inc. Latest Developments
13.15 Arch Meter Corporation
13.15.1 Arch Meter Corporation Company Information
13.15.2 Arch Meter Corporation Smart Metering Chip Product Portfolios and Specifications
13.15.3 Arch Meter Corporation Smart Metering Chip Sales, Revenue, Price and Gross Margin (2021-2026)
13.15.4 Arch Meter Corporation Main Business Overview

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13.15.5 Arch Meter Corporation Latest Developments

14 RESEARCH FINDINGS AND CONCLUSION

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List Of Tables

LIST OF TABLES

Table 1. Smart Metering Chip Annual Sales CAGR by Geographic Region (2021, 2025 & 2032) & ($ millions)
Table 2. Smart Metering Chip Annual Sales CAGR by Country/Region (2021, 2025 & 2032) & ($ millions)
Table 3. Major Players of Metering AFE
Table 4. Major Players of Metering SoC
Table 5. Major Players of Other
Table 6. Global Smart Metering Chip Sales by Type (2021-2026) & (Million Units)
Table 7. Global Smart Metering Chip Sales Market Share by Type (2021-2026)
Table 8. Global Smart Metering Chip Revenue by Type (2021-2026) & ($ million)
Table 9. Global Smart Metering Chip Revenue Market Share by Type (2021-2026)
Table 10. Global Smart Metering Chip Sale Price by Type (2021-2026) & (US$/Unit)
Table 11. Major Players of Single-phase Metering Chips
Table 12. Major Players of Three-phase Metering Chips
Table 13. Major Players of Multi-phase Compatible Metering Chips
Table 14. Global Smart Metering Chip Sales by Phase Configuration (2021-2026) & (Million Units)
Table 15. Global Smart Metering Chip Sales Market Share by Phase Configuration (2021-2026)
Table 16. Global Smart Metering Chip Revenue by Phase Configuration (2021-2026) & ($ million)
Table 17. Global Smart Metering Chip Revenue Market Share by Phase Configuration (2021-2026)
Table 18. Global Smart Metering Chip Sale Price by Phase Configuration (2021-2026) & (US$/Unit)
Table 19. Major Players of ?16-bit
Table 20. Major Players of 17–23-bit
Table 21. Major Players of ?24-bit
Table 22. Global Smart Metering Chip Sales by ADC Resolution (2021-2026) & (Million Units)
Table 23. Global Smart Metering Chip Sales Market Share by ADC Resolution (2021-2026)
Table 24. Global Smart Metering Chip Revenue by ADC Resolution (2021-2026) & ($ million)
Table 25. Global Smart Metering Chip Revenue Market Share by ADC Resolution

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(2021-2026)
Table 26. Global Smart Metering Chip Sale Price by ADC Resolution (2021-2026) & (US$/Unit)
Table 27. Major Players of Basic Energy Metering
Table 28. Major Players of Multi-parameter Metering
Table 29. Major Players of Advanced Power-quality Metering
Table 30. Global Smart Metering Chip Sales by Measurement Function (2021-2026) & (Million Units)
Table 31. Global Smart Metering Chip Sales Market Share by Measurement Function (2021-2026)
Table 32. Global Smart Metering Chip Revenue by Measurement Function (2021-2026) & ($ million)
Table 33. Global Smart Metering Chip Revenue Market Share by Measurement Function (2021-2026)
Table 34. Global Smart Metering Chip Sale Price by Measurement Function (2021-2026) & (US$/Unit)
Table 35. Global Smart Metering Chip Sale by Application (2021-2026) & (Million Units)
Table 36. Global Smart Metering Chip Sale Market Share by Application (2021-2026)
Table 37. Global Smart Metering Chip Revenue by Application (2021-2026) & ($ million)
Table 38. Global Smart Metering Chip Revenue Market Share by Application (2021-2026)
Table 39. Global Smart Metering Chip Sale Price by Application (2021-2026) & (US$/Unit)
Table 40. Global Smart Metering Chip Sales by Company (2021-2026) & (Million Units)
Table 41. Global Smart Metering Chip Sales Market Share by Company (2021-2026)
Table 42. Global Smart Metering Chip Revenue by Company (2021-2026) & ($ millions)
Table 43. Global Smart Metering Chip Revenue Market Share by Company (2021-2026)
Table 44. Global Smart Metering Chip Sale Price by Company (2021-2026) & (US$/Unit)
Table 45. Key Manufacturers Smart Metering Chip Producing Area Distribution and Sales Area
Table 46. Players Smart Metering Chip Products Offered
Table 47. Smart Metering Chip Concentration Ratio (CR3, CR5 and CR10) & (2024-2026)
Table 48. New Products and Potential Entrants
Table 49. Market M&A Activity & Strategy
Table 50. Global Smart Metering Chip Sales by Geographic Region (2021-2026) & (Million Units)
Table 51. Global Smart Metering Chip Sales Market Share Geographic Region

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(2021-2026)
Table 52. Global Smart Metering Chip Revenue by Geographic Region (2021-2026) & ($ millions)
Table 53. Global Smart Metering Chip Revenue Market Share by Geographic Region (2021-2026)
Table 54. Global Smart Metering Chip Sales by Country/Region (2021-2026) & (Million Units)
Table 55. Global Smart Metering Chip Sales Market Share by Country/Region (2021-2026)
Table 56. Global Smart Metering Chip Revenue by Country/Region (2021-2026) & ($ millions)
Table 57. Global Smart Metering Chip Revenue Market Share by Country/Region (2021-2026)
Table 58. Americas Smart Metering Chip Sales by Country (2021-2026) & (Million Units)
Table 59. Americas Smart Metering Chip Sales Market Share by Country (2021-2026)
Table 60. Americas Smart Metering Chip Revenue by Country (2021-2026) & ($ millions)
Table 61. Americas Smart Metering Chip Sales by Type (2021-2026) & (Million Units)
Table 62. Americas Smart Metering Chip Sales by Application (2021-2026) & (Million Units)
Table 63. APAC Smart Metering Chip Sales by Region (2021-2026) & (Million Units)
Table 64. APAC Smart Metering Chip Sales Market Share by Region (2021-2026)
Table 65. APAC Smart Metering Chip Revenue by Region (2021-2026) & ($ millions)
Table 66. APAC Smart Metering Chip Sales by Type (2021-2026) & (Million Units)
Table 67. APAC Smart Metering Chip Sales by Application (2021-2026) & (Million Units)
Table 68. Europe Smart Metering Chip Sales by Country (2021-2026) & (Million Units)
Table 69. Europe Smart Metering Chip Revenue by Country (2021-2026) & ($ millions)
Table 70. Europe Smart Metering Chip Sales by Type (2021-2026) & (Million Units)
Table 71. Europe Smart Metering Chip Sales by Application (2021-2026) & (Million Units)
Table 72. Middle East & Africa Smart Metering Chip Sales by Country (2021-2026) & (Million Units)
Table 73. Middle East & Africa Smart Metering Chip Revenue Market Share by Country (2021-2026)
Table 74. Middle East & Africa Smart Metering Chip Sales by Type (2021-2026) & (Million Units)
Table 75. Middle East & Africa Smart Metering Chip Sales by Application (2021-2026) & (Million Units)
Table 76. Key Market Drivers & Growth Opportunities of Smart Metering Chip

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Table 77. Key Market Challenges & Risks of Smart Metering Chip
Table 78. Key Industry Trends of Smart Metering Chip
Table 79. Smart Metering Chip Raw Material
Table 80. Key Suppliers of Raw Materials
Table 81. Smart Metering Chip Distributors List
Table 82. Smart Metering Chip Customer List
Table 83. Global Smart Metering Chip Sales Forecast by Region (2027-2032) & (Million Units)
Table 84. Global Smart Metering Chip Revenue Forecast by Region (2027-2032) & ($ millions)
Table 85. Americas Smart Metering Chip Sales Forecast by Country (2027-2032) & (Million Units)
Table 86. Americas Smart Metering Chip Annual Revenue Forecast by Country (2027-2032) & ($ millions)
Table 87. APAC Smart Metering Chip Sales Forecast by Region (2027-2032) & (Million Units)
Table 88. APAC Smart Metering Chip Annual Revenue Forecast by Region (2027-2032) & ($ millions)
Table 89. Europe Smart Metering Chip Sales Forecast by Country (2027-2032) & (Million Units)
Table 90. Europe Smart Metering Chip Revenue Forecast by Country (2027-2032) & ($ millions)
Table 91. Middle East & Africa Smart Metering Chip Sales Forecast by Country (2027-2032) & (Million Units)
Table 92. Middle East & Africa Smart Metering Chip Revenue Forecast by Country (2027-2032) & ($ millions)
Table 93. Global Smart Metering Chip Sales Forecast by Type (2027-2032) & (Million Units)
Table 94. Global Smart Metering Chip Revenue Forecast by Type (2027-2032) & ($ millions)
Table 95. Global Smart Metering Chip Sales Forecast by Application (2027-2032) & (Million Units)
Table 96. Global Smart Metering Chip Revenue Forecast by Application (2027-2032) & ($ millions)
Table 97. Texas Instruments Incorporated Basic Information, Smart Metering Chip Manufacturing Base, Sales Area and Its Competitors
Table 98. Texas Instruments Incorporated Smart Metering Chip Product Portfolios and Specifications
Table 99. Texas Instruments Incorporated Smart Metering Chip Sales (Million Units),

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Revenue ($ Million), Price (US$/Unit) and Gross Margin (2021-2026)
Table 100. Texas Instruments Incorporated Main Business
Table 101. Texas Instruments Incorporated Latest Developments
Table 102. NXP Semiconductors N.V. Basic Information, Smart Metering Chip Manufacturing Base, Sales Area and Its Competitors
Table 103. NXP Semiconductors N.V. Smart Metering Chip Product Portfolios and Specifications
Table 104. NXP Semiconductors N.V. Smart Metering Chip Sales (Million Units), Revenue ($ Million), Price (US$/Unit) and Gross Margin (2021-2026)
Table 105. NXP Semiconductors N.V. Main Business
Table 106. NXP Semiconductors N.V. Latest Developments
Table 107. STMicroelectronics N.V. Basic Information, Smart Metering Chip Manufacturing Base, Sales Area and Its Competitors
Table 108. STMicroelectronics N.V. Smart Metering Chip Product Portfolios and Specifications
Table 109. STMicroelectronics N.V. Smart Metering Chip Sales (Million Units), Revenue ($ Million), Price (US$/Unit) and Gross Margin (2021-2026)
Table 110. STMicroelectronics N.V. Main Business
Table 111. STMicroelectronics N.V. Latest Developments
Table 112. Analog Devices, Inc. Basic Information, Smart Metering Chip Manufacturing Base, Sales Area and Its Competitors
Table 113. Analog Devices, Inc. Smart Metering Chip Product Portfolios and Specifications
Table 114. Analog Devices, Inc. Smart Metering Chip Sales (Million Units), Revenue ($ Million), Price (US$/Unit) and Gross Margin (2021-2026)
Table 115. Analog Devices, Inc. Main Business
Table 116. Analog Devices, Inc. Latest Developments
Table 117. Renesas Electronics Corporation Basic Information, Smart Metering Chip Manufacturing Base, Sales Area and Its Competitors
Table 118. Renesas Electronics Corporation Smart Metering Chip Product Portfolios and Specifications
Table 119. Renesas Electronics Corporation Smart Metering Chip Sales (Million Units), Revenue ($ Million), Price (US$/Unit) and Gross Margin (2021-2026)
Table 120. Renesas Electronics Corporation Main Business
Table 121. Renesas Electronics Corporation Latest Developments
Table 122. Microchip Technology Incorporated Basic Information, Smart Metering Chip Manufacturing Base, Sales Area and Its Competitors
Table 123. Microchip Technology Incorporated Smart Metering Chip Product Portfolios and Specifications

+357 96 030922
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Table 124. Microchip Technology Incorporated Smart Metering Chip Sales (Million Units), Revenue ($ Million), Price (US$/Unit) and Gross Margin (2021-2026)
Table 125. Microchip Technology Incorporated Main Business
Table 126. Microchip Technology Incorporated Latest Developments
Table 127. Cirrus Logic, Inc. Basic Information, Smart Metering Chip Manufacturing Base, Sales Area and Its Competitors
Table 128. Cirrus Logic, Inc. Smart Metering Chip Product Portfolios and Specifications
Table 129. Cirrus Logic, Inc. Smart Metering Chip Sales (Million Units), Revenue ($ Million), Price (US$/Unit) and Gross Margin (2021-2026)
Table 130. Cirrus Logic, Inc. Main Business
Table 131. Cirrus Logic, Inc. Latest Developments
Table 132. Shanghai Belling Co., Ltd. Basic Information, Smart Metering Chip Manufacturing Base, Sales Area and Its Competitors
Table 133. Shanghai Belling Co., Ltd. Smart Metering Chip Product Portfolios and Specifications
Table 134. Shanghai Belling Co., Ltd. Smart Metering Chip Sales (Million Units), Revenue ($ Million), Price (US$/Unit) and Gross Margin (2021-2026)
Table 135. Shanghai Belling Co., Ltd. Main Business
Table 136. Shanghai Belling Co., Ltd. Latest Developments
Table 137. Sino Wealth Electronic Ltd. Basic Information, Smart Metering Chip Manufacturing Base, Sales Area and Its Competitors
Table 138. Sino Wealth Electronic Ltd. Smart Metering Chip Product Portfolios and Specifications
Table 139. Sino Wealth Electronic Ltd. Smart Metering Chip Sales (Million Units), Revenue ($ Million), Price (US$/Unit) and Gross Margin (2021-2026)
Table 140. Sino Wealth Electronic Ltd. Main Business
Table 141. Sino Wealth Electronic Ltd. Latest Developments
Table 142. Qingdao Eastsoft Communication Technology Co., Ltd. Basic Information, Smart Metering Chip Manufacturing Base, Sales Area and Its Competitors
Table 143. Qingdao Eastsoft Communication Technology Co., Ltd. Smart Metering Chip Product Portfolios and Specifications
Table 144. Qingdao Eastsoft Communication Technology Co., Ltd. Smart Metering Chip Sales (Million Units), Revenue ($ Million), Price (US$/Unit) and Gross Margin (2021-2026)
Table 145. Qingdao Eastsoft Communication Technology Co., Ltd. Main Business
Table 146. Qingdao Eastsoft Communication Technology Co., Ltd. Latest Developments
Table 147. Chipsea Technologies (Shenzhen) Corp., Ltd. Basic Information, Smart Metering Chip Manufacturing Base, Sales Area and Its Competitors

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Table 148. Chipsea Technologies (Shenzhen) Corp., Ltd. Smart Metering Chip Product Portfolios and Specifications
Table 149. Chipsea Technologies (Shenzhen) Corp., Ltd. Smart Metering Chip Sales (Million Units), Revenue ($ Million), Price (US$/Unit) and Gross Margin (2021-2026)
Table 150. Chipsea Technologies (Shenzhen) Corp., Ltd. Main Business
Table 151. Chipsea Technologies (Shenzhen) Corp., Ltd. Latest Developments
Table 152. Hi-Trend Technology (Shanghai) Co., Ltd. Basic Information, Smart Metering Chip Manufacturing Base, Sales Area and Its Competitors
Table 153. Hi-Trend Technology (Shanghai) Co., Ltd. Smart Metering Chip Product Portfolios and Specifications
Table 154. Hi-Trend Technology (Shanghai) Co., Ltd. Smart Metering Chip Sales (Million Units), Revenue ($ Million), Price (US$/Unit) and Gross Margin (2021-2026)
Table 155. Hi-Trend Technology (Shanghai) Co., Ltd. Main Business
Table 156. Hi-Trend Technology (Shanghai) Co., Ltd. Latest Developments
Table 157. Vango Technologies, Inc. Basic Information, Smart Metering Chip Manufacturing Base, Sales Area and Its Competitors
Table 158. Vango Technologies, Inc. Smart Metering Chip Product Portfolios and Specifications
Table 159. Vango Technologies, Inc. Smart Metering Chip Sales (Million Units), Revenue ($ Million), Price (US$/Unit) and Gross Margin (2021-2026)
Table 160. Vango Technologies, Inc. Main Business
Table 161. Vango Technologies, Inc. Latest Developments
Table 162. Prolific Technology Inc. Basic Information, Smart Metering Chip Manufacturing Base, Sales Area and Its Competitors
Table 163. Prolific Technology Inc. Smart Metering Chip Product Portfolios and Specifications
Table 164. Prolific Technology Inc. Smart Metering Chip Sales (Million Units), Revenue ($ Million), Price (US$/Unit) and Gross Margin (2021-2026)
Table 165. Prolific Technology Inc. Main Business
Table 166. Prolific Technology Inc. Latest Developments
Table 167. Arch Meter Corporation Basic Information, Smart Metering Chip Manufacturing Base, Sales Area and Its Competitors
Table 168. Arch Meter Corporation Smart Metering Chip Product Portfolios and Specifications
Table 169. Arch Meter Corporation Smart Metering Chip Sales (Million Units), Revenue ($ Million), Price (US$/Unit) and Gross Margin (2021-2026)
Table 170. Arch Meter Corporation Main Business
Table 171. Arch Meter Corporation Latest Developments

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List Of Figures

LIST OF FIGURES

Figure 1. Picture of Smart Metering Chip
Figure 2. Smart Metering Chip Report Years Considered
Figure 3. Research Objectives
Figure 4. Research Methodology
Figure 5. Research Process and Data Source
Figure 6. Global Smart Metering Chip Sales Growth Rate 2021-2032 (Million Units)
Figure 7. Global Smart Metering Chip Revenue Growth Rate 2021-2032 ($ millions)
Figure 8. Smart Metering Chip Sales by Geographic Region (2021, 2025 & 2032) & ($ millions)
Figure 9. Smart Metering Chip Sales Market Share by Country/Region (2025)
Figure 10. Smart Metering Chip Sales Market Share by Country/Region (2021, 2025 & 2032)
Figure 11. Product Picture of Metering AFE
Figure 12. Product Picture of Metering SoC
Figure 13. Product Picture of Other
Figure 14. Global Smart Metering Chip Sales Market Share by Type in 2026
Figure 15. Global Smart Metering Chip Revenue Market Share by Type (2021-2026)
Figure 16. Product Picture of Single-phase Metering Chips
Figure 17. Product Picture of Three-phase Metering Chips
Figure 18. Product Picture of Multi-phase Compatible Metering Chips
Figure 19. Global Smart Metering Chip Sales Market Share by Phase Configuration in 2026
Figure 20. Global Smart Metering Chip Revenue Market Share by Phase Configuration (2021-2026)
Figure 21. Product Picture of ?16-bit
Figure 22. Product Picture of 17–23-bit
Figure 23. Product Picture of ?24-bit
Figure 24. Global Smart Metering Chip Sales Market Share by ADC Resolution in 2026
Figure 25. Global Smart Metering Chip Revenue Market Share by ADC Resolution (2021-2026)
Figure 26. Product Picture of Basic Energy Metering
Figure 27. Product Picture of Multi-parameter Metering
Figure 28. Product Picture of Advanced Power-quality Metering
Figure 29. Global Smart Metering Chip Sales Market Share by Measurement Function in 2026

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Figure 30. Global Smart Metering Chip Revenue Market Share by Measurement Function (2021-2026)
Figure 31. Smart Metering Chip Consumed in Utility Electricity Metering
Figure 32. Global Smart Metering Chip Market: Utility Electricity Metering (2021-2026) & (Million Units)
Figure 33. Smart Metering Chip Consumed in Commercial Buildings
Figure 34. Global Smart Metering Chip Market: Commercial Buildings (2021-2026) & (Million Units)
Figure 35. Smart Metering Chip Consumed in Industrial Energy Management
Figure 36. Global Smart Metering Chip Market: Industrial Energy Management (2021-2026) & (Million Units)
Figure 37. Smart Metering Chip Consumed in Other Applications
Figure 38. Global Smart Metering Chip Market: Other Applications (2021-2026) & (Million Units)
Figure 39. Global Smart Metering Chip Sale Market Share by Application (2025)
Figure 40. Global Smart Metering Chip Revenue Market Share by Application in 2025
Figure 41. Smart Metering Chip Sales by Company in 2025 (Million Units)
Figure 42. Global Smart Metering Chip Sales Market Share by Company in 2025
Figure 43. Smart Metering Chip Revenue by Company in 2025 ($ millions)
Figure 44. Global Smart Metering Chip Revenue Market Share by Company in 2025
Figure 45. Global Smart Metering Chip Sales Market Share by Geographic Region (2021-2026)
Figure 46. Global Smart Metering Chip Revenue Market Share by Geographic Region in 2025
Figure 47. Americas Smart Metering Chip Sales 2021-2026 (Million Units)
Figure 48. Americas Smart Metering Chip Revenue 2021-2026 ($ millions)
Figure 49. APAC Smart Metering Chip Sales 2021-2026 (Million Units)
Figure 50. APAC Smart Metering Chip Revenue 2021-2026 ($ millions)
Figure 51. Europe Smart Metering Chip Sales 2021-2026 (Million Units)
Figure 52. Europe Smart Metering Chip Revenue 2021-2026 ($ millions)
Figure 53. Middle East & Africa Smart Metering Chip Sales 2021-2026 (Million Units)
Figure 54. Middle East & Africa Smart Metering Chip Revenue 2021-2026 ($ millions)
Figure 55. Americas Smart Metering Chip Sales Market Share by Country in 2025
Figure 56. Americas Smart Metering Chip Revenue Market Share by Country (2021-2026)
Figure 57. Americas Smart Metering Chip Sales Market Share by Type (2021-2026)
Figure 58. Americas Smart Metering Chip Sales Market Share by Application (2021-2026)
Figure 59. United States Smart Metering Chip Revenue Growth 2021-2026 ($ millions)

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Figure 60. Canada Smart Metering Chip Revenue Growth 2021-2026 ($ millions)
Figure 61. Mexico Smart Metering Chip Revenue Growth 2021-2026 ($ millions)
Figure 62. Brazil Smart Metering Chip Revenue Growth 2021-2026 ($ millions)
Figure 63. APAC Smart Metering Chip Sales Market Share by Region in 2025
Figure 64. APAC Smart Metering Chip Revenue Market Share by Region (2021-2026)
Figure 65. APAC Smart Metering Chip Sales Market Share by Type (2021-2026)
Figure 66. APAC Smart Metering Chip Sales Market Share by Application (2021-2026)
Figure 67. China Smart Metering Chip Revenue Growth 2021-2026 ($ millions)
Figure 68. Japan Smart Metering Chip Revenue Growth 2021-2026 ($ millions)
Figure 69. South Korea Smart Metering Chip Revenue Growth 2021-2026 ($ millions)
Figure 70. Southeast Asia Smart Metering Chip Revenue Growth 2021-2026 ($ millions)
Figure 71. India Smart Metering Chip Revenue Growth 2021-2026 ($ millions)
Figure 72. Australia Smart Metering Chip Revenue Growth 2021-2026 ($ millions)
Figure 73. China Taiwan Smart Metering Chip Revenue Growth 2021-2026 ($ millions)
Figure 74. Europe Smart Metering Chip Sales Market Share by Country in 2025
Figure 75. Europe Smart Metering Chip Revenue Market Share by Country (2021-2026)
Figure 76. Europe Smart Metering Chip Sales Market Share by Type (2021-2026)
Figure 77. Europe Smart Metering Chip Sales Market Share by Application (2021-2026)
Figure 78. Germany Smart Metering Chip Revenue Growth 2021-2026 ($ millions)
Figure 79. France Smart Metering Chip Revenue Growth 2021-2026 ($ millions)
Figure 80. UK Smart Metering Chip Revenue Growth 2021-2026 ($ millions)
Figure 81. Italy Smart Metering Chip Revenue Growth 2021-2026 ($ millions)
Figure 82. Russia Smart Metering Chip Revenue Growth 2021-2026 ($ millions)
Figure 83. Middle East & Africa Smart Metering Chip Sales Market Share by Country (2021-2026)
Figure 84. Middle East & Africa Smart Metering Chip Sales Market Share by Type (2021-2026)
Figure 85. Middle East & Africa Smart Metering Chip Sales Market Share by Application (2021-2026)
Figure 86. Egypt Smart Metering Chip Revenue Growth 2021-2026 ($ millions)
Figure 87. South Africa Smart Metering Chip Revenue Growth 2021-2026 ($ millions)
Figure 88. Israel Smart Metering Chip Revenue Growth 2021-2026 ($ millions)
Figure 89. Turkey Smart Metering Chip Revenue Growth 2021-2026 ($ millions)
Figure 90. GCC Countries Smart Metering Chip Revenue Growth 2021-2026 ($ millions)
Figure 91. Manufacturing Cost Structure Analysis of Smart Metering Chip in 2026
Figure 92. Manufacturing Process Analysis of Smart Metering Chip
Figure 93. Industry Chain Structure of Smart Metering Chip
Figure 94. Channels of Distribution
Figure 95. Global Smart Metering Chip Sales Market Forecast by Region (2027-2032)

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Figure 96. Global Smart Metering Chip Revenue Market Share Forecast by Region (2027-2032)
Figure 97. Global Smart Metering Chip Sales Market Share Forecast by Type (2027-2032)
Figure 98. Global Smart Metering Chip Revenue Market Share Forecast by Type (2027-2032)
Figure 99. Global Smart Metering Chip Sales Market Share Forecast by Application (2027-2032)
Figure 100. Global Smart Metering Chip Revenue Market Share Forecast by Application (2027-2032)

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