

Global Silicon EPI Wafer Market Growth 2024-2030

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Abstracts

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Silicon EPI Wafer is a semiconducting wafer used for the manufacturing of integrated circuits. At an advanced scale, the silicon wafer is blended with epitaxial growth that is deposition of a crystalline layer over a crystalline-based semiconductor substrate, hence the name Silicon EPI wafer, which acts as a high purity layer with room for the formation of thick dopes.

The global Silicon EPI Wafer market size is projected to grow from US\$ 3056.9 million in 2023 to US\$ 4626.9 million in 2030; it is expected to grow at a CAGR of 6.1% from 2024 to 2030.

LP Information, Inc. (LPI) 'newest research report, the "Silicon EPI Wafer Industry Forecast" looks at past sales and reviews total world Silicon EPI Wafer sales in 2023, providing a comprehensive analysis by region and market sector of projected Silicon EPI Wafer sales for 2024 through 2030. With Silicon EPI Wafer sales broken down by region, market sector and sub-sector, this report provides a detailed analysis in US\$ millions of the world Silicon EPI Wafer industry.

This Insight Report provides a comprehensive analysis of the global Silicon EPI Wafer landscape and highlights key trends related to product segmentation, company formation, revenue, and market share, latest development, and M&A activity. This report also analyzes the strategies of leading global companies with a focus on Silicon EPI Wafer portfolios and capabilities, market entry strategies, market positions, and geographic footprints, to better understand these firms' unique position in an accelerating global Silicon EPI Wafer market.

This Insight Report evaluates the key market trends, drivers, and affecting factors



shaping the global outlook for Silicon EPI Wafer and breaks down the forecast by Type, by Application, geography, and market size to highlight emerging pockets of opportunity. With a transparent methodology based on hundreds of bottom-up qualitative and quantitative market inputs, this study forecast offers a highly nuanced view of the current state and future trajectory in the global Silicon EPI Wafer.

Following a strong growth of 26.2 percent in the year 2021, WSTS revised it down to a single digit growth for the worldwide semiconductor market in 2022 with a total size of US\$580 billion, up 4.4 percent. WSTS lowered growth estimation as inflation rises and end markets seeing weaker demand, especially those exposed to consumer spending. While some major categories are still double-digit year-over-year growth in 2022, led by Analog with 20.8 percent, Sensors with 16.3 percent, and Logic with 14.5 percent growth. Memory declined with 12.6 percent year over year. In 2022, all geographical regions showed double-digit growth except Asia Pacific. The largest region, Asia Pacific, declined 2.0 percent. Sales in the Americas were US\$142.1 billion, up 17.0% year-on-year, sales in Europe were US\$53.8 billion, up 12.6% year-on-year, and sales in Japan were US\$48.1 billion, up 10.0% year-on-year. However, sales in the largest Asia-Pacific region were US\$336.2 billion, down 2.0% year-on-year.

This report presents a comprehensive overview, market shares, and growth opportunities of Silicon EPI Wafer market by product type, application, key manufacturers and key regions and countries.

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200	men	tatinr	ı nv	TVMA
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300 mm

200 mm

Below 150 mm

Others

Segmentation by application

Memory

Logic/MPU



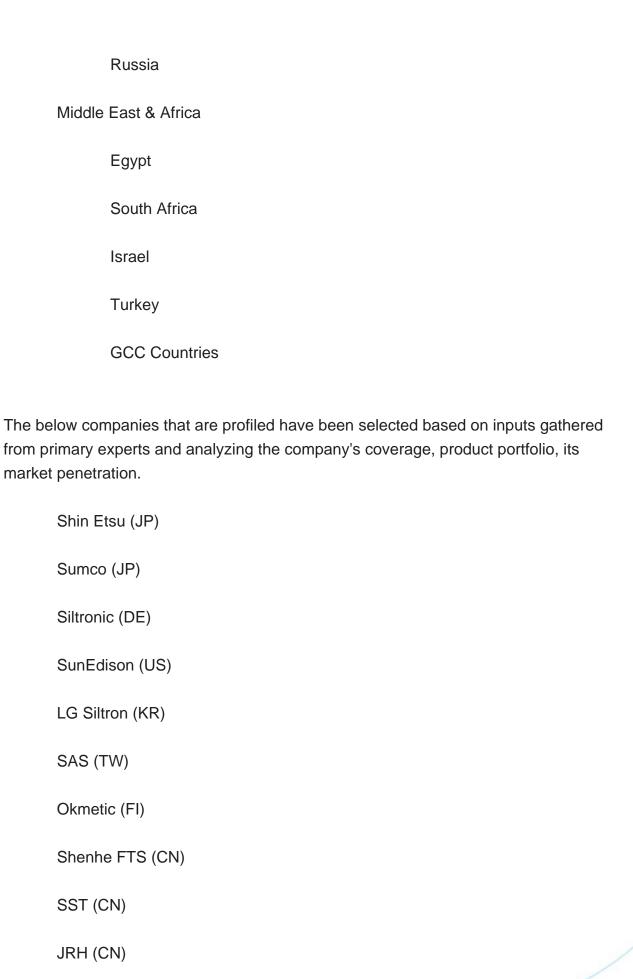
Others

Thi

nis report also splits the market by region:			
Americas			
	United States		
	Canada		
	Mexico		
	Brazil		
APAC			
	China		
	Japan		
	Korea		
	Southeast Asia		
	India		
	Australia		
Europe			
	Germany		
	France		
	UK		

Italy







MCL (CN)
GRITEK (CN)
Wafer Works (TW)
Zhonghuan Huanou (CN)
Simgui (CN)

Key Questions Addressed in this Report

What is the 10-year outlook for the global Silicon EPI Wafer market?

What factors are driving Silicon EPI Wafer market growth, globally and by region?

Which technologies are poised for the fastest growth by market and region?

How do Silicon EPI Wafer market opportunities vary by end market size?

How does Silicon EPI Wafer break out type, application?



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