

Global Silicon Carbide Wafer Lapping and Polishing Machine Market Growth 2026-2032

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Abstracts

The global Silicon Carbide Wafer Lapping and Polishing Machine market size is predicted to grow from US\$ 524 million in 2025 to US\$ 1630 million in 2032; it is expected to grow at a CAGR of 17.9% from 2026 to 2032.

A Silicon Carbide (SiC) Wafer Lapping and Polishing Machine is specialized equipment used in the semiconductor manufacturing process to finish Silicon Carbide wafers. These machines perform a precise lapping (grinding) and polishing process to achieve the desired surface quality, thickness, and flatness of SiC wafers. SiC is a wide-bandgap semiconductor material that is widely used in power electronics, automotive, and renewable energy applications due to its excellent properties like high thermal conductivity, high breakdown voltage, and high efficiency. The lapping and polishing process is crucial for ensuring the wafers meet stringent requirements for device performance and yield in these advanced applications.

Silicon Carbide (SiC) wafer lapping and polishing machines play a crucial role in the production of high-quality SiC wafers used in various semiconductor applications. SiC is a wide-bandgap semiconductor material known for its superior properties, such as high thermal conductivity, high breakdown voltage, and excellent chemical stability, making it highly suitable for applications in power electronics, automotive, renewable energy, and telecommunications.

The global market for SiC wafer lapping and polishing machines can be broadly classified into two categories based on the type of machine: CMP Polishing Machines and Wafer Grinding Machines. These machines are essential for the production of wafers with precise thickness, flatness, and surface finish, which are critical in ensuring the functionality of SiC-based devices.

In terms of wafer size, the global market is divided into two primary segments: 6 inches and Below and 8 inches and Above. The 6 inches and Below segment represents the major share of the market, accounting for approximately 70% of the total demand. This is due to the dominance of 6-inch and smaller wafers in high-power applications, such as electric vehicles (EVs), industrial power supplies, and renewable energy systems.

The Asia-Pacific (APAC) region is the largest consumer of SiC wafer lapping and polishing machines, with an estimated market share of 56%. This can be attributed to the strong presence of semiconductor manufacturers in countries such as Japan, China, Taiwan, and South Korea, which are major hubs for SiC wafer production.

Market Dynamics

Market Drivers

Increasing Demand for Silicon Carbide in Power Electronics One of the key drivers of the SiC wafer lapping and polishing machine market is the growing demand for SiC in power electronics. SiC is widely used in power devices such as power MOSFETs, diodes, and IGBTs (Insulated-Gate Bipolar Transistors), which are essential components in applications like electric vehicles (EVs), solar inverters, power supplies, and industrial motor drives. As the global demand for power electronics increases, so does the need for high-quality SiC wafers, driving the demand for wafer lapping and polishing equipment.

Growth in Electric Vehicle (EV) Production The rise of electric vehicles is one of the most significant factors influencing the demand for SiC wafers. SiC-based devices offer significant advantages over traditional silicon (Si) components in electric vehicles due to their higher efficiency, faster switching times, and better thermal performance. As automakers increasingly shift towards EV production, the demand for SiC wafers and the corresponding polishing and grinding machines is expected to grow.

Advancements in Semiconductor Manufacturing Technologies Ongoing advancements in semiconductor manufacturing technologies are improving the performance and yield of SiC wafer production. The development of high-precision CMP polishing machines and wafer grinding machines that offer better surface finishes, reduced defects, and greater wafer uniformity is expanding the market. These advancements also help reduce the cost of production, making SiC wafers more accessible to a broader range of industries.

Rising Adoption of Renewable Energy Technologies SiC wafers are also widely used in renewable energy technologies such as solar inverters and wind turbine controllers. The push for cleaner energy sources globally is driving the growth of the SiC wafer market. As renewable energy continues to grow in importance, the demand for high-quality SiC wafers and the associated lapping and polishing machines is expected to rise.

Regional Manufacturing Hubs in Asia-Pacific Asia-Pacific continues to be the largest consumer of SiC wafer lapping and polishing machines, with countries like Japan, China, South Korea, and Taiwan leading the way in semiconductor manufacturing. The region's dominance in semiconductor production and the growing demand for SiC in power electronics further contribute to the market's expansion in this region.

Market Restraints

High Initial Investment Costs One of the major challenges in the adoption of SiC wafer lapping and polishing machines is their high initial investment cost. The complexity of manufacturing high-quality SiC wafers requires sophisticated equipment that can be quite expensive. Small and medium-sized enterprises (SMEs) may find it difficult to justify such high initial capital expenditures, limiting the adoption of these machines in certain regions or companies.

Technological Challenges in Achieving High-Quality Wafers While SiC has superior properties, it is also more difficult to work with compared to traditional silicon wafers. Achieving the desired surface finish, flatness, and thickness in SiC wafers requires highly specialized equipment and expertise. The need for constant innovation in wafer lapping and polishing technologies to meet these challenges could increase production costs and act as a barrier to market growth.

Competition from Alternative Materials While SiC offers many advantages over traditional silicon in power electronics and renewable energy applications, other wide-bandgap materials, such as gallium nitride (GaN), are also emerging as alternatives. GaN has gained traction in some sectors, particularly in RF (radio frequency) and high-frequency applications. This growing competition from alternative materials could limit the growth potential of the SiC wafer market and, by extension, the market for lapping and polishing equipment.

Volatility in Raw Material Prices The production of SiC wafers relies on the availability and cost of high-quality raw materials such as silicon carbide powder. Price fluctuations

in these materials could affect the overall cost structure of SiC wafer production and the associated equipment, potentially impacting the profitability of manufacturers and hindering the market's growth.

Future Outlook

The market for Silicon Carbide Wafer Lapping and Polishing Machines is expected to grow steadily over the next several years. The continued demand for power electronics, driven by the rise of electric vehicles, renewable energy, and advanced manufacturing technologies, will be the primary driver of this growth. While the high initial investment cost and competition from alternative materials may pose challenges, the growing importance of SiC in next-generation electronics and power systems is likely to offset these constraints.

Key Trends to Watch:

Miniaturization and Precision: With the increasing complexity of SiC-based power devices, there is a growing need for more precise and miniaturized wafer lapping and polishing machines. Manufacturers are investing in innovations that can produce smaller, more accurate wafers for next-generation semiconductor devices.

Integration of Automation: Automation in the lapping and polishing processes is expected to become more prevalent, helping to improve consistency, reduce human error, and lower overall operational costs.

Sustainability: As global pressure for sustainable manufacturing practices increases, companies will focus on creating more energy-efficient and environmentally friendly equipment, helping to minimize waste and reduce the carbon footprint of SiC wafer production.

In conclusion, the Silicon Carbide Wafer Lapping and Polishing Machine market is poised for continued growth, driven by demand from the power electronics, automotive, and renewable energy sectors. The APAC region will remain the dominant market, while innovations in wafer processing technologies and equipment will play a critical role in meeting the growing needs of the industry.

LP Information, Inc. (LPI) ' newest research report, the "Silicon Carbide Wafer Lapping and Polishing Machine Industry Forecast" looks at past sales and reviews total world Silicon Carbide Wafer Lapping and Polishing Machine sales in 2025, providing a

comprehensive analysis by region and market sector of projected Silicon Carbide Wafer Lapping and Polishing Machine sales for 2026 through 2032. With Silicon Carbide Wafer Lapping and Polishing Machine sales broken down by region, market sector and sub-sector, this report provides a detailed analysis in US\$ millions of the world Silicon Carbide Wafer Lapping and Polishing Machine industry.

This Insight Report provides a comprehensive analysis of the global Silicon Carbide Wafer Lapping and Polishing Machine landscape and highlights key trends related to product segmentation, company formation, revenue, and market share, latest development, and M&A activity. This report also analyzes the strategies of leading global companies with a focus on Silicon Carbide Wafer Lapping and Polishing Machine portfolios and capabilities, market entry strategies, market positions, and geographic footprints, to better understand these firms' unique position in an accelerating global Silicon Carbide Wafer Lapping and Polishing Machine market.

This Insight Report evaluates the key market trends, drivers, and affecting factors shaping the global outlook for Silicon Carbide Wafer Lapping and Polishing Machine and breaks down the forecast by Type, by Application, geography, and market size to highlight emerging pockets of opportunity. With a transparent methodology based on hundreds of bottom-up qualitative and quantitative market inputs, this study forecast offers a highly nuanced view of the current state and future trajectory in the global Silicon Carbide Wafer Lapping and Polishing Machine.

This report presents a comprehensive overview, market shares, and growth opportunities of Silicon Carbide Wafer Lapping and Polishing Machine market by product type, application, key manufacturers and key regions and countries.

Segmentation by Type:

CMP Polishing Machines

Wafer Grinding Machines

Segmentation by Application:

6 inches and Below

8 inches and Above

This report also splits the market by region:

Americas

United States

Canada

Mexico

Brazil

APAC

China

Japan

Korea

Southeast Asia

India

Australia

Europe

Germany

France

UK

Italy

Russia

Middle East & Africa

Egypt

South Africa

Israel

Turkey

GCC Countries

The below companies that are profiled have been selected based on inputs gathered from primary experts and analysing the company's coverage, product portfolio, its market penetration.

Disco

TSD

TOKYO SEIMITSU

Engis Corporation

Okamoto Semiconductor Equipment Division

Revasum

Koyo Machinery

G&N

Applied Materials

Key Questions Addressed in this Report

What is the 10-year outlook for the global Silicon Carbide Wafer Lapping and Polishing Machine market?

What factors are driving Silicon Carbide Wafer Lapping and Polishing Machine market growth, globally and by region?

Which technologies are poised for the fastest growth by market and region?

How do Silicon Carbide Wafer Lapping and Polishing Machine market opportunities vary by end market size?

How does Silicon Carbide Wafer Lapping and Polishing Machine break out by Type, by Application?

Contents

1 SCOPE OF THE REPORT

- 1.1 Market Introduction
- 1.2 Years Considered
- 1.3 Research Objectives
- 1.4 Market Research Methodology
- 1.5 Research Process and Data Source
- 1.6 Economic Indicators
- 1.7 Currency Considered
- 1.8 Market Estimation Caveats

2 EXECUTIVE SUMMARY

2.1 World Market Overview

- 2.1.1 Global Silicon Carbide Wafer Lapping and Polishing Machine Annual Sales 2021-2032

- 2.1.2 World Current & Future Analysis for Silicon Carbide Wafer Lapping and Polishing Machine by Geographic Region, 2021, 2025 & 2032

- 2.1.3 World Current & Future Analysis for Silicon Carbide Wafer Lapping and Polishing Machine by Country/Region, 2021, 2025 & 2032

2.2 Silicon Carbide Wafer Lapping and Polishing Machine Segment by Type

- 2.2.1 CMP Polishing Machines

- 2.2.2 Wafer Grinding Machines

- 2.2.3 Silicon Carbide Wafer Lapping and Polishing Machine Sales by Type

- 2.2.3.1 Global Silicon Carbide Wafer Lapping and Polishing Machine Sales Market Share by Type (2021-2026)

- 2.2.3.2 Global Silicon Carbide Wafer Lapping and Polishing Machine Revenue and Market Share by Type (2021-2026)

- 2.2.3.3 Global Silicon Carbide Wafer Lapping and Polishing Machine Sale Price by Type (2021-2026)

2.3 Silicon Carbide Wafer Lapping and Polishing Machine Segment by Application

- 2.3.1 6 inches and Below

- 2.3.2 8 inches and Above

- 2.3.3 Silicon Carbide Wafer Lapping and Polishing Machine Sales by Application

- 2.3.3.1 Global Silicon Carbide Wafer Lapping and Polishing Machine Sale Market Share by Application (2021-2026)

- 2.3.3.2 Global Silicon Carbide Wafer Lapping and Polishing Machine Revenue and

Market Share by Application (2021-2026)

2.3.3.3 Global Silicon Carbide Wafer Lapping and Polishing Machine Sale Price by Application (2021-2026)

3 GLOBAL BY COMPANY

3.1 Global Silicon Carbide Wafer Lapping and Polishing Machine Breakdown Data by Company

3.1.1 Global Silicon Carbide Wafer Lapping and Polishing Machine Annual Sales by Company (2021-2026)

3.1.2 Global Silicon Carbide Wafer Lapping and Polishing Machine Sales Market Share by Company (2021-2026)

3.2 Global Silicon Carbide Wafer Lapping and Polishing Machine Annual Revenue by Company (2021-2026)

3.2.1 Global Silicon Carbide Wafer Lapping and Polishing Machine Revenue by Company (2021-2026)

3.2.2 Global Silicon Carbide Wafer Lapping and Polishing Machine Revenue Market Share by Company (2021-2026)

3.3 Global Silicon Carbide Wafer Lapping and Polishing Machine Sale Price by Company

3.4 Key Manufacturers Silicon Carbide Wafer Lapping and Polishing Machine Producing Area Distribution, Sales Area, Product Type

3.4.1 Key Manufacturers Silicon Carbide Wafer Lapping and Polishing Machine Product Location Distribution

3.4.2 Players Silicon Carbide Wafer Lapping and Polishing Machine Products Offered

3.5 Market Concentration Rate Analysis

3.5.1 Competition Landscape Analysis

3.5.2 Concentration Ratio (CR3, CR5 and CR10) & (2024-2026)

3.6 New Products and Potential Entrants

3.7 Market M&A Activity & Strategy

4 WORLD HISTORIC REVIEW FOR SILICON CARBIDE WAFER LAPPING AND POLISHING MACHINE BY GEOGRAPHIC REGION

4.1 World Historic Silicon Carbide Wafer Lapping and Polishing Machine Market Size by Geographic Region (2021-2026)

4.1.1 Global Silicon Carbide Wafer Lapping and Polishing Machine Annual Sales by Geographic Region (2021-2026)

4.1.2 Global Silicon Carbide Wafer Lapping and Polishing Machine Annual Revenue

by Geographic Region (2021-2026)

4.2 World Historic Silicon Carbide Wafer Lapping and Polishing Machine Market Size by Country/Region (2021-2026)

4.2.1 Global Silicon Carbide Wafer Lapping and Polishing Machine Annual Sales by Country/Region (2021-2026)

4.2.2 Global Silicon Carbide Wafer Lapping and Polishing Machine Annual Revenue by Country/Region (2021-2026)

4.3 Americas Silicon Carbide Wafer Lapping and Polishing Machine Sales Growth

4.4 APAC Silicon Carbide Wafer Lapping and Polishing Machine Sales Growth

4.5 Europe Silicon Carbide Wafer Lapping and Polishing Machine Sales Growth

4.6 Middle East & Africa Silicon Carbide Wafer Lapping and Polishing Machine Sales Growth

5 AMERICAS

5.1 Americas Silicon Carbide Wafer Lapping and Polishing Machine Sales by Country

5.1.1 Americas Silicon Carbide Wafer Lapping and Polishing Machine Sales by Country (2021-2026)

5.1.2 Americas Silicon Carbide Wafer Lapping and Polishing Machine Revenue by Country (2021-2026)

5.2 Americas Silicon Carbide Wafer Lapping and Polishing Machine Sales by Type (2021-2026)

5.3 Americas Silicon Carbide Wafer Lapping and Polishing Machine Sales by Application (2021-2026)

5.4 United States

5.5 Canada

5.6 Mexico

5.7 Brazil

6 APAC

6.1 APAC Silicon Carbide Wafer Lapping and Polishing Machine Sales by Region

6.1.1 APAC Silicon Carbide Wafer Lapping and Polishing Machine Sales by Region (2021-2026)

6.1.2 APAC Silicon Carbide Wafer Lapping and Polishing Machine Revenue by Region (2021-2026)

6.2 APAC Silicon Carbide Wafer Lapping and Polishing Machine Sales by Type (2021-2026)

6.3 APAC Silicon Carbide Wafer Lapping and Polishing Machine Sales by Application

(2021-2026)

6.4 China

6.5 Japan

6.6 South Korea

6.7 Southeast Asia

6.8 India

6.9 Australia

6.10 China Taiwan

7 EUROPE

7.1 Europe Silicon Carbide Wafer Lapping and Polishing Machine by Country

7.1.1 Europe Silicon Carbide Wafer Lapping and Polishing Machine Sales by Country (2021-2026)

7.1.2 Europe Silicon Carbide Wafer Lapping and Polishing Machine Revenue by Country (2021-2026)

7.2 Europe Silicon Carbide Wafer Lapping and Polishing Machine Sales by Type (2021-2026)

7.3 Europe Silicon Carbide Wafer Lapping and Polishing Machine Sales by Application (2021-2026)

7.4 Germany

7.5 France

7.6 UK

7.7 Italy

7.8 Russia

8 MIDDLE EAST & AFRICA

8.1 Middle East & Africa Silicon Carbide Wafer Lapping and Polishing Machine by Country

8.1.1 Middle East & Africa Silicon Carbide Wafer Lapping and Polishing Machine Sales by Country (2021-2026)

8.1.2 Middle East & Africa Silicon Carbide Wafer Lapping and Polishing Machine Revenue by Country (2021-2026)

8.2 Middle East & Africa Silicon Carbide Wafer Lapping and Polishing Machine Sales by Type (2021-2026)

8.3 Middle East & Africa Silicon Carbide Wafer Lapping and Polishing Machine Sales by Application (2021-2026)

8.4 Egypt

8.5 South Africa

8.6 Israel

8.7 Turkey

8.8 GCC Countries

9 MARKET DRIVERS, CHALLENGES AND TRENDS

9.1 Market Drivers & Growth Opportunities

9.2 Market Challenges & Risks

9.3 Industry Trends

10 MANUFACTURING COST STRUCTURE ANALYSIS

10.1 Raw Material and Suppliers

10.2 Manufacturing Cost Structure Analysis of Silicon Carbide Wafer Lapping and Polishing Machine

10.3 Manufacturing Process Analysis of Silicon Carbide Wafer Lapping and Polishing Machine

10.4 Industry Chain Structure of Silicon Carbide Wafer Lapping and Polishing Machine

11 MARKETING, DISTRIBUTORS AND CUSTOMER

11.1 Sales Channel

11.1.1 Direct Channels

11.1.2 Indirect Channels

11.2 Silicon Carbide Wafer Lapping and Polishing Machine Distributors

11.3 Silicon Carbide Wafer Lapping and Polishing Machine Customer

12 WORLD FORECAST REVIEW FOR SILICON CARBIDE WAFER LAPPING AND POLISHING MACHINE BY GEOGRAPHIC REGION

12.1 Global Silicon Carbide Wafer Lapping and Polishing Machine Market Size Forecast by Region

12.1.1 Global Silicon Carbide Wafer Lapping and Polishing Machine Forecast by Region (2027-2032)

12.1.2 Global Silicon Carbide Wafer Lapping and Polishing Machine Annual Revenue Forecast by Region (2027-2032)

12.2 Americas Forecast by Country (2027-2032)

12.3 APAC Forecast by Region (2027-2032)

12.4 Europe Forecast by Country (2027-2032)

12.5 Middle East & Africa Forecast by Country (2027-2032)

12.6 Global Silicon Carbide Wafer Lapping and Polishing Machine Forecast by Type (2027-2032)

12.7 Global Silicon Carbide Wafer Lapping and Polishing Machine Forecast by Application (2027-2032)

13 KEY PLAYERS ANALYSIS

13.1 Disco

13.1.1 Disco Company Information

13.1.2 Disco Silicon Carbide Wafer Lapping and Polishing Machine Product Portfolios and Specifications

13.1.3 Disco Silicon Carbide Wafer Lapping and Polishing Machine Sales, Revenue, Price and Gross Margin (2021-2026)

13.1.4 Disco Main Business Overview

13.1.5 Disco Latest Developments

13.2 TSD

13.2.1 TSD Company Information

13.2.2 TSD Silicon Carbide Wafer Lapping and Polishing Machine Product Portfolios and Specifications

13.2.3 TSD Silicon Carbide Wafer Lapping and Polishing Machine Sales, Revenue, Price and Gross Margin (2021-2026)

13.2.4 TSD Main Business Overview

13.2.5 TSD Latest Developments

13.3 TOKYO SEIMITSU

13.3.1 TOKYO SEIMITSU Company Information

13.3.2 TOKYO SEIMITSU Silicon Carbide Wafer Lapping and Polishing Machine Product Portfolios and Specifications

13.3.3 TOKYO SEIMITSU Silicon Carbide Wafer Lapping and Polishing Machine Sales, Revenue, Price and Gross Margin (2021-2026)

13.3.4 TOKYO SEIMITSU Main Business Overview

13.3.5 TOKYO SEIMITSU Latest Developments

13.4 Engis Corporation

13.4.1 Engis Corporation Company Information

13.4.2 Engis Corporation Silicon Carbide Wafer Lapping and Polishing Machine Product Portfolios and Specifications

13.4.3 Engis Corporation Silicon Carbide Wafer Lapping and Polishing Machine Sales, Revenue, Price and Gross Margin (2021-2026)

- 13.4.4 Engis Corporation Main Business Overview
- 13.4.5 Engis Corporation Latest Developments
- 13.5 Okamoto Semiconductor Equipment Division
 - 13.5.1 Okamoto Semiconductor Equipment Division Company Information
 - 13.5.2 Okamoto Semiconductor Equipment Division Silicon Carbide Wafer Lapping and Polishing Machine Product Portfolios and Specifications
 - 13.5.3 Okamoto Semiconductor Equipment Division Silicon Carbide Wafer Lapping and Polishing Machine Sales, Revenue, Price and Gross Margin (2021-2026)
 - 13.5.4 Okamoto Semiconductor Equipment Division Main Business Overview
 - 13.5.5 Okamoto Semiconductor Equipment Division Latest Developments
- 13.6 Revasum
 - 13.6.1 Revasum Company Information
 - 13.6.2 Revasum Silicon Carbide Wafer Lapping and Polishing Machine Product Portfolios and Specifications
 - 13.6.3 Revasum Silicon Carbide Wafer Lapping and Polishing Machine Sales, Revenue, Price and Gross Margin (2021-2026)
 - 13.6.4 Revasum Main Business Overview
 - 13.6.5 Revasum Latest Developments
- 13.7 Koyo Machinery
 - 13.7.1 Koyo Machinery Company Information
 - 13.7.2 Koyo Machinery Silicon Carbide Wafer Lapping and Polishing Machine Product Portfolios and Specifications
 - 13.7.3 Koyo Machinery Silicon Carbide Wafer Lapping and Polishing Machine Sales, Revenue, Price and Gross Margin (2021-2026)
 - 13.7.4 Koyo Machinery Main Business Overview
 - 13.7.5 Koyo Machinery Latest Developments
- 13.8 G&N
 - 13.8.1 G&N Company Information
 - 13.8.2 G&N Silicon Carbide Wafer Lapping and Polishing Machine Product Portfolios and Specifications
 - 13.8.3 G&N Silicon Carbide Wafer Lapping and Polishing Machine Sales, Revenue, Price and Gross Margin (2021-2026)
 - 13.8.4 G&N Main Business Overview
 - 13.8.5 G&N Latest Developments
- 13.9 Applied Materials
 - 13.9.1 Applied Materials Company Information
 - 13.9.2 Applied Materials Silicon Carbide Wafer Lapping and Polishing Machine Product Portfolios and Specifications
 - 13.9.3 Applied Materials Silicon Carbide Wafer Lapping and Polishing Machine Sales,

Revenue, Price and Gross Margin (2021-2026)

13.9.4 Applied Materials Main Business Overview

13.9.5 Applied Materials Latest Developments

14 RESEARCH FINDINGS AND CONCLUSION

List Of Tables

LIST OF TABLES

Table 1. Silicon Carbide Wafer Lapping and Polishing Machine Annual Sales CAGR by Geographic Region (2021, 2025 & 2032) & (\$ millions)

Table 2. Silicon Carbide Wafer Lapping and Polishing Machine Annual Sales CAGR by Country/Region (2021, 2025 & 2032) & (\$ millions)

Table 3. Major Players of CMP Polishing Machines

Table 4. Major Players of Wafer Grinding Machines

Table 5. Global Silicon Carbide Wafer Lapping and Polishing Machine Sales by Type (2021-2026) & (Units)

Table 6. Global Silicon Carbide Wafer Lapping and Polishing Machine Sales Market Share by Type (2021-2026)

Table 7. Global Silicon Carbide Wafer Lapping and Polishing Machine Revenue by Type (2021-2026) & (\$ million)

Table 8. Global Silicon Carbide Wafer Lapping and Polishing Machine Revenue Market Share by Type (2021-2026)

Table 9. Global Silicon Carbide Wafer Lapping and Polishing Machine Sale Price by Type (2021-2026) & (K US\$/Unit)

Table 10. Global Silicon Carbide Wafer Lapping and Polishing Machine Sale by Application (2021-2026) & (Units)

Table 11. Global Silicon Carbide Wafer Lapping and Polishing Machine Sale Market Share by Application (2021-2026)

Table 12. Global Silicon Carbide Wafer Lapping and Polishing Machine Revenue by Application (2021-2026) & (\$ million)

Table 13. Global Silicon Carbide Wafer Lapping and Polishing Machine Revenue Market Share by Application (2021-2026)

Table 14. Global Silicon Carbide Wafer Lapping and Polishing Machine Sale Price by Application (2021-2026) & (K US\$/Unit)

Table 15. Global Silicon Carbide Wafer Lapping and Polishing Machine Sales by Company (2021-2026) & (Units)

Table 16. Global Silicon Carbide Wafer Lapping and Polishing Machine Sales Market Share by Company (2021-2026)

Table 17. Global Silicon Carbide Wafer Lapping and Polishing Machine Revenue by Company (2021-2026) & (\$ millions)

Table 18. Global Silicon Carbide Wafer Lapping and Polishing Machine Revenue Market Share by Company (2021-2026)

Table 19. Global Silicon Carbide Wafer Lapping and Polishing Machine Sale Price by

Company (2021-2026) & (K US\$/Unit)

Table 20. Key Manufacturers Silicon Carbide Wafer Lapping and Polishing Machine Producing Area Distribution and Sales Area

Table 21. Players Silicon Carbide Wafer Lapping and Polishing Machine Products Offered

Table 22. Silicon Carbide Wafer Lapping and Polishing Machine Concentration Ratio (CR3, CR5 and CR10) & (2024-2026)

Table 23. New Products and Potential Entrants

Table 24. Market M&A Activity & Strategy

Table 25. Global Silicon Carbide Wafer Lapping and Polishing Machine Sales by Geographic Region (2021-2026) & (Units)

Table 26. Global Silicon Carbide Wafer Lapping and Polishing Machine Sales Market Share Geographic Region (2021-2026)

Table 27. Global Silicon Carbide Wafer Lapping and Polishing Machine Revenue by Geographic Region (2021-2026) & (\$ millions)

Table 28. Global Silicon Carbide Wafer Lapping and Polishing Machine Revenue Market Share by Geographic Region (2021-2026)

Table 29. Global Silicon Carbide Wafer Lapping and Polishing Machine Sales by Country/Region (2021-2026) & (Units)

Table 30. Global Silicon Carbide Wafer Lapping and Polishing Machine Sales Market Share by Country/Region (2021-2026)

Table 31. Global Silicon Carbide Wafer Lapping and Polishing Machine Revenue by Country/Region (2021-2026) & (\$ millions)

Table 32. Global Silicon Carbide Wafer Lapping and Polishing Machine Revenue Market Share by Country/Region (2021-2026)

Table 33. Americas Silicon Carbide Wafer Lapping and Polishing Machine Sales by Country (2021-2026) & (Units)

Table 34. Americas Silicon Carbide Wafer Lapping and Polishing Machine Sales Market Share by Country (2021-2026)

Table 35. Americas Silicon Carbide Wafer Lapping and Polishing Machine Revenue by Country (2021-2026) & (\$ millions)

Table 36. Americas Silicon Carbide Wafer Lapping and Polishing Machine Sales by Type (2021-2026) & (Units)

Table 37. Americas Silicon Carbide Wafer Lapping and Polishing Machine Sales by Application (2021-2026) & (Units)

Table 38. APAC Silicon Carbide Wafer Lapping and Polishing Machine Sales by Region (2021-2026) & (Units)

Table 39. APAC Silicon Carbide Wafer Lapping and Polishing Machine Sales Market Share by Region (2021-2026)

Table 40. APAC Silicon Carbide Wafer Lapping and Polishing Machine Revenue by Region (2021-2026) & (\$ millions)

Table 41. APAC Silicon Carbide Wafer Lapping and Polishing Machine Sales by Type (2021-2026) & (Units)

Table 42. APAC Silicon Carbide Wafer Lapping and Polishing Machine Sales by Application (2021-2026) & (Units)

Table 43. Europe Silicon Carbide Wafer Lapping and Polishing Machine Sales by Country (2021-2026) & (Units)

Table 44. Europe Silicon Carbide Wafer Lapping and Polishing Machine Revenue by Country (2021-2026) & (\$ millions)

Table 45. Europe Silicon Carbide Wafer Lapping and Polishing Machine Sales by Type (2021-2026) & (Units)

Table 46. Europe Silicon Carbide Wafer Lapping and Polishing Machine Sales by Application (2021-2026) & (Units)

Table 47. Middle East & Africa Silicon Carbide Wafer Lapping and Polishing Machine Sales by Country (2021-2026) & (Units)

Table 48. Middle East & Africa Silicon Carbide Wafer Lapping and Polishing Machine Revenue Market Share by Country (2021-2026)

Table 49. Middle East & Africa Silicon Carbide Wafer Lapping and Polishing Machine Sales by Type (2021-2026) & (Units)

Table 50. Middle East & Africa Silicon Carbide Wafer Lapping and Polishing Machine Sales by Application (2021-2026) & (Units)

Table 51. Key Market Drivers & Growth Opportunities of Silicon Carbide Wafer Lapping and Polishing Machine

Table 52. Key Market Challenges & Risks of Silicon Carbide Wafer Lapping and Polishing Machine

Table 53. Key Industry Trends of Silicon Carbide Wafer Lapping and Polishing Machine

Table 54. Silicon Carbide Wafer Lapping and Polishing Machine Raw Material

Table 55. Key Suppliers of Raw Materials

Table 56. Silicon Carbide Wafer Lapping and Polishing Machine Distributors List

Table 57. Silicon Carbide Wafer Lapping and Polishing Machine Customer List

Table 58. Global Silicon Carbide Wafer Lapping and Polishing Machine Sales Forecast by Region (2027-2032) & (Units)

Table 59. Global Silicon Carbide Wafer Lapping and Polishing Machine Revenue Forecast by Region (2027-2032) & (\$ millions)

Table 60. Americas Silicon Carbide Wafer Lapping and Polishing Machine Sales Forecast by Country (2027-2032) & (Units)

Table 61. Americas Silicon Carbide Wafer Lapping and Polishing Machine Annual Revenue Forecast by Country (2027-2032) & (\$ millions)

- Table 62. APAC Silicon Carbide Wafer Lapping and Polishing Machine Sales Forecast by Region (2027-2032) & (Units)
- Table 63. APAC Silicon Carbide Wafer Lapping and Polishing Machine Annual Revenue Forecast by Region (2027-2032) & (\$ millions)
- Table 64. Europe Silicon Carbide Wafer Lapping and Polishing Machine Sales Forecast by Country (2027-2032) & (Units)
- Table 65. Europe Silicon Carbide Wafer Lapping and Polishing Machine Revenue Forecast by Country (2027-2032) & (\$ millions)
- Table 66. Middle East & Africa Silicon Carbide Wafer Lapping and Polishing Machine Sales Forecast by Country (2027-2032) & (Units)
- Table 67. Middle East & Africa Silicon Carbide Wafer Lapping and Polishing Machine Revenue Forecast by Country (2027-2032) & (\$ millions)
- Table 68. Global Silicon Carbide Wafer Lapping and Polishing Machine Sales Forecast by Type (2027-2032) & (Units)
- Table 69. Global Silicon Carbide Wafer Lapping and Polishing Machine Revenue Forecast by Type (2027-2032) & (\$ millions)
- Table 70. Global Silicon Carbide Wafer Lapping and Polishing Machine Sales Forecast by Application (2027-2032) & (Units)
- Table 71. Global Silicon Carbide Wafer Lapping and Polishing Machine Revenue Forecast by Application (2027-2032) & (\$ millions)
- Table 72. Disco Basic Information, Silicon Carbide Wafer Lapping and Polishing Machine Manufacturing Base, Sales Area and Its Competitors
- Table 73. Disco Silicon Carbide Wafer Lapping and Polishing Machine Product Portfolios and Specifications
- Table 74. Disco Silicon Carbide Wafer Lapping and Polishing Machine Sales (Units), Revenue (\$ Million), Price (K US\$/Unit) and Gross Margin (2021-2026)
- Table 75. Disco Main Business
- Table 76. Disco Latest Developments
- Table 77. TSD Basic Information, Silicon Carbide Wafer Lapping and Polishing Machine Manufacturing Base, Sales Area and Its Competitors
- Table 78. TSD Silicon Carbide Wafer Lapping and Polishing Machine Product Portfolios and Specifications
- Table 79. TSD Silicon Carbide Wafer Lapping and Polishing Machine Sales (Units), Revenue (\$ Million), Price (K US\$/Unit) and Gross Margin (2021-2026)
- Table 80. TSD Main Business
- Table 81. TSD Latest Developments
- Table 82. TOKYO SEIMITSU Basic Information, Silicon Carbide Wafer Lapping and Polishing Machine Manufacturing Base, Sales Area and Its Competitors
- Table 83. TOKYO SEIMITSU Silicon Carbide Wafer Lapping and Polishing Machine

Product Portfolios and Specifications

Table 84. TOKYO SEIMITSU Silicon Carbide Wafer Lapping and Polishing Machine Sales (Units), Revenue (\$ Million), Price (K US\$/Unit) and Gross Margin (2021-2026)

Table 85. TOKYO SEIMITSU Main Business

Table 86. TOKYO SEIMITSU Latest Developments

Table 87. Engis Corporation Basic Information, Silicon Carbide Wafer Lapping and Polishing Machine Manufacturing Base, Sales Area and Its Competitors

Table 88. Engis Corporation Silicon Carbide Wafer Lapping and Polishing Machine Product Portfolios and Specifications

Table 89. Engis Corporation Silicon Carbide Wafer Lapping and Polishing Machine Sales (Units), Revenue (\$ Million), Price (K US\$/Unit) and Gross Margin (2021-2026)

Table 90. Engis Corporation Main Business

Table 91. Engis Corporation Latest Developments

Table 92. Okamoto Semiconductor Equipment Division Basic Information, Silicon Carbide Wafer Lapping and Polishing Machine Manufacturing Base, Sales Area and Its Competitors

Table 93. Okamoto Semiconductor Equipment Division Silicon Carbide Wafer Lapping and Polishing Machine Product Portfolios and Specifications

Table 94. Okamoto Semiconductor Equipment Division Silicon Carbide Wafer Lapping and Polishing Machine Sales (Units), Revenue (\$ Million), Price (K US\$/Unit) and Gross Margin (2021-2026)

Table 95. Okamoto Semiconductor Equipment Division Main Business

Table 96. Okamoto Semiconductor Equipment Division Latest Developments

Table 97. Revasum Basic Information, Silicon Carbide Wafer Lapping and Polishing Machine Manufacturing Base, Sales Area and Its Competitors

Table 98. Revasum Silicon Carbide Wafer Lapping and Polishing Machine Product Portfolios and Specifications

Table 99. Revasum Silicon Carbide Wafer Lapping and Polishing Machine Sales (Units), Revenue (\$ Million), Price (K US\$/Unit) and Gross Margin (2021-2026)

Table 100. Revasum Main Business

Table 101. Revasum Latest Developments

Table 102. Koyo Machinery Basic Information, Silicon Carbide Wafer Lapping and Polishing Machine Manufacturing Base, Sales Area and Its Competitors

Table 103. Koyo Machinery Silicon Carbide Wafer Lapping and Polishing Machine Product Portfolios and Specifications

Table 104. Koyo Machinery Silicon Carbide Wafer Lapping and Polishing Machine Sales (Units), Revenue (\$ Million), Price (K US\$/Unit) and Gross Margin (2021-2026)

Table 105. Koyo Machinery Main Business

Table 106. Koyo Machinery Latest Developments

Table 107. G&N Basic Information, Silicon Carbide Wafer Lapping and Polishing Machine Manufacturing Base, Sales Area and Its Competitors

Table 108. G&N Silicon Carbide Wafer Lapping and Polishing Machine Product Portfolios and Specifications

Table 109. G&N Silicon Carbide Wafer Lapping and Polishing Machine Sales (Units), Revenue (\$ Million), Price (K US\$/Unit) and Gross Margin (2021-2026)

Table 110. G&N Main Business

Table 111. G&N Latest Developments

Table 112. Applied Materials Basic Information, Silicon Carbide Wafer Lapping and Polishing Machine Manufacturing Base, Sales Area and Its Competitors

Table 113. Applied Materials Silicon Carbide Wafer Lapping and Polishing Machine Product Portfolios and Specifications

Table 114. Applied Materials Silicon Carbide Wafer Lapping and Polishing Machine Sales (Units), Revenue (\$ Million), Price (K US\$/Unit) and Gross Margin (2021-2026)

Table 115. Applied Materials Main Business

Table 116. Applied Materials Latest Developments

List Of Figures

LIST OF FIGURES

Figure 1. Picture of Silicon Carbide Wafer Lapping and Polishing Machine

Figure 2. Silicon Carbide Wafer Lapping and Polishing Machine Report Years Considered

Figure 3. Research Objectives

Figure 4. Research Methodology

Figure 5. Research Process and Data Source

Figure 6. Global Silicon Carbide Wafer Lapping and Polishing Machine Sales Growth Rate 2021-2032 (Units)

Figure 7. Global Silicon Carbide Wafer Lapping and Polishing Machine Revenue Growth Rate 2021-2032 (\$ millions)

Figure 8. Silicon Carbide Wafer Lapping and Polishing Machine Sales by Geographic Region (2021, 2025 & 2032) & (\$ millions)

Figure 9. Silicon Carbide Wafer Lapping and Polishing Machine Sales Market Share by Country/Region (2025)

Figure 10. Silicon Carbide Wafer Lapping and Polishing Machine Sales Market Share by Country/Region (2021, 2025 & 2032)

Figure 11. Product Picture of CMP Polishing Machines

Figure 12. Product Picture of Wafer Grinding Machines

Figure 13. Global Silicon Carbide Wafer Lapping and Polishing Machine Sales Market Share by Type in 2026

Figure 14. Global Silicon Carbide Wafer Lapping and Polishing Machine Revenue Market Share by Type (2021-2026)

Figure 15. Silicon Carbide Wafer Lapping and Polishing Machine Consumed in 6 inches and Below

Figure 16. Global Silicon Carbide Wafer Lapping and Polishing Machine Market: 6 inches and Below (2021-2026) & (Units)

Figure 17. Silicon Carbide Wafer Lapping and Polishing Machine Consumed in 8 inches and Above

Figure 18. Global Silicon Carbide Wafer Lapping and Polishing Machine Market: 8 inches and Above (2021-2026) & (Units)

Figure 19. Global Silicon Carbide Wafer Lapping and Polishing Machine Sale Market Share by Application (2025)

Figure 20. Global Silicon Carbide Wafer Lapping and Polishing Machine Revenue Market Share by Application in 2026

Figure 21. Silicon Carbide Wafer Lapping and Polishing Machine Sales by Company in

2026 (Units)

Figure 22. Global Silicon Carbide Wafer Lapping and Polishing Machine Sales Market Share by Company in 2026

Figure 23. Silicon Carbide Wafer Lapping and Polishing Machine Revenue by Company in 2026 (\$ millions)

Figure 24. Global Silicon Carbide Wafer Lapping and Polishing Machine Revenue Market Share by Company in 2026

Figure 25. Global Silicon Carbide Wafer Lapping and Polishing Machine Sales Market Share by Geographic Region (2021-2026)

Figure 26. Global Silicon Carbide Wafer Lapping and Polishing Machine Revenue Market Share by Geographic Region in 2026

Figure 27. Americas Silicon Carbide Wafer Lapping and Polishing Machine Sales 2021-2026 (Units)

Figure 28. Americas Silicon Carbide Wafer Lapping and Polishing Machine Revenue 2021-2026 (\$ millions)

Figure 29. APAC Silicon Carbide Wafer Lapping and Polishing Machine Sales 2021-2026 (Units)

Figure 30. APAC Silicon Carbide Wafer Lapping and Polishing Machine Revenue 2021-2026 (\$ millions)

Figure 31. Europe Silicon Carbide Wafer Lapping and Polishing Machine Sales 2021-2026 (Units)

Figure 32. Europe Silicon Carbide Wafer Lapping and Polishing Machine Revenue 2021-2026 (\$ millions)

Figure 33. Middle East & Africa Silicon Carbide Wafer Lapping and Polishing Machine Sales 2021-2026 (Units)

Figure 34. Middle East & Africa Silicon Carbide Wafer Lapping and Polishing Machine Revenue 2021-2026 (\$ millions)

Figure 35. Americas Silicon Carbide Wafer Lapping and Polishing Machine Sales Market Share by Country in 2026

Figure 36. Americas Silicon Carbide Wafer Lapping and Polishing Machine Revenue Market Share by Country (2021-2026)

Figure 37. Americas Silicon Carbide Wafer Lapping and Polishing Machine Sales Market Share by Type (2021-2026)

Figure 38. Americas Silicon Carbide Wafer Lapping and Polishing Machine Sales Market Share by Application (2021-2026)

Figure 39. United States Silicon Carbide Wafer Lapping and Polishing Machine Revenue Growth 2021-2026 (\$ millions)

Figure 40. Canada Silicon Carbide Wafer Lapping and Polishing Machine Revenue Growth 2021-2026 (\$ millions)

Figure 41. Mexico Silicon Carbide Wafer Lapping and Polishing Machine Revenue Growth 2021-2026 (\$ millions)

Figure 42. Brazil Silicon Carbide Wafer Lapping and Polishing Machine Revenue Growth 2021-2026 (\$ millions)

Figure 43. APAC Silicon Carbide Wafer Lapping and Polishing Machine Sales Market Share by Region in 2026

Figure 44. APAC Silicon Carbide Wafer Lapping and Polishing Machine Revenue Market Share by Region (2021-2026)

Figure 45. APAC Silicon Carbide Wafer Lapping and Polishing Machine Sales Market Share by Type (2021-2026)

Figure 46. APAC Silicon Carbide Wafer Lapping and Polishing Machine Sales Market Share by Application (2021-2026)

Figure 47. China Silicon Carbide Wafer Lapping and Polishing Machine Revenue Growth 2021-2026 (\$ millions)

Figure 48. Japan Silicon Carbide Wafer Lapping and Polishing Machine Revenue Growth 2021-2026 (\$ millions)

Figure 49. South Korea Silicon Carbide Wafer Lapping and Polishing Machine Revenue Growth 2021-2026 (\$ millions)

Figure 50. Southeast Asia Silicon Carbide Wafer Lapping and Polishing Machine Revenue Growth 2021-2026 (\$ millions)

Figure 51. India Silicon Carbide Wafer Lapping and Polishing Machine Revenue Growth 2021-2026 (\$ millions)

Figure 52. Australia Silicon Carbide Wafer Lapping and Polishing Machine Revenue Growth 2021-2026 (\$ millions)

Figure 53. China Taiwan Silicon Carbide Wafer Lapping and Polishing Machine Revenue Growth 2021-2026 (\$ millions)

Figure 54. Europe Silicon Carbide Wafer Lapping and Polishing Machine Sales Market Share by Country in 2026

Figure 55. Europe Silicon Carbide Wafer Lapping and Polishing Machine Revenue Market Share by Country (2021-2026)

Figure 56. Europe Silicon Carbide Wafer Lapping and Polishing Machine Sales Market Share by Type (2021-2026)

Figure 57. Europe Silicon Carbide Wafer Lapping and Polishing Machine Sales Market Share by Application (2021-2026)

Figure 58. Germany Silicon Carbide Wafer Lapping and Polishing Machine Revenue Growth 2021-2026 (\$ millions)

Figure 59. France Silicon Carbide Wafer Lapping and Polishing Machine Revenue Growth 2021-2026 (\$ millions)

Figure 60. UK Silicon Carbide Wafer Lapping and Polishing Machine Revenue Growth

2021-2026 (\$ millions)

Figure 61. Italy Silicon Carbide Wafer Lapping and Polishing Machine Revenue Growth 2021-2026 (\$ millions)

Figure 62. Russia Silicon Carbide Wafer Lapping and Polishing Machine Revenue Growth 2021-2026 (\$ millions)

Figure 63. Middle East & Africa Silicon Carbide Wafer Lapping and Polishing Machine Sales Market Share by Country (2021-2026)

Figure 64. Middle East & Africa Silicon Carbide Wafer Lapping and Polishing Machine Sales Market Share by Type (2021-2026)

Figure 65. Middle East & Africa Silicon Carbide Wafer Lapping and Polishing Machine Sales Market Share by Application (2021-2026)

Figure 66. Egypt Silicon Carbide Wafer Lapping and Polishing Machine Revenue Growth 2021-2026 (\$ millions)

Figure 67. South Africa Silicon Carbide Wafer Lapping and Polishing Machine Revenue Growth 2021-2026 (\$ millions)

Figure 68. Israel Silicon Carbide Wafer Lapping and Polishing Machine Revenue Growth 2021-2026 (\$ millions)

Figure 69. Turkey Silicon Carbide Wafer Lapping and Polishing Machine Revenue Growth 2021-2026 (\$ millions)

Figure 70. GCC Countries Silicon Carbide Wafer Lapping and Polishing Machine Revenue Growth 2021-2026 (\$ millions)

Figure 71. Manufacturing Cost Structure Analysis of Silicon Carbide Wafer Lapping and Polishing Machine in 2026

Figure 72. Manufacturing Process Analysis of Silicon Carbide Wafer Lapping and Polishing Machine

Figure 73. Industry Chain Structure of Silicon Carbide Wafer Lapping and Polishing Machine

Figure 74. Channels of Distribution

Figure 75. Global Silicon Carbide Wafer Lapping and Polishing Machine Sales Market Forecast by Region (2027-2032)

Figure 76. Global Silicon Carbide Wafer Lapping and Polishing Machine Revenue Market Share Forecast by Region (2027-2032)

Figure 77. Global Silicon Carbide Wafer Lapping and Polishing Machine Sales Market Share Forecast by Type (2027-2032)

Figure 78. Global Silicon Carbide Wafer Lapping and Polishing Machine Revenue Market Share Forecast by Type (2027-2032)

Figure 79. Global Silicon Carbide Wafer Lapping and Polishing Machine Sales Market Share Forecast by Application (2027-2032)

Figure 80. Global Silicon Carbide Wafer Lapping and Polishing Machine Revenue

Market Share Forecast by Application (2027-2032)

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