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Global SiC EPI Wafer Market Growth 2025-2031

https://marketpublishers.com/r/G474B851ACC7EN.html
Date: February 2025
Pages: 120
Price: US$ 3,660.00 (Single User License)
ID: G474B851ACC7EN

Abstracts

The report requires updating with new data and is sent in 48 hours after order is placed.

The global SiC EPI Wafer market size is predicted to grow from US$ million in 2025 to US$ million in 2031; it is expected to grow at a CAGR of %from 2025 to 2031.

The main epitaxial technology for SiC epitaxy growth is chemical vapor deposition (CVD),which realizes a certain thickness and doped siliconcarbide epitaxial material through the growth of SiC epitaxy reactor step flow.With the improvement of silicon carbide power device manufacturingrequirements and withstand voltage levels,SiC epi wafer continues to develop in the direction of low defects and thick epitaxy.

According to our Semiconductor Research Center, in 2022, the global SiC wafer was valued at US$ 750 million, will grow rapidly in next six years, driven by the strong demand from electric vehicle (EV). Currently the 6 inch SiC substrates are dominating this market, and in next six years, more players will put into production the 8 inch SiC wafers. Currently the key players of SiC are mainly located headquartered United States, Europe, Japan and China, especially in China, more companies are entering the SiC market. It is predicted that, Chinese companies will play key roles in the SiC market in next ten years.

LP Information, Inc. (LPI) ' newest research report, the “SiC EPI Wafer Industry Forecast” looks at past sales and reviews total world SiC EPI Wafer sales in 2024, providing a comprehensive analysis by region and market sector of projected SiC EPI Wafer sales for 2025 through 2031. With SiC EPI Wafer sales broken down by region, market sector and sub-sector, this report provides a detailed analysis in US$ millions of the world SiC EPI Wafer industry.

This Insight Report provides a comprehensive analysis of the global SiC EPI Wafer

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landscape and highlights key trends related to product segmentation, company formation, revenue, and market share, latest development, and M&A activity. This report also analyzes the strategies of leading global companies with a focus on SiC EPI Wafer portfolios and capabilities, market entry strategies, market positions, and geographic footprints, to better understand these firms' unique position in an accelerating global SiC EPI Wafer market.

This Insight Report evaluates the key market trends, drivers, and affecting factors shaping the global outlook for SiC EPI Wafer and breaks down the forecast by Type, by Application, geography, and market size to highlight emerging pockets of opportunity. With a transparent methodology based on hundreds of bottom-up qualitative and quantitative market inputs, this study forecast offers a highly nuanced view of the current state and future trajectory in the global SiC EPI Wafer.

This report presents a comprehensive overview, market shares, and growth opportunities of SiC EPI Wafer market by product type, application, key manufacturers and key regions and countries.

Segmentation by Type:

N-Type

P-Type

Others

Segmentation by Application:

Radar

5G

Electric Vehicle

Solid State Lighting

Others

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This report also splits the market by region:

- Americas
  - United States
  - Canada
  - Mexico
  - Brazil
- APAC
  - China
  - Japan
  - Korea
  - Southeast Asia
  - India
  - Australia
- Europe
  - Germany
  - France
  - UK
  - Italy
  - Russia
- Middle East & Africa

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Egypt

South Africa

Israel

Turkey

GCC Countries

The below companies that are profiled have been selected based on inputs gathered from primary experts and analysing the company's coverage, product portfolio, its market penetration.

Cree (Wolfspeed)

II-VI Advanced Materials

Showa Denko K.K.

Epiworld intenational

SK siltron css

Siltronic AG

SweGaN AB

GlobalWafer Japan CO.Ltd.

DongGuan TIAN YU Semiconductor Technology Co., Ltd.

STMicroelectronics

Rohm Semiconductor

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Key Questions Addressed in this Report

What is the 10-year outlook for the global SiC EPI Wafer market?

What factors are driving SiC EPI Wafer market growth, globally and by region?

Which technologies are poised for the fastest growth by market and region?

How do SiC EPI Wafer market opportunities vary by end market size?

How does SiC EPI Wafer break out by Type, by Application?

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