

Global Semiconductor Packaging and Test Equipment Market Growth 2024-2030

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Abstracts

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According to our LPI (LP Information) latest study, the global Semiconductor Packaging and Test Equipment market size was valued at US\$ 11340 million in 2023. With growing demand in downstream market, the Semiconductor Packaging and Test Equipment is forecast to a readjusted size of US\$ 17300 million by 2030 with a CAGR of 6.2% during review period.

The research report highlights the growth potential of the global Semiconductor Packaging and Test Equipment market. Semiconductor Packaging and Test Equipment are expected to show stable growth in the future market. However, product differentiation, reducing costs, and supply chain optimization remain crucial for the widespread adoption of Semiconductor Packaging and Test Equipment. Market players need to invest in research and development, forge strategic partnerships, and align their offerings with evolving consumer preferences to capitalize on the immense opportunities presented by the Semiconductor Packaging and Test Equipment market.

The rapid expansion of semiconductor chip application is one of the primary factors contributing to the growth of the semiconductor packaging and test market in China. The use of semiconductor chips has expanded broadly with rising demands from various industries such as power, energy, medical, green cars, networking and telecommunications, LED lighting, automobile, consumer applications, military, aerospace and defense, motor control applications, and robotics.

In China and China Taiwan, Semiconductor Packaging and Test Equipment key players include TEL, DISCO, ASM, etc. China and China Taiwan top three manufacturers hold



a share over 40%.

In terms of product, Wafer Probe Station is the largest segment of Cobalt, with a share over 35%. And in terms of application, the largest application is OSAT, followed by IDMs.

Key Features:

The report on Semiconductor Packaging and Test Equipment market reflects various aspects and provide valuable insights into the industry.

Market Size and Growth: The research report provide an overview of the current size and growth of the Semiconductor Packaging and Test Equipment market. It may include historical data, market segmentation by Type (e.g., Wafer Probe Station, Die Bonder), and regional breakdowns.

Market Drivers and Challenges: The report can identify and analyse the factors driving the growth of the Semiconductor Packaging and Test Equipment market, such as government regulations, environmental concerns, technological advancements, and changing consumer preferences. It can also highlight the challenges faced by the industry, including infrastructure limitations, range anxiety, and high upfront costs.

Competitive Landscape: The research report provides analysis of the competitive landscape within the Semiconductor Packaging and Test Equipment market. It includes profiles of key players, their market share, strategies, and product offerings. The report can also highlight emerging players and their potential impact on the market.

Technological Developments: The research report can delve into the latest technological developments in the Semiconductor Packaging and Test Equipment industry. This include advancements in Semiconductor Packaging and Test Equipment technology, Semiconductor Packaging and Test Equipment new entrants, Semiconductor Packaging and Test Equipment new investment, and other innovations that are shaping the future of Semiconductor Packaging and Test Equipment.

Downstream Procumbent Preference: The report can shed light on customer procumbent behaviour and adoption trends in the Semiconductor Packaging and Test Equipment market. It includes factors influencing customer 'purchasing decisions, preferences for Semiconductor Packaging and Test Equipment product.



Government Policies and Incentives: The research report analyse the impact of government policies and incentives on the Semiconductor Packaging and Test Equipment market. This may include an assessment of regulatory frameworks, subsidies, tax incentives, and other measures aimed at promoting Semiconductor Packaging and Test Equipment market. The report also evaluates the effectiveness of these policies in driving market growth.

Environmental Impact and Sustainability: The research report assess the environmental impact and sustainability aspects of the Semiconductor Packaging and Test Equipment market.

Market Forecasts and Future Outlook: Based on the analysis conducted, the research report provide market forecasts and outlook for the Semiconductor Packaging and Test Equipment industry. This includes projections of market size, growth rates, regional trends, and predictions on technological advancements and policy developments.

Recommendations and Opportunities: The report conclude with recommendations for industry stakeholders, policymakers, and investors. It highlights potential opportunities for market players to capitalize on emerging trends, overcome challenges, and contribute to the growth and development of the Semiconductor Packaging and Test Equipment market.

Market Segmentation:

Semiconductor Packaging and Test Equipment market is split by Type and by Application. For the period 2019-2030, the growth among segments provides accurate calculations and forecasts for consumption value by Type, and by Application in terms of volume and value.

Segmentation by type

Wafer Probe Station

Die Bonder

Dicing Machine

Test handler



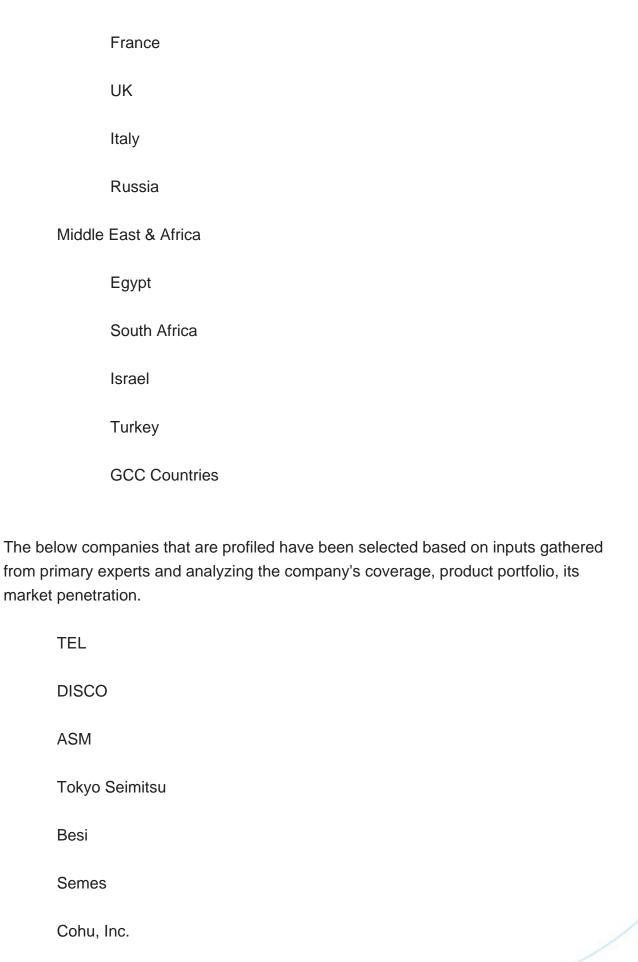
Sorter

| Segmentation | by application |
|-----------------|----------------------------------------------|
| Integra | ated Device Manufacturer (IDMs) |
| Outsou | urced Semiconductor Assembly and Test (OSAT) |
| This report als | so splits the market by region: |
| Americ | cas |
| | United States |
| | Canada |
| | Mexico |
| | Brazil |
| APAC | |
| | China |
| | Japan |
| | Korea |
| | Southeast Asia |
| | India |
| | Australia |
| _ | |

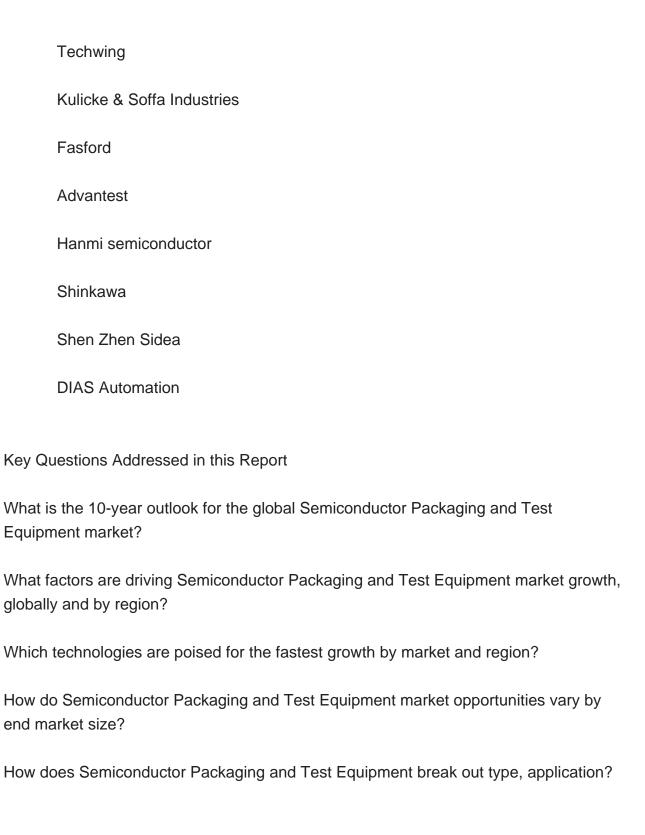
Europe

Germany











Contents

1 SCOPE OF THE REPORT

- 1.1 Market Introduction
- 1.2 Years Considered
- 1.3 Research Objectives
- 1.4 Market Research Methodology
- 1.5 Research Process and Data Source
- 1.6 Economic Indicators
- 1.7 Currency Considered
- 1.8 Market Estimation Caveats

2 EXECUTIVE SUMMARY

- 2.1 World Market Overview
 - 2.1.1 Global Semiconductor Packaging and Test Equipment Annual Sales 2019-2030
- 2.1.2 World Current & Future Analysis for Semiconductor Packaging and Test Equipment by Geographic Region, 2019, 2023 & 2030
- 2.1.3 World Current & Future Analysis for Semiconductor Packaging and Test Equipment by Country/Region, 2019, 2023 & 2030
- 2.2 Semiconductor Packaging and Test Equipment Segment by Type
 - 2.2.1 Wafer Probe Station
 - 2.2.2 Die Bonder
 - 2.2.3 Dicing Machine
 - 2.2.4 Test handler
 - 2.2.5 Sorter
- 2.3 Semiconductor Packaging and Test Equipment Sales by Type
- 2.3.1 Global Semiconductor Packaging and Test Equipment Sales Market Share by Type (2019-2024)
- 2.3.2 Global Semiconductor Packaging and Test Equipment Revenue and Market Share by Type (2019-2024)
- 2.3.3 Global Semiconductor Packaging and Test Equipment Sale Price by Type (2019-2024)
- 2.4 Semiconductor Packaging and Test Equipment Segment by Application
 - 2.4.1 Integrated Device Manufacturer (IDMs)
 - 2.4.2 Outsourced Semiconductor Assembly and Test (OSAT)
- 2.5 Semiconductor Packaging and Test Equipment Sales by Application
- 2.5.1 Global Semiconductor Packaging and Test Equipment Sale Market Share by



Application (2019-2024)

- 2.5.2 Global Semiconductor Packaging and Test Equipment Revenue and Market Share by Application (2019-2024)
- 2.5.3 Global Semiconductor Packaging and Test Equipment Sale Price by Application (2019-2024)

3 GLOBAL SEMICONDUCTOR PACKAGING AND TEST EQUIPMENT BY COMPANY

- 3.1 Global Semiconductor Packaging and Test Equipment Breakdown Data by Company
- 3.1.1 Global Semiconductor Packaging and Test Equipment Annual Sales by Company (2019-2024)
- 3.1.2 Global Semiconductor Packaging and Test Equipment Sales Market Share by Company (2019-2024)
- 3.2 Global Semiconductor Packaging and Test Equipment Annual Revenue by Company (2019-2024)
- 3.2.1 Global Semiconductor Packaging and Test Equipment Revenue by Company (2019-2024)
- 3.2.2 Global Semiconductor Packaging and Test Equipment Revenue Market Share by Company (2019-2024)
- 3.3 Global Semiconductor Packaging and Test Equipment Sale Price by Company
- 3.4 Key Manufacturers Semiconductor Packaging and Test Equipment Producing Area Distribution, Sales Area, Product Type
- 3.4.1 Key Manufacturers Semiconductor Packaging and Test Equipment Product Location Distribution
 - 3.4.2 Players Semiconductor Packaging and Test Equipment Products Offered
- 3.5 Market Concentration Rate Analysis
 - 3.5.1 Competition Landscape Analysis
 - 3.5.2 Concentration Ratio (CR3, CR5 and CR10) & (2019-2024)
- 3.6 New Products and Potential Entrants
- 3.7 Mergers & Acquisitions, Expansion

4 WORLD HISTORIC REVIEW FOR SEMICONDUCTOR PACKAGING AND TEST EQUIPMENT BY GEOGRAPHIC REGION

- 4.1 World Historic Semiconductor Packaging and Test Equipment Market Size by Geographic Region (2019-2024)
 - 4.1.1 Global Semiconductor Packaging and Test Equipment Annual Sales by



Geographic Region (2019-2024)

- 4.1.2 Global Semiconductor Packaging and Test Equipment Annual Revenue by Geographic Region (2019-2024)
- 4.2 World Historic Semiconductor Packaging and Test Equipment Market Size by Country/Region (2019-2024)
- 4.2.1 Global Semiconductor Packaging and Test Equipment Annual Sales by Country/Region (2019-2024)
- 4.2.2 Global Semiconductor Packaging and Test Equipment Annual Revenue by Country/Region (2019-2024)
- 4.3 Americas Semiconductor Packaging and Test Equipment Sales Growth
- 4.4 APAC Semiconductor Packaging and Test Equipment Sales Growth
- 4.5 Europe Semiconductor Packaging and Test Equipment Sales Growth
- 4.6 Middle East & Africa Semiconductor Packaging and Test Equipment Sales Growth

5 AMERICAS

- 5.1 Americas Semiconductor Packaging and Test Equipment Sales by Country
- 5.1.1 Americas Semiconductor Packaging and Test Equipment Sales by Country (2019-2024)
- 5.1.2 Americas Semiconductor Packaging and Test Equipment Revenue by Country (2019-2024)
- 5.2 Americas Semiconductor Packaging and Test Equipment Sales by Type
- 5.3 Americas Semiconductor Packaging and Test Equipment Sales by Application
- 5.4 United States
- 5.5 Canada
- 5.6 Mexico
- 5.7 Brazil

6 APAC

- 6.1 APAC Semiconductor Packaging and Test Equipment Sales by Region
- 6.1.1 APAC Semiconductor Packaging and Test Equipment Sales by Region (2019-2024)
- 6.1.2 APAC Semiconductor Packaging and Test Equipment Revenue by Region (2019-2024)
- 6.2 APAC Semiconductor Packaging and Test Equipment Sales by Type
- 6.3 APAC Semiconductor Packaging and Test Equipment Sales by Application
- 6.4 China
- 6.5 Japan



- 6.6 South Korea
- 6.7 Southeast Asia
- 6.8 India
- 6.9 Australia
- 6.10 China Taiwan

7 EUROPE

- 7.1 Europe Semiconductor Packaging and Test Equipment by Country
- 7.1.1 Europe Semiconductor Packaging and Test Equipment Sales by Country (2019-2024)
- 7.1.2 Europe Semiconductor Packaging and Test Equipment Revenue by Country (2019-2024)
- 7.2 Europe Semiconductor Packaging and Test Equipment Sales by Type
- 7.3 Europe Semiconductor Packaging and Test Equipment Sales by Application
- 7.4 Germany
- 7.5 France
- 7.6 UK
- 7.7 Italy
- 7.8 Russia

8 MIDDLE EAST & AFRICA

- 8.1 Middle East & Africa Semiconductor Packaging and Test Equipment by Country
- 8.1.1 Middle East & Africa Semiconductor Packaging and Test Equipment Sales by Country (2019-2024)
- 8.1.2 Middle East & Africa Semiconductor Packaging and Test Equipment Revenue by Country (2019-2024)
- 8.2 Middle East & Africa Semiconductor Packaging and Test Equipment Sales by Type
- 8.3 Middle East & Africa Semiconductor Packaging and Test Equipment Sales by Application
- 8.4 Egypt
- 8.5 South Africa
- 8.6 Israel
- 8.7 Turkey
- 8.8 GCC Countries

9 MARKET DRIVERS, CHALLENGES AND TRENDS



- 9.1 Market Drivers & Growth Opportunities
- 9.2 Market Challenges & Risks
- 9.3 Industry Trends

10 MANUFACTURING COST STRUCTURE ANALYSIS

- 10.1 Raw Material and Suppliers
- 10.2 Manufacturing Cost Structure Analysis of Semiconductor Packaging and Test Equipment
- 10.3 Manufacturing Process Analysis of Semiconductor Packaging and Test Equipment
- 10.4 Industry Chain Structure of Semiconductor Packaging and Test Equipment

11 MARKETING, DISTRIBUTORS AND CUSTOMER

- 11.1 Sales Channel
- 11.1.1 Direct Channels
- 11.1.2 Indirect Channels
- 11.2 Semiconductor Packaging and Test Equipment Distributors
- 11.3 Semiconductor Packaging and Test Equipment Customer

12 WORLD FORECAST REVIEW FOR SEMICONDUCTOR PACKAGING AND TEST EQUIPMENT BY GEOGRAPHIC REGION

- 12.1 Global Semiconductor Packaging and Test Equipment Market Size Forecast by Region
- 12.1.1 Global Semiconductor Packaging and Test Equipment Forecast by Region (2025-2030)
- 12.1.2 Global Semiconductor Packaging and Test Equipment Annual Revenue Forecast by Region (2025-2030)
- 12.2 Americas Forecast by Country
- 12.3 APAC Forecast by Region
- 12.4 Europe Forecast by Country
- 12.5 Middle East & Africa Forecast by Country
- 12.6 Global Semiconductor Packaging and Test Equipment Forecast by Type
- 12.7 Global Semiconductor Packaging and Test Equipment Forecast by Application

13 KEY PLAYERS ANALYSIS

13.1 TEL



- 13.1.1 TEL Company Information
- 13.1.2 TEL Semiconductor Packaging and Test Equipment Product Portfolios and Specifications
- 13.1.3 TEL Semiconductor Packaging and Test Equipment Sales, Revenue, Price and Gross Margin (2019-2024)
 - 13.1.4 TEL Main Business Overview
 - 13.1.5 TEL Latest Developments
- 13.2 DISCO
 - 13.2.1 DISCO Company Information
- 13.2.2 DISCO Semiconductor Packaging and Test Equipment Product Portfolios and Specifications
- 13.2.3 DISCO Semiconductor Packaging and Test Equipment Sales, Revenue, Price and Gross Margin (2019-2024)
 - 13.2.4 DISCO Main Business Overview
 - 13.2.5 DISCO Latest Developments
- 13.3 ASM
 - 13.3.1 ASM Company Information
- 13.3.2 ASM Semiconductor Packaging and Test Equipment Product Portfolios and Specifications
- 13.3.3 ASM Semiconductor Packaging and Test Equipment Sales, Revenue, Price and Gross Margin (2019-2024)
 - 13.3.4 ASM Main Business Overview
 - 13.3.5 ASM Latest Developments
- 13.4 Tokyo Seimitsu
 - 13.4.1 Tokyo Seimitsu Company Information
- 13.4.2 Tokyo Seimitsu Semiconductor Packaging and Test Equipment Product Portfolios and Specifications
 - 13.4.3 Tokyo Seimitsu Semiconductor Packaging and Test Equipment Sales,
- Revenue, Price and Gross Margin (2019-2024)
 - 13.4.4 Tokyo Seimitsu Main Business Overview
 - 13.4.5 Tokyo Seimitsu Latest Developments
- 13.5 Besi
 - 13.5.1 Besi Company Information
- 13.5.2 Besi Semiconductor Packaging and Test Equipment Product Portfolios and Specifications
- 13.5.3 Besi Semiconductor Packaging and Test Equipment Sales, Revenue, Price and Gross Margin (2019-2024)
 - 13.5.4 Besi Main Business Overview
 - 13.5.5 Besi Latest Developments



- 13.6 Semes
 - 13.6.1 Semes Company Information
- 13.6.2 Semes Semiconductor Packaging and Test Equipment Product Portfolios and Specifications
- 13.6.3 Semes Semiconductor Packaging and Test Equipment Sales, Revenue, Price and Gross Margin (2019-2024)
 - 13.6.4 Semes Main Business Overview
 - 13.6.5 Semes Latest Developments
- 13.7 Cohu, Inc.
 - 13.7.1 Cohu, Inc. Company Information
- 13.7.2 Cohu, Inc. Semiconductor Packaging and Test Equipment Product Portfolios and Specifications
- 13.7.3 Cohu, Inc. Semiconductor Packaging and Test Equipment Sales, Revenue, Price and Gross Margin (2019-2024)
 - 13.7.4 Cohu, Inc. Main Business Overview
 - 13.7.5 Cohu, Inc. Latest Developments
- 13.8 Techwing
 - 13.8.1 Techwing Company Information
- 13.8.2 Techwing Semiconductor Packaging and Test Equipment Product Portfolios and Specifications
- 13.8.3 Techwing Semiconductor Packaging and Test Equipment Sales, Revenue, Price and Gross Margin (2019-2024)
 - 13.8.4 Techwing Main Business Overview
 - 13.8.5 Techwing Latest Developments
- 13.9 Kulicke & Soffa Industries
 - 13.9.1 Kulicke & Soffa Industries Company Information
- 13.9.2 Kulicke & Soffa Industries Semiconductor Packaging and Test Equipment Product Portfolios and Specifications
- 13.9.3 Kulicke & Soffa Industries Semiconductor Packaging and Test Equipment Sales, Revenue, Price and Gross Margin (2019-2024)
 - 13.9.4 Kulicke & Soffa Industries Main Business Overview
 - 13.9.5 Kulicke & Soffa Industries Latest Developments
- 13.10 Fasford
 - 13.10.1 Fasford Company Information
- 13.10.2 Fasford Semiconductor Packaging and Test Equipment Product Portfolios and Specifications
- 13.10.3 Fasford Semiconductor Packaging and Test Equipment Sales, Revenue, Price and Gross Margin (2019-2024)
 - 13.10.4 Fasford Main Business Overview



- 13.10.5 Fasford Latest Developments
- 13.11 Advantest
- 13.11.1 Advantest Company Information
- 13.11.2 Advantest Semiconductor Packaging and Test Equipment Product Portfolios and Specifications
- 13.11.3 Advantest Semiconductor Packaging and Test Equipment Sales, Revenue, Price and Gross Margin (2019-2024)
 - 13.11.4 Advantest Main Business Overview
- 13.11.5 Advantest Latest Developments
- 13.12 Hanmi semiconductor
- 13.12.1 Hanmi semiconductor Company Information
- 13.12.2 Hanmi semiconductor Semiconductor Packaging and Test Equipment Product Portfolios and Specifications
- 13.12.3 Hanmi semiconductor Semiconductor Packaging and Test Equipment Sales, Revenue, Price and Gross Margin (2019-2024)
 - 13.12.4 Hanmi semiconductor Main Business Overview
 - 13.12.5 Hanmi semiconductor Latest Developments
- 13.13 Shinkawa
 - 13.13.1 Shinkawa Company Information
- 13.13.2 Shinkawa Semiconductor Packaging and Test Equipment Product Portfolios and Specifications
- 13.13.3 Shinkawa Semiconductor Packaging and Test Equipment Sales, Revenue, Price and Gross Margin (2019-2024)
 - 13.13.4 Shinkawa Main Business Overview
 - 13.13.5 Shinkawa Latest Developments
- 13.14 Shen Zhen Sidea
 - 13.14.1 Shen Zhen Sidea Company Information
- 13.14.2 Shen Zhen Sidea Semiconductor Packaging and Test Equipment Product Portfolios and Specifications
- 13.14.3 Shen Zhen Sidea Semiconductor Packaging and Test Equipment Sales, Revenue, Price and Gross Margin (2019-2024)
 - 13.14.4 Shen Zhen Sidea Main Business Overview
 - 13.14.5 Shen Zhen Sidea Latest Developments
- 13.15 DIAS Automation
 - 13.15.1 DIAS Automation Company Information
- 13.15.2 DIAS Automation Semiconductor Packaging and Test Equipment Product Portfolios and Specifications
- 13.15.3 DIAS Automation Semiconductor Packaging and Test Equipment Sales, Revenue, Price and Gross Margin (2019-2024)



13.15.4 DIAS Automation Main Business Overview13.15.5 DIAS Automation Latest Developments

14 RESEARCH FINDINGS AND CONCLUSION



List Of Tables

LIST OF TABLES

Table 1. Semiconductor Packaging and Test Equipment Annual Sales CAGR by Geographic Region (2019, 2023 & 2030) & (\$ millions)

Table 2. Semiconductor Packaging and Test Equipment Annual Sales CAGR by Country/Region (2019, 2023 & 2030) & (\$ millions)

Table 3. Major Players of Wafer Probe Station

Table 4. Major Players of Die Bonder

Table 5. Major Players of Dicing Machine

Table 6. Major Players of Test handler

Table 7. Major Players of Sorter

Table 8. Global Semiconductor Packaging and Test Equipment Sales by Type (2019-2024) & (Units)

Table 9. Global Semiconductor Packaging and Test Equipment Sales Market Share by Type (2019-2024)

Table 10. Global Semiconductor Packaging and Test Equipment Revenue by Type (2019-2024) & (\$ million)

Table 11. Global Semiconductor Packaging and Test Equipment Revenue Market Share by Type (2019-2024)

Table 12. Global Semiconductor Packaging and Test Equipment Sale Price by Type (2019-2024) & (K USD/Unit)

Table 13. Global Semiconductor Packaging and Test Equipment Sales by Application (2019-2024) & (Units)

Table 14. Global Semiconductor Packaging and Test Equipment Sales Market Share by Application (2019-2024)

Table 15. Global Semiconductor Packaging and Test Equipment Revenue by Application (2019-2024)

Table 16. Global Semiconductor Packaging and Test Equipment Revenue Market Share by Application (2019-2024)

Table 17. Global Semiconductor Packaging and Test Equipment Sale Price by Application (2019-2024) & (K USD/Unit)

Table 18. Global Semiconductor Packaging and Test Equipment Sales by Company (2019-2024) & (Units)

Table 19. Global Semiconductor Packaging and Test Equipment Sales Market Share by Company (2019-2024)

Table 20. Global Semiconductor Packaging and Test Equipment Revenue by Company (2019-2024) (\$ Millions)



- Table 21. Global Semiconductor Packaging and Test Equipment Revenue Market Share by Company (2019-2024)
- Table 22. Global Semiconductor Packaging and Test Equipment Sale Price by Company (2019-2024) & (K USD/Unit)
- Table 23. Key Manufacturers Semiconductor Packaging and Test Equipment Producing Area Distribution and Sales Area
- Table 24. Players Semiconductor Packaging and Test Equipment Products Offered
- Table 25. Semiconductor Packaging and Test Equipment Concentration Ratio (CR3, CR5 and CR10) & (2019-2024)
- Table 26. New Products and Potential Entrants
- Table 27. Mergers & Acquisitions, Expansion
- Table 28. Global Semiconductor Packaging and Test Equipment Sales by Geographic Region (2019-2024) & (Units)
- Table 29. Global Semiconductor Packaging and Test Equipment Sales Market Share Geographic Region (2019-2024)
- Table 30. Global Semiconductor Packaging and Test Equipment Revenue by Geographic Region (2019-2024) & (\$ millions)
- Table 31. Global Semiconductor Packaging and Test Equipment Revenue Market Share by Geographic Region (2019-2024)
- Table 32. Global Semiconductor Packaging and Test Equipment Sales by Country/Region (2019-2024) & (Units)
- Table 33. Global Semiconductor Packaging and Test Equipment Sales Market Share by Country/Region (2019-2024)
- Table 34. Global Semiconductor Packaging and Test Equipment Revenue by Country/Region (2019-2024) & (\$ millions)
- Table 35. Global Semiconductor Packaging and Test Equipment Revenue Market Share by Country/Region (2019-2024)
- Table 36. Americas Semiconductor Packaging and Test Equipment Sales by Country (2019-2024) & (Units)
- Table 37. Americas Semiconductor Packaging and Test Equipment Sales Market Share by Country (2019-2024)
- Table 38. Americas Semiconductor Packaging and Test Equipment Revenue by Country (2019-2024) & (\$ Millions)
- Table 39. Americas Semiconductor Packaging and Test Equipment Revenue Market Share by Country (2019-2024)
- Table 40. Americas Semiconductor Packaging and Test Equipment Sales by Type (2019-2024) & (Units)
- Table 41. Americas Semiconductor Packaging and Test Equipment Sales by Application (2019-2024) & (Units)



Table 42. APAC Semiconductor Packaging and Test Equipment Sales by Region (2019-2024) & (Units)

Table 43. APAC Semiconductor Packaging and Test Equipment Sales Market Share by Region (2019-2024)

Table 44. APAC Semiconductor Packaging and Test Equipment Revenue by Region (2019-2024) & (\$ Millions)

Table 45. APAC Semiconductor Packaging and Test Equipment Revenue Market Share by Region (2019-2024)

Table 46. APAC Semiconductor Packaging and Test Equipment Sales by Type (2019-2024) & (Units)

Table 47. APAC Semiconductor Packaging and Test Equipment Sales by Application (2019-2024) & (Units)

Table 48. Europe Semiconductor Packaging and Test Equipment Sales by Country (2019-2024) & (Units)

Table 49. Europe Semiconductor Packaging and Test Equipment Sales Market Share by Country (2019-2024)

Table 50. Europe Semiconductor Packaging and Test Equipment Revenue by Country (2019-2024) & (\$ Millions)

Table 51. Europe Semiconductor Packaging and Test Equipment Revenue Market Share by Country (2019-2024)

Table 52. Europe Semiconductor Packaging and Test Equipment Sales by Type (2019-2024) & (Units)

Table 53. Europe Semiconductor Packaging and Test Equipment Sales by Application (2019-2024) & (Units)

Table 54. Middle East & Africa Semiconductor Packaging and Test Equipment Sales by Country (2019-2024) & (Units)

Table 55. Middle East & Africa Semiconductor Packaging and Test Equipment Sales Market Share by Country (2019-2024)

Table 56. Middle East & Africa Semiconductor Packaging and Test Equipment Revenue by Country (2019-2024) & (\$ Millions)

Table 57. Middle East & Africa Semiconductor Packaging and Test Equipment Revenue Market Share by Country (2019-2024)

Table 58. Middle East & Africa Semiconductor Packaging and Test Equipment Sales by Type (2019-2024) & (Units)

Table 59. Middle East & Africa Semiconductor Packaging and Test Equipment Sales by Application (2019-2024) & (Units)

Table 60. Key Market Drivers & Growth Opportunities of Semiconductor Packaging and Test Equipment

Table 61. Key Market Challenges & Risks of Semiconductor Packaging and Test



Equipment

- Table 62. Key Industry Trends of Semiconductor Packaging and Test Equipment
- Table 63. Semiconductor Packaging and Test Equipment Raw Material
- Table 64. Key Suppliers of Raw Materials
- Table 65. Semiconductor Packaging and Test Equipment Distributors List
- Table 66. Semiconductor Packaging and Test Equipment Customer List
- Table 67. Global Semiconductor Packaging and Test Equipment Sales Forecast by Region (2025-2030) & (Units)
- Table 68. Global Semiconductor Packaging and Test Equipment Revenue Forecast by Region (2025-2030) & (\$ millions)
- Table 69. Americas Semiconductor Packaging and Test Equipment Sales Forecast by Country (2025-2030) & (Units)
- Table 70. Americas Semiconductor Packaging and Test Equipment Revenue Forecast by Country (2025-2030) & (\$ millions)
- Table 71. APAC Semiconductor Packaging and Test Equipment Sales Forecast by Region (2025-2030) & (Units)
- Table 72. APAC Semiconductor Packaging and Test Equipment Revenue Forecast by Region (2025-2030) & (\$ millions)
- Table 73. Europe Semiconductor Packaging and Test Equipment Sales Forecast by Country (2025-2030) & (Units)
- Table 74. Europe Semiconductor Packaging and Test Equipment Revenue Forecast by Country (2025-2030) & (\$ millions)
- Table 75. Middle East & Africa Semiconductor Packaging and Test Equipment Sales Forecast by Country (2025-2030) & (Units)
- Table 76. Middle East & Africa Semiconductor Packaging and Test Equipment Revenue Forecast by Country (2025-2030) & (\$ millions)
- Table 77. Global Semiconductor Packaging and Test Equipment Sales Forecast by Type (2025-2030) & (Units)
- Table 78. Global Semiconductor Packaging and Test Equipment Revenue Forecast by Type (2025-2030) & (\$ Millions)
- Table 79. Global Semiconductor Packaging and Test Equipment Sales Forecast by Application (2025-2030) & (Units)
- Table 80. Global Semiconductor Packaging and Test Equipment Revenue Forecast by Application (2025-2030) & (\$ Millions)
- Table 81. TEL Basic Information, Semiconductor Packaging and Test Equipment Manufacturing Base, Sales Area and Its Competitors
- Table 82. TEL Semiconductor Packaging and Test Equipment Product Portfolios and Specifications
- Table 83. TEL Semiconductor Packaging and Test Equipment Sales (Units), Revenue



(\$ Million), Price (K USD/Unit) and Gross Margin (2019-2024)

Table 84. TEL Main Business

Table 85. TEL Latest Developments

Table 86. DISCO Basic Information, Semiconductor Packaging and Test Equipment

Manufacturing Base, Sales Area and Its Competitors

Table 87. DISCO Semiconductor Packaging and Test Equipment Product Portfolios and Specifications

Table 88. DISCO Semiconductor Packaging and Test Equipment Sales (Units),

Revenue (\$ Million), Price (K USD/Unit) and Gross Margin (2019-2024)

Table 89. DISCO Main Business

Table 90. DISCO Latest Developments

Table 91. ASM Basic Information, Semiconductor Packaging and Test Equipment

Manufacturing Base, Sales Area and Its Competitors

Table 92. ASM Semiconductor Packaging and Test Equipment Product Portfolios and Specifications

Table 93. ASM Semiconductor Packaging and Test Equipment Sales (Units), Revenue (\$ Million), Price (K USD/Unit) and Gross Margin (2019-2024)

Table 94. ASM Main Business

Table 95. ASM Latest Developments

Table 96. Tokyo Seimitsu Basic Information, Semiconductor Packaging and Test

Equipment Manufacturing Base, Sales Area and Its Competitors

Table 97. Tokyo Seimitsu Semiconductor Packaging and Test Equipment Product

Portfolios and Specifications

Table 98. Tokyo Seimitsu Semiconductor Packaging and Test Equipment Sales (Units),

Revenue (\$ Million), Price (K USD/Unit) and Gross Margin (2019-2024)

Table 99. Tokyo Seimitsu Main Business

Table 100. Tokyo Seimitsu Latest Developments

Table 101. Besi Basic Information, Semiconductor Packaging and Test Equipment

Manufacturing Base, Sales Area and Its Competitors

Table 102. Besi Semiconductor Packaging and Test Equipment Product Portfolios and Specifications

Table 103. Besi Semiconductor Packaging and Test Equipment Sales (Units), Revenue

(\$ Million), Price (K USD/Unit) and Gross Margin (2019-2024)

Table 104. Besi Main Business

Table 105. Besi Latest Developments

Table 106. Semes Basic Information, Semiconductor Packaging and Test Equipment

Manufacturing Base, Sales Area and Its Competitors

Table 107. Semes Semiconductor Packaging and Test Equipment Product Portfolios and Specifications



Table 108. Semes Semiconductor Packaging and Test Equipment Sales (Units),

Revenue (\$ Million), Price (K USD/Unit) and Gross Margin (2019-2024)

Table 109. Semes Main Business

Table 110. Semes Latest Developments

Table 111. Cohu, Inc. Basic Information, Semiconductor Packaging and Test Equipment Manufacturing Base, Sales Area and Its Competitors

Table 112. Cohu, Inc. Semiconductor Packaging and Test Equipment Product Portfolios and Specifications

Table 113. Cohu, Inc. Semiconductor Packaging and Test Equipment Sales (Units),

Revenue (\$ Million), Price (K USD/Unit) and Gross Margin (2019-2024)

Table 114. Cohu, Inc. Main Business

Table 115. Cohu, Inc. Latest Developments

Table 116. Techwing Basic Information, Semiconductor Packaging and Test Equipment Manufacturing Base, Sales Area and Its Competitors

Table 117. Techwing Semiconductor Packaging and Test Equipment Product Portfolios and Specifications

Table 118. Techwing Semiconductor Packaging and Test Equipment Sales (Units),

Revenue (\$ Million), Price (K USD/Unit) and Gross Margin (2019-2024)

Table 119. Techwing Main Business

Table 120. Techwing Latest Developments

Table 121. Kulicke & Soffa Industries Basic Information, Semiconductor Packaging and Test Equipment Manufacturing Base, Sales Area and Its Competitors

Table 122. Kulicke & Soffa Industries Semiconductor Packaging and Test Equipment Product Portfolios and Specifications

Table 123. Kulicke & Soffa Industries Semiconductor Packaging and Test Equipment Sales (Units), Revenue (\$ Million), Price (K USD/Unit) and Gross Margin (2019-2024)

Table 124. Kulicke & Soffa Industries Main Business

Table 125. Kulicke & Soffa Industries Latest Developments

Table 126. Fasford Basic Information, Semiconductor Packaging and Test Equipment Manufacturing Base, Sales Area and Its Competitors

Table 127. Fasford Semiconductor Packaging and Test Equipment Product Portfolios and Specifications

Table 128. Fasford Semiconductor Packaging and Test Equipment Sales (Units),

Revenue (\$ Million), Price (K USD/Unit) and Gross Margin (2019-2024)

Table 129. Fasford Main Business

Table 130. Fasford Latest Developments

Table 131. Advantest Basic Information, Semiconductor Packaging and Test Equipment Manufacturing Base, Sales Area and Its Competitors

Table 132. Advantest Semiconductor Packaging and Test Equipment Product Portfolios



and Specifications

Table 133. Advantest Semiconductor Packaging and Test Equipment Sales (Units),

Revenue (\$ Million), Price (K USD/Unit) and Gross Margin (2019-2024)

Table 134. Advantest Main Business

Table 135. Advantest Latest Developments

Table 136. Hanmi semiconductor Basic Information, Semiconductor Packaging and

Test Equipment Manufacturing Base, Sales Area and Its Competitors

Table 137. Hanmi semiconductor Semiconductor Packaging and Test Equipment

Product Portfolios and Specifications

Table 138. Hanmi semiconductor Semiconductor Packaging and Test Equipment Sales

(Units), Revenue (\$ Million), Price (K USD/Unit) and Gross Margin (2019-2024)

Table 139. Hanmi semiconductor Main Business

Table 140. Hanmi semiconductor Latest Developments

Table 141. Shinkawa Basic Information, Semiconductor Packaging and Test Equipment

Manufacturing Base, Sales Area and Its Competitors

Table 142. Shinkawa Semiconductor Packaging and Test Equipment Product Portfolios

and Specifications

Table 143. Shinkawa Semiconductor Packaging and Test Equipment Sales (Units),

Revenue (\$ Million), Price (K USD/Unit) and Gross Margin (2019-2024)

Table 144. Shinkawa Main Business

Table 145. Shinkawa Latest Developments

Table 146. Shen Zhen Sidea Basic Information, Semiconductor Packaging and Test

Equipment Manufacturing Base, Sales Area and Its Competitors

Table 147. Shen Zhen Sidea Semiconductor Packaging and Test Equipment Product

Portfolios and Specifications

Table 148. Shen Zhen Sidea Semiconductor Packaging and Test Equipment Sales

(Units), Revenue (\$ Million), Price (K USD/Unit) and Gross Margin (2019-2024)

Table 149. Shen Zhen Sidea Main Business

Table 150. Shen Zhen Sidea Latest Developments

Table 151. DIAS Automation Basic Information, Semiconductor Packaging and Test

Equipment Manufacturing Base, Sales Area and Its Competitors

Table 152. DIAS Automation Semiconductor Packaging and Test Equipment Product

Portfolios and Specifications

Table 153. DIAS Automation Semiconductor Packaging and Test Equipment Sales

(Units), Revenue (\$ Million), Price (K USD/Unit) and Gross Margin (2019-2024)

Table 154. DIAS Automation Main Business

Table 155. DIAS Automation Latest Developments



List Of Figures

LIST OF FIGURES

- Figure 1. Picture of Semiconductor Packaging and Test Equipment
- Figure 2. Semiconductor Packaging and Test Equipment Report Years Considered
- Figure 3. Research Objectives
- Figure 4. Research Methodology
- Figure 5. Research Process and Data Source
- Figure 6. Global Semiconductor Packaging and Test Equipment Sales Growth Rate 2019-2030 (Units)
- Figure 7. Global Semiconductor Packaging and Test Equipment Revenue Growth Rate 2019-2030 (\$ Millions)
- Figure 8. Semiconductor Packaging and Test Equipment Sales by Region (2019, 2023 & 2030) & (\$ Millions)
- Figure 9. Product Picture of Wafer Probe Station
- Figure 10. Product Picture of Die Bonder
- Figure 11. Product Picture of Dicing Machine
- Figure 12. Product Picture of Test handler
- Figure 13. Product Picture of Sorter
- Figure 14. Global Semiconductor Packaging and Test Equipment Sales Market Share by Type in 2023
- Figure 15. Global Semiconductor Packaging and Test Equipment Revenue Market Share by Type (2019-2024)
- Figure 16. Semiconductor Packaging and Test Equipment Consumed in Integrated Device Manufacturer (IDMs)
- Figure 17. Global Semiconductor Packaging and Test Equipment Market: Integrated Device Manufacturer (IDMs) (2019-2024) & (Units)
- Figure 18. Semiconductor Packaging and Test Equipment Consumed in Outsourced Semiconductor Assembly and Test (OSAT)
- Figure 19. Global Semiconductor Packaging and Test Equipment Market: Outsourced Semiconductor Assembly and Test (OSAT) (2019-2024) & (Units)
- Figure 20. Global Semiconductor Packaging and Test Equipment Sales Market Share by Application (2023)
- Figure 21. Global Semiconductor Packaging and Test Equipment Revenue Market Share by Application in 2023
- Figure 22. Semiconductor Packaging and Test Equipment Sales Market by Company in 2023 (Units)
- Figure 23. Global Semiconductor Packaging and Test Equipment Sales Market Share



by Company in 2023

Figure 24. Semiconductor Packaging and Test Equipment Revenue Market by Company in 2023 (\$ Million)

Figure 25. Global Semiconductor Packaging and Test Equipment Revenue Market Share by Company in 2023

Figure 26. Global Semiconductor Packaging and Test Equipment Sales Market Share by Geographic Region (2019-2024)

Figure 27. Global Semiconductor Packaging and Test Equipment Revenue Market Share by Geographic Region in 2023

Figure 28. Americas Semiconductor Packaging and Test Equipment Sales 2019-2024 (Units)

Figure 29. Americas Semiconductor Packaging and Test Equipment Revenue 2019-2024 (\$ Millions)

Figure 30. APAC Semiconductor Packaging and Test Equipment Sales 2019-2024 (Units)

Figure 31. APAC Semiconductor Packaging and Test Equipment Revenue 2019-2024 (\$ Millions)

Figure 32. Europe Semiconductor Packaging and Test Equipment Sales 2019-2024 (Units)

Figure 33. Europe Semiconductor Packaging and Test Equipment Revenue 2019-2024 (\$ Millions)

Figure 34. Middle East & Africa Semiconductor Packaging and Test Equipment Sales 2019-2024 (Units)

Figure 35. Middle East & Africa Semiconductor Packaging and Test Equipment Revenue 2019-2024 (\$ Millions)

Figure 36. Americas Semiconductor Packaging and Test Equipment Sales Market Share by Country in 2023

Figure 37. Americas Semiconductor Packaging and Test Equipment Revenue Market Share by Country in 2023

Figure 38. Americas Semiconductor Packaging and Test Equipment Sales Market Share by Type (2019-2024)

Figure 39. Americas Semiconductor Packaging and Test Equipment Sales Market Share by Application (2019-2024)

Figure 40. United States Semiconductor Packaging and Test Equipment Revenue Growth 2019-2024 (\$ Millions)

Figure 41. Canada Semiconductor Packaging and Test Equipment Revenue Growth 2019-2024 (\$ Millions)

Figure 42. Mexico Semiconductor Packaging and Test Equipment Revenue Growth 2019-2024 (\$ Millions)



Figure 43. Brazil Semiconductor Packaging and Test Equipment Revenue Growth 2019-2024 (\$ Millions)

Figure 44. APAC Semiconductor Packaging and Test Equipment Sales Market Share by Region in 2023

Figure 45. APAC Semiconductor Packaging and Test Equipment Revenue Market Share by Regions in 2023

Figure 46. APAC Semiconductor Packaging and Test Equipment Sales Market Share by Type (2019-2024)

Figure 47. APAC Semiconductor Packaging and Test Equipment Sales Market Share by Application (2019-2024)

Figure 48. China Semiconductor Packaging and Test Equipment Revenue Growth 2019-2024 (\$ Millions)

Figure 49. Japan Semiconductor Packaging and Test Equipment Revenue Growth 2019-2024 (\$ Millions)

Figure 50. South Korea Semiconductor Packaging and Test Equipment Revenue Growth 2019-2024 (\$ Millions)

Figure 51. Southeast Asia Semiconductor Packaging and Test Equipment Revenue Growth 2019-2024 (\$ Millions)

Figure 52. India Semiconductor Packaging and Test Equipment Revenue Growth 2019-2024 (\$ Millions)

Figure 53. Australia Semiconductor Packaging and Test Equipment Revenue Growth 2019-2024 (\$ Millions)

Figure 54. China Taiwan Semiconductor Packaging and Test Equipment Revenue Growth 2019-2024 (\$ Millions)

Figure 55. Europe Semiconductor Packaging and Test Equipment Sales Market Share by Country in 2023

Figure 56. Europe Semiconductor Packaging and Test Equipment Revenue Market Share by Country in 2023

Figure 57. Europe Semiconductor Packaging and Test Equipment Sales Market Share by Type (2019-2024)

Figure 58. Europe Semiconductor Packaging and Test Equipment Sales Market Share by Application (2019-2024)

Figure 59. Germany Semiconductor Packaging and Test Equipment Revenue Growth 2019-2024 (\$ Millions)

Figure 60. France Semiconductor Packaging and Test Equipment Revenue Growth 2019-2024 (\$ Millions)

Figure 61. UK Semiconductor Packaging and Test Equipment Revenue Growth 2019-2024 (\$ Millions)

Figure 62. Italy Semiconductor Packaging and Test Equipment Revenue Growth



2019-2024 (\$ Millions)

Figure 63. Russia Semiconductor Packaging and Test Equipment Revenue Growth 2019-2024 (\$ Millions)

Figure 64. Middle East & Africa Semiconductor Packaging and Test Equipment Sales Market Share by Country in 2023

Figure 65. Middle East & Africa Semiconductor Packaging and Test Equipment Revenue Market Share by Country in 2023

Figure 66. Middle East & Africa Semiconductor Packaging and Test Equipment Sales Market Share by Type (2019-2024)

Figure 67. Middle East & Africa Semiconductor Packaging and Test Equipment Sales Market Share by Application (2019-2024)

Figure 68. Egypt Semiconductor Packaging and Test Equipment Revenue Growth 2019-2024 (\$ Millions)

Figure 69. South Africa Semiconductor Packaging and Test Equipment Revenue Growth 2019-2024 (\$ Millions)

Figure 70. Israel Semiconductor Packaging and Test Equipment Revenue Growth 2019-2024 (\$ Millions)

Figure 71. Turkey Semiconductor Packaging and Test Equipment Revenue Growth 2019-2024 (\$ Millions)

Figure 72. GCC Country Semiconductor Packaging and Test Equipment Revenue Growth 2019-2024 (\$ Millions)

Figure 73. Manufacturing Cost Structure Analysis of Semiconductor Packaging and Test Equipment in 2023

Figure 74. Manufacturing Process Analysis of Semiconductor Packaging and Test Equipment

Figure 75. Industry Chain Structure of Semiconductor Packaging and Test Equipment Figure 76. Channels of Distribution

Figure 77. Global Semiconductor Packaging and Test Equipment Sales Market Forecast by Region (2025-2030)

Figure 78. Global Semiconductor Packaging and Test Equipment Revenue Market Share Forecast by Region (2025-2030)

Figure 79. Global Semiconductor Packaging and Test Equipment Sales Market Share Forecast by Type (2025-2030)

Figure 80. Global Semiconductor Packaging and Test Equipment Revenue Market Share Forecast by Type (2025-2030)

Figure 81. Global Semiconductor Packaging and Test Equipment Sales Market Share Forecast by Application (2025-2030)

Figure 82. Global Semiconductor Packaging and Test Equipment Revenue Market Share Forecast by Application (2025-2030)



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