

Global Polished Silicon Wafer Market Growth 2025-2031

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Abstracts

The global Polished Silicon Wafer market size is predicted to grow from US\$ 11480 million in 2025 to US\$ 17190 million in 2031; it is expected to grow at a CAGR of 7.0% from 2025 to 2031.

The impact of the latest U.S. tariff measures and the corresponding policy responses from countries worldwide on market competitiveness, regional economic performance, and supply chain configurations will be comprehensively evaluated in this report.

A silicon wafer is a material essential for manufacturing semiconductors, which are found in all kinds of electronic devices that enrich our lives.

This ultra-flat disk is polished to a mirror-like surface, and made as free as possible of tiny surface irregularities, making it the flattest object in the world. It is also ultra-clean, virtually free of microparticles and other impurities. These qualities are necessary so it can be used as the substrate material of today's state-of-the-art semiconductors.

Polished Wafer is a thin disc-shaped single crystal silicon product manufactured from high-purity poly-crystalline silicon by undergoing melting, crystal growth, cutting, polishing and washing processes. It is produced in diameters of 200mm/300mm and is usually used in the manufacture of memory semi-conductors such as DRAM/NAND Flash Memory.

From the perspective of the overall semiconductor silicon wafer market, the global sales of semiconductor silicon wafers reached \$16.377 billion in 2023 and are expected to reach \$25.278 billion by 2030, with a compound annual growth rate (CAGR) of 8.07% from 2024 to 2030. The Chinese market has undergone significant changes in recent

years, with a market size of \$3.379 billion in 2023, accounting for approximately 20.63% of the global market. It is expected to reach \$5.867 billion by 2030, with a global market share of 23.21%. In terms of wafer shipments, the global total semiconductor silicon wafer sales in 2023 amounted to 214 million wafers, including 84 million 12-inch wafers, 59 million 8-inch wafers, and 70.85 million small-size wafers. The share of 12-inch wafers is expected to increase from 39.23% in 2023 to 49.86% in 2030. In terms of million square inches, the total global semiconductor silicon wafer sales in 2023 were 13.769 billion square inches, with projections to reach 18.484 billion square inches by 2030. The share of 12-inch wafers in 2023 was 67%, and is expected to increase to 72.73% by 2030. In terms of international market share and rankings, major manufacturers include Shin-Etsu Chemical, SUMCO, GlobalWafers, Siltronic, SK Siltron, and Soitec, with the top six companies occupying approximately 81% of the international market share in 2023. In the Chinese market, key manufacturers include Shin-Etsu Chemical, SUMCO, GlobalWafers, Siltronic, SK Siltron, Zhonghuan Advanced Semiconductor Materials, Hangzhou Semiconductor Wafer, Hangzhou Lion Microelectronics, GRINM Semiconductor Materials, Shanghai Advanced Silicon Technology (AST), and Beijing ESWIN Technology Group. The top nine manufacturers in China hold more than 80% of the market share in 2023.

Regarding semiconductor silicon polished wafers, the key manufacturers currently include Shin-Etsu Chemical, SUMCO, GlobalWafers, Siltronic AG, SK Siltron, FST Corporation, and Wafer Works Corporation, among others.

LP Information, Inc. (LPI) ' newest research report, the “Polished Silicon Wafer Industry Forecast” looks at past sales and reviews total world Polished Silicon Wafer sales in 2024, providing a comprehensive analysis by region and market sector of projected Polished Silicon Wafer sales for 2025 through 2031. With Polished Silicon Wafer sales broken down by region, market sector and sub-sector, this report provides a detailed analysis in US\$ millions of the world Polished Silicon Wafer industry.

This Insight Report provides a comprehensive analysis of the global Polished Silicon Wafer landscape and highlights key trends related to product segmentation, company formation, revenue, and market share, latest development, and M&A activity. This report also analyzes the strategies of leading global companies with a focus on Polished Silicon Wafer portfolios and capabilities, market entry strategies, market positions, and geographic footprints, to better understand these firms' unique position in an accelerating global Polished Silicon Wafer market.

This Insight Report evaluates the key market trends, drivers, and affecting factors

shaping the global outlook for Polished Silicon Wafer and breaks down the forecast by Type, by Application, geography, and market size to highlight emerging pockets of opportunity. With a transparent methodology based on hundreds of bottom-up qualitative and quantitative market inputs, this study forecast offers a highly nuanced view of the current state and future trajectory in the global Polished Silicon Wafer.

This report presents a comprehensive overview, market shares, and growth opportunities of Polished Silicon Wafer market by product type, application, key manufacturers and key regions and countries.

Segmentation by Type:

12 inch Polished Wafers

8 inch Polished Wafers

6 inch Polished Wafers

Segmentation by Application:

Memory (DRAM & NAND)

Logic IC

Semiconductor Discrete Devices

Analog IC

Sensor

Others

This report also splits the market by region:

Americas

United States

Canada

Mexico

Brazil

APAC

China

Japan

Korea

Southeast Asia

India

Australia

Europe

Germany

France

UK

Italy

Russia

Middle East & Africa

Egypt

South Africa

Israel

Turkey

GCC Countries

The below companies that are profiled have been selected based on inputs gathered from primary experts and analysing the company's coverage, product portfolio, its market penetration.

Shin-Etsu Chemical

SUMCO

GlobalWafers

Siltronic AG

SK Siltron

FST Corporation

Wafer Works Corporation

Soitec

National Silicon Industry Group (NSIG)

Zhonghuan Advanced Semiconductor Materials

Hangzhou Lion Microelectronics

Hangzhou Semiconductor Wafer +AK12+G1+G12:AD12

GRINM Semiconductor Materials

MCL Electronic Materials

Shanghai Advanced Silicon Technology (AST)

Beijing ESWIN Technology Group

Zhejiang MTCN Technology

Hebei Puxing Electronic Technology

Nanjing Guosheng Electronics

Key Questions Addressed in this Report

What is the 10-year outlook for the global Polished Silicon Wafer market?

What factors are driving Polished Silicon Wafer market growth, globally and by region?

Which technologies are poised for the fastest growth by market and region?

How do Polished Silicon Wafer market opportunities vary by end market size?

How does Polished Silicon Wafer break out by Type, by Application?

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