

Global Photonic Chip KGD Test and Sorting Systems Market Growth (Status and Outlook) 2026-2032

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Abstracts

The global Photonic Chip KGD Test and Sorting Systems market size is predicted to grow from US\$ 254 million in 2025 to US\$ 1213 million in 2032; it is expected to grow at a CAGR of 22.6% from 2026 to 2032.

Photonic Chip KGD Test and Sorting Systems are automated equipment platforms used to test, classify and screen photonic, optoelectronic and silicon photonics devices at wafer level, bar level, singulated-die level or before advanced packaging. These systems typically integrate wafer or die handling, probe stations, optical coupling and nano-positioning modules, electrical/RF test instruments, LIV, spectrum, near-field and far-field measurement modules, thermal control, software automation, die mapping, binning and data traceability. The core purpose is to identify Known Good Die before costly photonic packaging, co-packaged optics assembly, optical module manufacturing, or heterogeneous integration, thereby improving final yield and reducing downstream scrap cost.

Based on our research, the market for Photonic Chip KGD Test and Sorting Systems should be understood as a specialized equipment segment rather than a generic semiconductor tester or optical-module test market. The core system combines optical coupling, electrical and RF testing, high-precision motion control, wafer or die automation, temperature control, binning software and traceability. Unlike conventional IC wafer sort, photonic devices require optical-to-optical, optical-to-electrical, electrical-to-optical and electrical-to-electrical measurements at wafer or die level. This creates difficult requirements around edge coupling, grating coupling, fiber-array alignment, optical loss stability, LIV, near-field and far-field measurements, and automated calibration. Therefore, the most appropriate market boundary is chip-level KGD screening and sorting before photonic packaging, advanced packaging or optical-engine

assembly.

Demand growth is driven by three major forces. First, AI data centers, co-packaged optics, near-package optics and optical I/O are pushing silicon photonics closer to manufacturing scale, making wafer-level electro-optical test a bottleneck for yield ramp. Second, VCSEL and laser-diode applications in datacom, 3D sensing, LiDAR and optical sensing continue to require high-throughput wafer testing and binning. Third, advanced packaging increases the value of KGD screening, because a defective photonic die integrated into a high-value package can cause disproportionate downstream scrap cost. This makes the segment structurally attractive, although the pace of adoption will depend on CPO deployment, silicon photonics wafer capacity and the willingness of large customers to buy external systems rather than build internal test platforms.

From a technology perspective, the next stage of competition will move beyond basic measurement capability toward throughput, repeatability, automated alignment, multi-channel scaling, factory integration and data traceability. Silicon photonics test systems are moving toward 300mm wafer compatibility, edge and grating coupling support, RF/DC/optical co-test, automated probe and fiber calibration, and SECS/GEM-ready production cells. VCSEL and laser-diode test systems are moving toward higher pulsed current, shorter pulse width, wider temperature range and combined LIV, spectrum, near-field and far-field characterization. Regional supply-chain localization will create opportunities for Chinese and Asian vendors, but the leading production platforms will still be defined by customer qualification history, uptime, alignment stability and installed-base learning.

LPI (LP Information)'s newest research report, the "Photonic Chip KGD Test and Sorting Systems Industry Forecast" looks at past sales and reviews total world Photonic Chip KGD Test and Sorting Systems sales in 2025, providing a comprehensive analysis by region and market sector of projected Photonic Chip KGD Test and Sorting Systems sales for 2026 through 2032. With Photonic Chip KGD Test and Sorting Systems sales broken down by region, market sector and sub-sector, this report provides a detailed analysis in US\$ millions of the world Photonic Chip KGD Test and Sorting Systems industry.

This Insight Report provides a comprehensive analysis of the global Photonic Chip KGD Test and Sorting Systems landscape and highlights key trends related to product segmentation, company formation, revenue, and market share, latest development, and M&A activity. This report also analyses the strategies of leading global companies with

a focus on Photonic Chip KGD Test and Sorting Systems portfolios and capabilities, market entry strategies, market positions, and geographic footprints, to better understand these firms' unique position in an accelerating global Photonic Chip KGD Test and Sorting Systems market.

This Insight Report evaluates the key market trends, drivers, and affecting factors shaping the global outlook for Photonic Chip KGD Test and Sorting Systems and breaks down the forecast by Type, by Application, geography, and market size to highlight emerging pockets of opportunity. With a transparent methodology based on hundreds of bottom-up qualitative and quantitative market inputs, this study forecast offers a highly nuanced view of the current state and future trajectory in the global Photonic Chip KGD Test and Sorting Systems.

This report presents a comprehensive overview, market shares, and growth opportunities of Photonic Chip KGD Test and Sorting Systems market by product type, application, key players and key regions and countries.

Segmentation by Type:

Wafer-level Test System

Die-level KGD Test System

Other Customized System

Segmentation by Optical Coupling Method:

Grating Coupling

Edge Coupling

Free-space Coupling

Hybrid Coupling

Other

Segmentation by Test Function:

- Optical Test
- Electrical Test
- RF / High-speed Test
- Thermal Test
- Other

Segmentation by Automation Level:

- Manual Test System
- Semi-automatic Test System
- Fully Automatic Test System

Segmentation by Application:

- Optical Communications
- Data Center & AI Computing
- Advanced Semiconductor Packaging
- Consumer Electronics
- Medical & Biophotonics
- R&D and Engineering Validation
- Defense & Aerospace Optoelectronics

This report also splits the market by region:

Americas

United States

Canada

Mexico

Brazil

APAC

China

Japan

Korea

Southeast Asia

India

Australia

Europe

Germany

France

UK

Italy

Russia

Middle East & Africa

Egypt

South Africa

Israel

Turkey

GCC Countries

The below companies that are profiled have been selected based on inputs gathered from primary experts and analyzing the company's coverage, product portfolio, its market penetration.

FormFactor, Inc.

Teradyne, Inc.

Advantest Corporation

Tokyo Electron Limited

Keysight Technologies, Inc.

RoboTechnik Intelligent Technology Co., Ltd.

MPI Corporation

Chroma ATE Inc.

Konica Minolta, Inc.

SPEA S.p.A.

Aehr Test Systems

EXFO Inc.

Pentamaster Corporation Berhad

FitTech Co., Ltd.

Enlitech Co., Ltd.

SEMISHARE

Semight Instruments Co., Ltd.

Alphax Co., Ltd.

Yuasa Electronics Co., Ltd.

Wuhan Precise Electronics Co., Ltd.

Hebei THAHOO Optoelectronics Technology Co., Ltd.

SemiProbe, Inc.

Opto System Co., Ltd.

Synos Technology, Inc.

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