

# Global Parallel Interface MRAM Market Growth 2026-2032

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## Abstracts

The global Parallel Interface MRAM market size is predicted to grow from US\$ 167 million in 2025 to US\$ 249 million in 2032; it is expected to grow at a CAGR of 5.9% from 2026 to 2032.

Parallel Interface MRAM refers to a type of Magnetoresistive Random Access Memory (MRAM) that communicates with a host system through a parallel data bus rather than a serial interface. It typically uses wide data lines (such as x8, x16, or x32) and operates in a manner similar to traditional SRAM or asynchronous SRAM-like memory interfaces. This architecture enables high data throughput and low-latency access, making it suitable for applications requiring fast random access and compatibility with legacy memory controllers, such as industrial systems, aerospace electronics, and embedded computing platforms. In 2025, global Parallel Interface MRAM production reached approximately 4.87 M units, with an average global market price of around US\$ 35 per unit. Annual production capacity is approximately 5.6 M units. Gross Profit Margin is approximately 62%. The industry chain of Parallel Interface MRAM is highly concentrated and technology-intensive. Upstream mainly includes high-purity semiconductor materials such as silicon wafers, magnetic tunnel junction (MTJ) stack materials (CoFeB-based thin films), and advanced deposition equipment (PVD, ALD). Midstream is dominated by a few specialized manufacturers and foundry partners that integrate MRAM cell structures into CMOS logic processes, with key players focusing on high-reliability discrete chip design and packaging (e.g., SRAM-compatible x8/x16 interfaces). Downstream demand is driven by industrial control systems, aerospace and defense electronics, and legacy SRAM replacement markets, where low latency and non-volatility are critical requirements. Parallel Interface MRAM is a classic 'high-value but low-volume' niche technology. Its value proposition is strong, combining SRAM-like speed with non-volatile memory, but its adoption is constrained by cost, limited

ecosystem scale, and the gradual shift toward SPI MRAM and embedded MRAM solutions. In my view, its long-term growth will remain steady but capped, primarily sustained by mission-critical systems rather than mass commercial electronics.

United States market for Parallel Interface MRAM is estimated to increase from US\$ million in 2025 to US\$ million by 2032, at a CAGR of % from 2026 through 2032.

China market for Parallel Interface MRAM is estimated to increase from US\$ million in 2025 to US\$ million by 2032, at a CAGR of % from 2026 through 2032.

Europe market for Parallel Interface MRAM is estimated to increase from US\$ million in 2025 to US\$ million by 2032, at a CAGR of % from 2026 through 2032.

Global key Parallel Interface MRAM players cover Spin Memory, Avalanche Technology, Micron Technology Inc., Infineon Technologies (Germany), Renesas Electronics Corporation, etc. In terms of revenue, the global two largest companies occupied for a share nearly % in 2025.

LP Information, Inc. (LPI) ' newest research report, the "Parallel Interface MRAM Industry Forecast" looks at past sales and reviews total world Parallel Interface MRAM sales in 2025, providing a comprehensive analysis by region and market sector of projected Parallel Interface MRAM sales for 2026 through 2032. With Parallel Interface MRAM sales broken down by region, market sector and sub-sector, this report provides a detailed analysis in US\$ millions of the world Parallel Interface MRAM industry.

This Insight Report provides a comprehensive analysis of the global Parallel Interface MRAM landscape and highlights key trends related to product segmentation, company formation, revenue, and market share, latest development, and M&A activity. This report also analyzes the strategies of leading global companies with a focus on Parallel Interface MRAM portfolios and capabilities, market entry strategies, market positions, and geographic footprints, to better understand these firms' unique position in an accelerating global Parallel Interface MRAM market.

This Insight Report evaluates the key market trends, drivers, and affecting factors shaping the global outlook for Parallel Interface MRAM and breaks down the forecast by Interface Protocol, by Application, geography, and market size to highlight emerging pockets of opportunity. With a transparent methodology based on hundreds of bottom-up qualitative and quantitative market inputs, this study forecast offers a highly nuanced view of the current state and future trajectory in the global Parallel Interface MRAM.

This report presents a comprehensive overview, market shares, and growth opportunities of Parallel Interface MRAM market by product type, application, key manufacturers and key regions and countries.

#### Segmentation by Interface Protocol:

Parallel SRAM-Compatible MRAM

Parallel Asynchronous MRAM

Burst Mode Parallel MRAM

High-Reliability Parallel MRAM

#### Segmentation by Parameter Sub-Dimension:

x8 Parallel MRAM

x16 Parallel MRAM

x32 Parallel MRAM

#### Segmentation by Product Form:

Discrete Parallel MRAM IC

Multi-chip Module Parallel MRAM

Embedded Parallel MRAM IP

#### Segmentation by Application:

Industrial

Automotive

Aerospace & Defense

Commercial

Others

This report also splits the market by region:

Americas

United States

Canada

Mexico

Brazil

APAC

China

Japan

Korea

Southeast Asia

India

Australia

Europe

Germany

France

UK

Italy

Russia

Middle East & Africa

Egypt

South Africa

Israel

Turkey

GCC Countries

The below companies that are profiled have been selected based on inputs gathered from primary experts and analysing the company's coverage, product portfolio, its market penetration.

Spin Memory

Avalanche Technology

Micron Technology Inc.

Infineon Technologies (Germany)

Renesas Electronics Corporation

Everspin Technologies

Honeywell

NXP Semiconductors

STMicroelectronics

Nve Corporation

Siproin Microelectronics

GigaDevice Semiconductor

Fudan Microelectronics Group

Wuhan Xinxin Semiconductor

Changxin Memory Technologies

Suzhou Memory Technology Co., Ltd.(China)

Beijing Tongfang Microelectronics(China)

Nanjing SemiDrive Technology(China)

Zhuhai Jieli Technology(China)

Hangzhou Silan Microelectronic(China)

#### Key Questions Addressed in this Report

What is the 10-year outlook for the global Parallel Interface MRAM market?

What factors are driving Parallel Interface MRAM market growth, globally and by region?

Which technologies are poised for the fastest growth by market and region?

How do Parallel Interface MRAM market opportunities vary by end market size?

How does Parallel Interface MRAM break out by Interface Protocol, by Application?

## Contents

### 1 SCOPE OF THE REPORT

- 1.1 Market Introduction
- 1.2 Years Considered
- 1.3 Research Objectives
- 1.4 Market Research Methodology
- 1.5 Research Process and Data Source
- 1.6 Economic Indicators
- 1.7 Currency Considered
- 1.8 Market Estimation Caveats

### 2 EXECUTIVE SUMMARY

- 2.1 World Market Overview
  - 2.1.1 Global Parallel Interface MRAM Annual Sales 2021-2032
  - 2.1.2 World Current & Future Analysis for Parallel Interface MRAM by Geographic Region, 2021, 2025 & 2032
  - 2.1.3 World Current & Future Analysis for Parallel Interface MRAM by Country/Region, 2021, 2025 & 2032
- 2.2 Parallel Interface MRAM Segment by Interface Protocol
  - 2.2.1 Parallel SRAM-Compatible MRAM
  - 2.2.2 Parallel Asynchronous MRAM
  - 2.2.3 Burst Mode Parallel MRAM
  - 2.2.4 High-Reliability Parallel MRAM
  - 2.2.5 Parallel Interface MRAM Sales by Interface Protocol
    - 2.2.5.1 Global Parallel Interface MRAM Sales Market Share by Interface Protocol (2021-2026)
    - 2.2.5.2 Global Parallel Interface MRAM Revenue and Market Share by Interface Protocol (2021-2026)
    - 2.2.5.3 Global Parallel Interface MRAM Sale Price by Interface Protocol (2021-2026)
- 2.3 Parallel Interface MRAM Segment by Parameter Sub-Dimension
  - 2.3.1 x8 Parallel MRAM
  - 2.3.2 x16 Parallel MRAM
  - 2.3.3 x32 Parallel MRAM
  - 2.3.4 Parallel Interface MRAM Sales by Parameter Sub-Dimension
    - 2.3.4.1 Global Parallel Interface MRAM Sales Market Share by Parameter Sub-Dimension (2021-2026)

2.3.4.2 Global Parallel Interface MRAM Revenue and Market Share by Parameter Sub-Dimension (2021-2026)

2.3.4.3 Global Parallel Interface MRAM Sale Price by Parameter Sub-Dimension (2021-2026)

2.4 Parallel Interface MRAM Segment by Product Form

2.4.1 Discrete Parallel MRAM IC

2.4.2 Multi-chip Module Parallel MRAM

2.4.3 Embedded Parallel MRAM IP

2.4.4 Parallel Interface MRAM Sales by Product Form

2.4.4.1 Global Parallel Interface MRAM Sales Market Share by Product Form (2021-2026)

2.4.4.2 Global Parallel Interface MRAM Revenue and Market Share by Product Form (2021-2026)

2.4.4.3 Global Parallel Interface MRAM Sale Price by Product Form (2021-2026)

2.5 Parallel Interface MRAM Segment by Application

2.5.1 Industrial

2.5.2 Automotive

2.5.3 Aerospace & Defense

2.5.4 Commercial

2.5.5 Others

2.5.6 Parallel Interface MRAM Sales by Application

2.5.6.1 Global Parallel Interface MRAM Sale Market Share by Application (2021-2026)

2.5.6.2 Global Parallel Interface MRAM Revenue and Market Share by Application (2021-2026)

2.5.6.3 Global Parallel Interface MRAM Sale Price by Application (2021-2026)

### **3 GLOBAL BY COMPANY**

3.1 Global Parallel Interface MRAM Breakdown Data by Company

3.1.1 Global Parallel Interface MRAM Annual Sales by Company (2021-2026)

3.1.2 Global Parallel Interface MRAM Sales Market Share by Company (2021-2026)

3.2 Global Parallel Interface MRAM Annual Revenue by Company (2021-2026)

3.2.1 Global Parallel Interface MRAM Revenue by Company (2021-2026)

3.2.2 Global Parallel Interface MRAM Revenue Market Share by Company (2021-2026)

3.3 Global Parallel Interface MRAM Sale Price by Company

3.4 Key Manufacturers Parallel Interface MRAM Producing Area Distribution, Sales Area, Product Type

- 3.4.1 Key Manufacturers Parallel Interface MRAM Product Location Distribution
- 3.4.2 Players Parallel Interface MRAM Products Offered
- 3.5 Market Concentration Rate Analysis
  - 3.5.1 Competition Landscape Analysis
  - 3.5.2 Concentration Ratio (CR3, CR5 and CR10) & (2024-2026)
- 3.6 New Products and Potential Entrants
- 3.7 Market M&A Activity & Strategy

## **4 WORLD HISTORIC REVIEW FOR PARALLEL INTERFACE MRAM BY GEOGRAPHIC REGION**

- 4.1 World Historic Parallel Interface MRAM Market Size by Geographic Region (2021-2026)
  - 4.1.1 Global Parallel Interface MRAM Annual Sales by Geographic Region (2021-2026)
  - 4.1.2 Global Parallel Interface MRAM Annual Revenue by Geographic Region (2021-2026)
- 4.2 World Historic Parallel Interface MRAM Market Size by Country/Region (2021-2026)
  - 4.2.1 Global Parallel Interface MRAM Annual Sales by Country/Region (2021-2026)
  - 4.2.2 Global Parallel Interface MRAM Annual Revenue by Country/Region (2021-2026)
- 4.3 Americas Parallel Interface MRAM Sales Growth
- 4.4 APAC Parallel Interface MRAM Sales Growth
- 4.5 Europe Parallel Interface MRAM Sales Growth
- 4.6 Middle East & Africa Parallel Interface MRAM Sales Growth

## **5 AMERICAS**

- 5.1 Americas Parallel Interface MRAM Sales by Country
  - 5.1.1 Americas Parallel Interface MRAM Sales by Country (2021-2026)
  - 5.1.2 Americas Parallel Interface MRAM Revenue by Country (2021-2026)
- 5.2 Americas Parallel Interface MRAM Sales by Interface Protocol (2021-2026)
- 5.3 Americas Parallel Interface MRAM Sales by Application (2021-2026)
- 5.4 United States
- 5.5 Canada
- 5.6 Mexico
- 5.7 Brazil

## **6 APAC**

## 6.1 APAC Parallel Interface MRAM Sales by Region

6.1.1 APAC Parallel Interface MRAM Sales by Region (2021-2026)

6.1.2 APAC Parallel Interface MRAM Revenue by Region (2021-2026)

## 6.2 APAC Parallel Interface MRAM Sales by Interface Protocol (2021-2026)

## 6.3 APAC Parallel Interface MRAM Sales by Application (2021-2026)

### 6.4 China

### 6.5 Japan

### 6.6 South Korea

### 6.7 Southeast Asia

### 6.8 India

### 6.9 Australia

### 6.10 China Taiwan

## 7 EUROPE

### 7.1 Europe Parallel Interface MRAM by Country

7.1.1 Europe Parallel Interface MRAM Sales by Country (2021-2026)

7.1.2 Europe Parallel Interface MRAM Revenue by Country (2021-2026)

### 7.2 Europe Parallel Interface MRAM Sales by Interface Protocol (2021-2026)

### 7.3 Europe Parallel Interface MRAM Sales by Application (2021-2026)

#### 7.4 Germany

#### 7.5 France

#### 7.6 UK

#### 7.7 Italy

#### 7.8 Russia

## 8 MIDDLE EAST & AFRICA

### 8.1 Middle East & Africa Parallel Interface MRAM by Country

8.1.1 Middle East & Africa Parallel Interface MRAM Sales by Country (2021-2026)

8.1.2 Middle East & Africa Parallel Interface MRAM Revenue by Country (2021-2026)

### 8.2 Middle East & Africa Parallel Interface MRAM Sales by Interface Protocol (2021-2026)

### 8.3 Middle East & Africa Parallel Interface MRAM Sales by Application (2021-2026)

#### 8.4 Egypt

#### 8.5 South Africa

#### 8.6 Israel

#### 8.7 Turkey

## 8.8 GCC Countries

# 9 MARKET DRIVERS, CHALLENGES AND TRENDS

## 9.1 Market Drivers & Growth Opportunities

## 9.2 Market Challenges & Risks

## 9.3 Industry Trends

# 10 MANUFACTURING COST STRUCTURE ANALYSIS

## 10.1 Raw Material and Suppliers

## 10.2 Manufacturing Cost Structure Analysis of Parallel Interface MRAM

## 10.3 Manufacturing Process Analysis of Parallel Interface MRAM

## 10.4 Industry Chain Structure of Parallel Interface MRAM

# 11 MARKETING, DISTRIBUTORS AND CUSTOMER

## 11.1 Sales Channel

### 11.1.1 Direct Channels

### 11.1.2 Indirect Channels

## 11.2 Parallel Interface MRAM Distributors

## 11.3 Parallel Interface MRAM Customer

# 12 WORLD FORECAST REVIEW FOR PARALLEL INTERFACE MRAM BY GEOGRAPHIC REGION

## 12.1 Global Parallel Interface MRAM Market Size Forecast by Region

### 12.1.1 Global Parallel Interface MRAM Forecast by Region (2027-2032)

### 12.1.2 Global Parallel Interface MRAM Annual Revenue Forecast by Region (2027-2032)

## 12.2 Americas Forecast by Country (2027-2032)

## 12.3 APAC Forecast by Region (2027-2032)

## 12.4 Europe Forecast by Country (2027-2032)

## 12.5 Middle East & Africa Forecast by Country (2027-2032)

## 12.6 Global Parallel Interface MRAM Forecast by Interface Protocol (2027-2032)

## 12.7 Global Parallel Interface MRAM Forecast by Application (2027-2032)

# 13 KEY PLAYERS ANALYSIS

## 13.1 Spin Memory

### 13.1.1 Spin Memory Company Information

### 13.1.2 Spin Memory Parallel Interface MRAM Product Portfolios and Specifications

### 13.1.3 Spin Memory Parallel Interface MRAM Sales, Revenue, Price and Gross Margin (2021-2026)

### 13.1.4 Spin Memory Main Business Overview

### 13.1.5 Spin Memory Latest Developments

## 13.2 Avalanche Technology

### 13.2.1 Avalanche Technology Company Information

### 13.2.2 Avalanche Technology Parallel Interface MRAM Product Portfolios and Specifications

### 13.2.3 Avalanche Technology Parallel Interface MRAM Sales, Revenue, Price and Gross Margin (2021-2026)

### 13.2.4 Avalanche Technology Main Business Overview

### 13.2.5 Avalanche Technology Latest Developments

## 13.3 Micron Technology Inc.

### 13.3.1 Micron Technology Inc. Company Information

### 13.3.2 Micron Technology Inc. Parallel Interface MRAM Product Portfolios and Specifications

### 13.3.3 Micron Technology Inc. Parallel Interface MRAM Sales, Revenue, Price and Gross Margin (2021-2026)

### 13.3.4 Micron Technology Inc. Main Business Overview

### 13.3.5 Micron Technology Inc. Latest Developments

## 13.4 Infineon Technologies (Germany)

### 13.4.1 Infineon Technologies (Germany) Company Information

### 13.4.2 Infineon Technologies (Germany) Parallel Interface MRAM Product Portfolios and Specifications

### 13.4.3 Infineon Technologies (Germany) Parallel Interface MRAM Sales, Revenue, Price and Gross Margin (2021-2026)

### 13.4.4 Infineon Technologies (Germany) Main Business Overview

### 13.4.5 Infineon Technologies (Germany) Latest Developments

## 13.5 Renesas Electronics Corporation

### 13.5.1 Renesas Electronics Corporation Company Information

### 13.5.2 Renesas Electronics Corporation Parallel Interface MRAM Product Portfolios and Specifications

### 13.5.3 Renesas Electronics Corporation Parallel Interface MRAM Sales, Revenue, Price and Gross Margin (2021-2026)

### 13.5.4 Renesas Electronics Corporation Main Business Overview

### 13.5.5 Renesas Electronics Corporation Latest Developments

## 13.6 Everspin Technologies

### 13.6.1 Everspin Technologies Company Information

### 13.6.2 Everspin Technologies Parallel Interface MRAM Product Portfolios and Specifications

### 13.6.3 Everspin Technologies Parallel Interface MRAM Sales, Revenue, Price and Gross Margin (2021-2026)

### 13.6.4 Everspin Technologies Main Business Overview

### 13.6.5 Everspin Technologies Latest Developments

## 13.7 Honeywell

### 13.7.1 Honeywell Company Information

### 13.7.2 Honeywell Parallel Interface MRAM Product Portfolios and Specifications

### 13.7.3 Honeywell Parallel Interface MRAM Sales, Revenue, Price and Gross Margin (2021-2026)

### 13.7.4 Honeywell Main Business Overview

### 13.7.5 Honeywell Latest Developments

## 13.8 NXP Semiconductors

### 13.8.1 NXP Semiconductors Company Information

### 13.8.2 NXP Semiconductors Parallel Interface MRAM Product Portfolios and Specifications

### 13.8.3 NXP Semiconductors Parallel Interface MRAM Sales, Revenue, Price and Gross Margin (2021-2026)

### 13.8.4 NXP Semiconductors Main Business Overview

### 13.8.5 NXP Semiconductors Latest Developments

## 13.9 STMicroelectronics

### 13.9.1 STMicroelectronics Company Information

### 13.9.2 STMicroelectronics Parallel Interface MRAM Product Portfolios and Specifications

### 13.9.3 STMicroelectronics Parallel Interface MRAM Sales, Revenue, Price and Gross Margin (2021-2026)

### 13.9.4 STMicroelectronics Main Business Overview

### 13.9.5 STMicroelectronics Latest Developments

## 13.10 Nve Corporation

### 13.10.1 Nve Corporation Company Information

### 13.10.2 Nve Corporation Parallel Interface MRAM Product Portfolios and Specifications

### 13.10.3 Nve Corporation Parallel Interface MRAM Sales, Revenue, Price and Gross Margin (2021-2026)

### 13.10.4 Nve Corporation Main Business Overview

### 13.10.5 Nve Corporation Latest Developments

### 13.11 Siproin Microelectronics

#### 13.11.1 Siproin Microelectronics Company Information

#### 13.11.2 Siproin Microelectronics Parallel Interface MRAM Product Portfolios and Specifications

#### 13.11.3 Siproin Microelectronics Parallel Interface MRAM Sales, Revenue, Price and Gross Margin (2021-2026)

#### 13.11.4 Siproin Microelectronics Main Business Overview

#### 13.11.5 Siproin Microelectronics Latest Developments

### 13.12 GigaDevice Semiconductor

#### 13.12.1 GigaDevice Semiconductor Company Information

#### 13.12.2 GigaDevice Semiconductor Parallel Interface MRAM Product Portfolios and Specifications

#### 13.12.3 GigaDevice Semiconductor Parallel Interface MRAM Sales, Revenue, Price and Gross Margin (2021-2026)

#### 13.12.4 GigaDevice Semiconductor Main Business Overview

#### 13.12.5 GigaDevice Semiconductor Latest Developments

### 13.13 Fudan Microelectronics Group

#### 13.13.1 Fudan Microelectronics Group Company Information

#### 13.13.2 Fudan Microelectronics Group Parallel Interface MRAM Product Portfolios and Specifications

#### 13.13.3 Fudan Microelectronics Group Parallel Interface MRAM Sales, Revenue, Price and Gross Margin (2021-2026)

#### 13.13.4 Fudan Microelectronics Group Main Business Overview

#### 13.13.5 Fudan Microelectronics Group Latest Developments

### 13.14 Wuhan Xinxin Semiconductor

#### 13.14.1 Wuhan Xinxin Semiconductor Company Information

#### 13.14.2 Wuhan Xinxin Semiconductor Parallel Interface MRAM Product Portfolios and Specifications

#### 13.14.3 Wuhan Xinxin Semiconductor Parallel Interface MRAM Sales, Revenue, Price and Gross Margin (2021-2026)

#### 13.14.4 Wuhan Xinxin Semiconductor Main Business Overview

#### 13.14.5 Wuhan Xinxin Semiconductor Latest Developments

### 13.15 Changxin Memory Technologies

#### 13.15.1 Changxin Memory Technologies Company Information

#### 13.15.2 Changxin Memory Technologies Parallel Interface MRAM Product Portfolios and Specifications

#### 13.15.3 Changxin Memory Technologies Parallel Interface MRAM Sales, Revenue, Price and Gross Margin (2021-2026)

#### 13.15.4 Changxin Memory Technologies Main Business Overview

- 13.15.5 Changxin Memory Technologies Latest Developments
- 13.16 Suzhou Memory Technology Co., Ltd.(China)
  - 13.16.1 Suzhou Memory Technology Co., Ltd.(China) Company Information
  - 13.16.2 Suzhou Memory Technology Co., Ltd.(China) Parallel Interface MRAM Product Portfolios and Specifications
  - 13.16.3 Suzhou Memory Technology Co., Ltd.(China) Parallel Interface MRAM Sales, Revenue, Price and Gross Margin (2021-2026)
  - 13.16.4 Suzhou Memory Technology Co., Ltd.(China) Main Business Overview
  - 13.16.5 Suzhou Memory Technology Co., Ltd.(China) Latest Developments
- 13.17 Beijing Tongfang Microelectronics(China)
  - 13.17.1 Beijing Tongfang Microelectronics(China) Company Information
  - 13.17.2 Beijing Tongfang Microelectronics(China) Parallel Interface MRAM Product Portfolios and Specifications
  - 13.17.3 Beijing Tongfang Microelectronics(China) Parallel Interface MRAM Sales, Revenue, Price and Gross Margin (2021-2026)
  - 13.17.4 Beijing Tongfang Microelectronics(China) Main Business Overview
  - 13.17.5 Beijing Tongfang Microelectronics(China) Latest Developments
- 13.18 Nanjing SemiDrive Technology(China)
  - 13.18.1 Nanjing SemiDrive Technology(China) Company Information
  - 13.18.2 Nanjing SemiDrive Technology(China) Parallel Interface MRAM Product Portfolios and Specifications
  - 13.18.3 Nanjing SemiDrive Technology(China) Parallel Interface MRAM Sales, Revenue, Price and Gross Margin (2021-2026)
  - 13.18.4 Nanjing SemiDrive Technology(China) Main Business Overview
  - 13.18.5 Nanjing SemiDrive Technology(China) Latest Developments
- 13.19 Zhuhai Jieli Technology(China)
  - 13.19.1 Zhuhai Jieli Technology(China) Company Information
  - 13.19.2 Zhuhai Jieli Technology(China) Parallel Interface MRAM Product Portfolios and Specifications
  - 13.19.3 Zhuhai Jieli Technology(China) Parallel Interface MRAM Sales, Revenue, Price and Gross Margin (2021-2026)
  - 13.19.4 Zhuhai Jieli Technology(China) Main Business Overview
  - 13.19.5 Zhuhai Jieli Technology(China) Latest Developments
- 13.20 Hangzhou Silan Microelectronic(China)
  - 13.20.1 Hangzhou Silan Microelectronic(China) Company Information
  - 13.20.2 Hangzhou Silan Microelectronic(China) Parallel Interface MRAM Product Portfolios and Specifications
  - 13.20.3 Hangzhou Silan Microelectronic(China) Parallel Interface MRAM Sales, Revenue, Price and Gross Margin (2021-2026)

13.20.4 Hangzhou Silan Microelectronic(China) Main Business Overview

13.20.5 Hangzhou Silan Microelectronic(China) Latest Developments

## **14 RESEARCH FINDINGS AND CONCLUSION**

## List Of Tables

### LIST OF TABLES

Table 1. Parallel Interface MRAM Annual Sales CAGR by Geographic Region (2021, 2025 & 2032) & (\$ millions)

Table 2. Parallel Interface MRAM Annual Sales CAGR by Country/Region (2021, 2025 & 2032) & (\$ millions)

Table 3. Major Players of Parallel SRAM-Compatible MRAM

Table 4. Major Players of Parallel Asynchronous MRAM

Table 5. Major Players of Burst Mode Parallel MRAM

Table 6. Major Players of High-Reliability Parallel MRAM

Table 7. Global Parallel Interface MRAM Sales by Interface Protocol (2021-2026) & (Million Units)

Table 8. Global Parallel Interface MRAM Sales Market Share by Interface Protocol (2021-2026)

Table 9. Global Parallel Interface MRAM Revenue by Interface Protocol (2021-2026) & (\$ million)

Table 10. Global Parallel Interface MRAM Revenue Market Share by Interface Protocol (2021-2026)

Table 11. Global Parallel Interface MRAM Sale Price by Interface Protocol (2021-2026) & (US\$/Unit)

Table 12. Major Players of x8 Parallel MRAM

Table 13. Major Players of x16 Parallel MRAM

Table 14. Major Players of x32 Parallel MRAM

Table 15. Global Parallel Interface MRAM Sales by Parameter Sub-Dimension (2021-2026) & (Million Units)

Table 16. Global Parallel Interface MRAM Sales Market Share by Parameter Sub-Dimension (2021-2026)

Table 17. Global Parallel Interface MRAM Revenue by Parameter Sub-Dimension (2021-2026) & (\$ million)

Table 18. Global Parallel Interface MRAM Revenue Market Share by Parameter Sub-Dimension (2021-2026)

Table 19. Global Parallel Interface MRAM Sale Price by Parameter Sub-Dimension (2021-2026) & (US\$/Unit)

Table 20. Major Players of Discrete Parallel MRAM IC

Table 21. Major Players of Multi-chip Module Parallel MRAM

Table 22. Major Players of Embedded Parallel MRAM IP

Table 23. Global Parallel Interface MRAM Sales by Product Form (2021-2026) &

(Million Units)

Table 24. Global Parallel Interface MRAM Sales Market Share by Product Form (2021-2026)

Table 25. Global Parallel Interface MRAM Revenue by Product Form (2021-2026) & (\$ million)

Table 26. Global Parallel Interface MRAM Revenue Market Share by Product Form (2021-2026)

Table 27. Global Parallel Interface MRAM Sale Price by Product Form (2021-2026) & (US\$/Unit)

Table 28. Global Parallel Interface MRAM Sale by Application (2021-2026) & (Million Units)

Table 29. Global Parallel Interface MRAM Sale Market Share by Application (2021-2026)

Table 30. Global Parallel Interface MRAM Revenue by Application (2021-2026) & (\$ million)

Table 31. Global Parallel Interface MRAM Revenue Market Share by Application (2021-2026)

Table 32. Global Parallel Interface MRAM Sale Price by Application (2021-2026) & (US\$/Unit)

Table 33. Global Parallel Interface MRAM Sales by Company (2021-2026) & (Million Units)

Table 34. Global Parallel Interface MRAM Sales Market Share by Company (2021-2026)

Table 35. Global Parallel Interface MRAM Revenue by Company (2021-2026) & (\$ millions)

Table 36. Global Parallel Interface MRAM Revenue Market Share by Company (2021-2026)

Table 37. Global Parallel Interface MRAM Sale Price by Company (2021-2026) & (US\$/Unit)

Table 38. Key Manufacturers Parallel Interface MRAM Producing Area Distribution and Sales Area

Table 39. Players Parallel Interface MRAM Products Offered

Table 40. Parallel Interface MRAM Concentration Ratio (CR3, CR5 and CR10) & (2024-2026)

Table 41. New Products and Potential Entrants

Table 42. Market M&A Activity & Strategy

Table 43. Global Parallel Interface MRAM Sales by Geographic Region (2021-2026) & (Million Units)

Table 44. Global Parallel Interface MRAM Sales Market Share Geographic Region

(2021-2026)

Table 45. Global Parallel Interface MRAM Revenue by Geographic Region (2021-2026) & (\$ millions)

Table 46. Global Parallel Interface MRAM Revenue Market Share by Geographic Region (2021-2026)

Table 47. Global Parallel Interface MRAM Sales by Country/Region (2021-2026) & (Million Units)

Table 48. Global Parallel Interface MRAM Sales Market Share by Country/Region (2021-2026)

Table 49. Global Parallel Interface MRAM Revenue by Country/Region (2021-2026) & (\$ millions)

Table 50. Global Parallel Interface MRAM Revenue Market Share by Country/Region (2021-2026)

Table 51. Americas Parallel Interface MRAM Sales by Country (2021-2026) & (Million Units)

Table 52. Americas Parallel Interface MRAM Sales Market Share by Country (2021-2026)

Table 53. Americas Parallel Interface MRAM Revenue by Country (2021-2026) & (\$ millions)

Table 54. Americas Parallel Interface MRAM Sales by Interface Protocol (2021-2026) & (Million Units)

Table 55. Americas Parallel Interface MRAM Sales by Application (2021-2026) & (Million Units)

Table 56. APAC Parallel Interface MRAM Sales by Region (2021-2026) & (Million Units)

Table 57. APAC Parallel Interface MRAM Sales Market Share by Region (2021-2026)

Table 58. APAC Parallel Interface MRAM Revenue by Region (2021-2026) & (\$ millions)

Table 59. APAC Parallel Interface MRAM Sales by Interface Protocol (2021-2026) & (Million Units)

Table 60. APAC Parallel Interface MRAM Sales by Application (2021-2026) & (Million Units)

Table 61. Europe Parallel Interface MRAM Sales by Country (2021-2026) & (Million Units)

Table 62. Europe Parallel Interface MRAM Revenue by Country (2021-2026) & (\$ millions)

Table 63. Europe Parallel Interface MRAM Sales by Interface Protocol (2021-2026) & (Million Units)

Table 64. Europe Parallel Interface MRAM Sales by Application (2021-2026) & (Million Units)

Table 65. Middle East & Africa Parallel Interface MRAM Sales by Country (2021-2026) & (Million Units)

Table 66. Middle East & Africa Parallel Interface MRAM Revenue Market Share by Country (2021-2026)

Table 67. Middle East & Africa Parallel Interface MRAM Sales by Interface Protocol (2021-2026) & (Million Units)

Table 68. Middle East & Africa Parallel Interface MRAM Sales by Application (2021-2026) & (Million Units)

Table 69. Key Market Drivers & Growth Opportunities of Parallel Interface MRAM

Table 70. Key Market Challenges & Risks of Parallel Interface MRAM

Table 71. Key Industry Trends of Parallel Interface MRAM

Table 72. Parallel Interface MRAM Raw Material

Table 73. Key Suppliers of Raw Materials

Table 74. Parallel Interface MRAM Distributors List

Table 75. Parallel Interface MRAM Customer List

Table 76. Global Parallel Interface MRAM Sales Forecast by Region (2027-2032) & (Million Units)

Table 77. Global Parallel Interface MRAM Revenue Forecast by Region (2027-2032) & (\$ millions)

Table 78. Americas Parallel Interface MRAM Sales Forecast by Country (2027-2032) & (Million Units)

Table 79. Americas Parallel Interface MRAM Annual Revenue Forecast by Country (2027-2032) & (\$ millions)

Table 80. APAC Parallel Interface MRAM Sales Forecast by Region (2027-2032) & (Million Units)

Table 81. APAC Parallel Interface MRAM Annual Revenue Forecast by Region (2027-2032) & (\$ millions)

Table 82. Europe Parallel Interface MRAM Sales Forecast by Country (2027-2032) & (Million Units)

Table 83. Europe Parallel Interface MRAM Revenue Forecast by Country (2027-2032) & (\$ millions)

Table 84. Middle East & Africa Parallel Interface MRAM Sales Forecast by Country (2027-2032) & (Million Units)

Table 85. Middle East & Africa Parallel Interface MRAM Revenue Forecast by Country (2027-2032) & (\$ millions)

Table 86. Global Parallel Interface MRAM Sales Forecast by Interface Protocol (2027-2032) & (Million Units)

Table 87. Global Parallel Interface MRAM Revenue Forecast by Interface Protocol (2027-2032) & (\$ millions)

Table 88. Global Parallel Interface MRAM Sales Forecast by Application (2027-2032) & (Million Units)

Table 89. Global Parallel Interface MRAM Revenue Forecast by Application (2027-2032) & (\$ millions)

Table 90. Spin Memory Basic Information, Parallel Interface MRAM Manufacturing Base, Sales Area and Its Competitors

Table 91. Spin Memory Parallel Interface MRAM Product Portfolios and Specifications

Table 92. Spin Memory Parallel Interface MRAM Sales (Million Units), Revenue (\$ Million), Price (US\$/Unit) and Gross Margin (2021-2026)

Table 93. Spin Memory Main Business

Table 94. Spin Memory Latest Developments

Table 95. Avalanche Technology Basic Information, Parallel Interface MRAM Manufacturing Base, Sales Area and Its Competitors

Table 96. Avalanche Technology Parallel Interface MRAM Product Portfolios and Specifications

Table 97. Avalanche Technology Parallel Interface MRAM Sales (Million Units), Revenue (\$ Million), Price (US\$/Unit) and Gross Margin (2021-2026)

Table 98. Avalanche Technology Main Business

Table 99. Avalanche Technology Latest Developments

Table 100. Micron Technology Inc. Basic Information, Parallel Interface MRAM Manufacturing Base, Sales Area and Its Competitors

Table 101. Micron Technology Inc. Parallel Interface MRAM Product Portfolios and Specifications

Table 102. Micron Technology Inc. Parallel Interface MRAM Sales (Million Units), Revenue (\$ Million), Price (US\$/Unit) and Gross Margin (2021-2026)

Table 103. Micron Technology Inc. Main Business

Table 104. Micron Technology Inc. Latest Developments

Table 105. Infineon Technologies (Germany) Basic Information, Parallel Interface MRAM Manufacturing Base, Sales Area and Its Competitors

Table 106. Infineon Technologies (Germany) Parallel Interface MRAM Product Portfolios and Specifications

Table 107. Infineon Technologies (Germany) Parallel Interface MRAM Sales (Million Units), Revenue (\$ Million), Price (US\$/Unit) and Gross Margin (2021-2026)

Table 108. Infineon Technologies (Germany) Main Business

Table 109. Infineon Technologies (Germany) Latest Developments

Table 110. Renesas Electronics Corporation Basic Information, Parallel Interface MRAM Manufacturing Base, Sales Area and Its Competitors

Table 111. Renesas Electronics Corporation Parallel Interface MRAM Product Portfolios and Specifications

Table 112. Renesas Electronics Corporation Parallel Interface MRAM Sales (Million Units), Revenue (\$ Million), Price (US\$/Unit) and Gross Margin (2021-2026)

Table 113. Renesas Electronics Corporation Main Business

Table 114. Renesas Electronics Corporation Latest Developments

Table 115. Everspin Technologies Basic Information, Parallel Interface MRAM Manufacturing Base, Sales Area and Its Competitors

Table 116. Everspin Technologies Parallel Interface MRAM Product Portfolios and Specifications

Table 117. Everspin Technologies Parallel Interface MRAM Sales (Million Units), Revenue (\$ Million), Price (US\$/Unit) and Gross Margin (2021-2026)

Table 118. Everspin Technologies Main Business

Table 119. Everspin Technologies Latest Developments

Table 120. Honeywell Basic Information, Parallel Interface MRAM Manufacturing Base, Sales Area and Its Competitors

Table 121. Honeywell Parallel Interface MRAM Product Portfolios and Specifications

Table 122. Honeywell Parallel Interface MRAM Sales (Million Units), Revenue (\$ Million), Price (US\$/Unit) and Gross Margin (2021-2026)

Table 123. Honeywell Main Business

Table 124. Honeywell Latest Developments

Table 125. NXP Semiconductors Basic Information, Parallel Interface MRAM Manufacturing Base, Sales Area and Its Competitors

Table 126. NXP Semiconductors Parallel Interface MRAM Product Portfolios and Specifications

Table 127. NXP Semiconductors Parallel Interface MRAM Sales (Million Units), Revenue (\$ Million), Price (US\$/Unit) and Gross Margin (2021-2026)

Table 128. NXP Semiconductors Main Business

Table 129. NXP Semiconductors Latest Developments

Table 130. STMicroelectronics Basic Information, Parallel Interface MRAM Manufacturing Base, Sales Area and Its Competitors

Table 131. STMicroelectronics Parallel Interface MRAM Product Portfolios and Specifications

Table 132. STMicroelectronics Parallel Interface MRAM Sales (Million Units), Revenue (\$ Million), Price (US\$/Unit) and Gross Margin (2021-2026)

Table 133. STMicroelectronics Main Business

Table 134. STMicroelectronics Latest Developments

Table 135. Nve Corporation Basic Information, Parallel Interface MRAM Manufacturing Base, Sales Area and Its Competitors

Table 136. Nve Corporation Parallel Interface MRAM Product Portfolios and Specifications

Table 137. Nve Corporation Parallel Interface MRAM Sales (Million Units), Revenue (\$ Million), Price (US\$/Unit) and Gross Margin (2021-2026)

Table 138. Nve Corporation Main Business

Table 139. Nve Corporation Latest Developments

Table 140. Siproin Microelectronics Basic Information, Parallel Interface MRAM Manufacturing Base, Sales Area and Its Competitors

Table 141. Siproin Microelectronics Parallel Interface MRAM Product Portfolios and Specifications

Table 142. Siproin Microelectronics Parallel Interface MRAM Sales (Million Units), Revenue (\$ Million), Price (US\$/Unit) and Gross Margin (2021-2026)

Table 143. Siproin Microelectronics Main Business

Table 144. Siproin Microelectronics Latest Developments

Table 145. GigaDevice Semiconductor Basic Information, Parallel Interface MRAM Manufacturing Base, Sales Area and Its Competitors

Table 146. GigaDevice Semiconductor Parallel Interface MRAM Product Portfolios and Specifications

Table 147. GigaDevice Semiconductor Parallel Interface MRAM Sales (Million Units), Revenue (\$ Million), Price (US\$/Unit) and Gross Margin (2021-2026)

Table 148. GigaDevice Semiconductor Main Business

Table 149. GigaDevice Semiconductor Latest Developments

Table 150. Fudan Microelectronics Group Basic Information, Parallel Interface MRAM Manufacturing Base, Sales Area and Its Competitors

Table 151. Fudan Microelectronics Group Parallel Interface MRAM Product Portfolios and Specifications

Table 152. Fudan Microelectronics Group Parallel Interface MRAM Sales (Million Units), Revenue (\$ Million), Price (US\$/Unit) and Gross Margin (2021-2026)

Table 153. Fudan Microelectronics Group Main Business

Table 154. Fudan Microelectronics Group Latest Developments

Table 155. Wuhan Xinxin Semiconductor Basic Information, Parallel Interface MRAM Manufacturing Base, Sales Area and Its Competitors

Table 156. Wuhan Xinxin Semiconductor Parallel Interface MRAM Product Portfolios and Specifications

Table 157. Wuhan Xinxin Semiconductor Parallel Interface MRAM Sales (Million Units), Revenue (\$ Million), Price (US\$/Unit) and Gross Margin (2021-2026)

Table 158. Wuhan Xinxin Semiconductor Main Business

Table 159. Wuhan Xinxin Semiconductor Latest Developments

Table 160. Changxin Memory Technologies Basic Information, Parallel Interface MRAM Manufacturing Base, Sales Area and Its Competitors

Table 161. Changxin Memory Technologies Parallel Interface MRAM Product Portfolios

and Specifications

Table 162. Changxin Memory Technologies Parallel Interface MRAM Sales (Million Units), Revenue (\$ Million), Price (US\$/Unit) and Gross Margin (2021-2026)

Table 163. Changxin Memory Technologies Main Business

Table 164. Changxin Memory Technologies Latest Developments

Table 165. Suzhou Memory Technology Co., Ltd.(China) Basic Information, Parallel Interface MRAM Manufacturing Base, Sales Area and Its Competitors

Table 166. Suzhou Memory Technology Co., Ltd.(China) Parallel Interface MRAM Product Portfolios and Specifications

Table 167. Suzhou Memory Technology Co., Ltd.(China) Parallel Interface MRAM Sales (Million Units), Revenue (\$ Million), Price (US\$/Unit) and Gross Margin (2021-2026)

Table 168. Suzhou Memory Technology Co., Ltd.(China) Main Business

Table 169. Suzhou Memory Technology Co., Ltd.(China) Latest Developments

Table 170. Beijing Tongfang Microelectronics(China) Basic Information, Parallel Interface MRAM Manufacturing Base, Sales Area and Its Competitors

Table 171. Beijing Tongfang Microelectronics(China) Parallel Interface MRAM Product Portfolios and Specifications

Table 172. Beijing Tongfang Microelectronics(China) Parallel Interface MRAM Sales (Million Units), Revenue (\$ Million), Price (US\$/Unit) and Gross Margin (2021-2026)

Table 173. Beijing Tongfang Microelectronics(China) Main Business

Table 174. Beijing Tongfang Microelectronics(China) Latest Developments

Table 175. Nanjing SemiDrive Technology(China) Basic Information, Parallel Interface MRAM Manufacturing Base, Sales Area and Its Competitors

Table 176. Nanjing SemiDrive Technology(China) Parallel Interface MRAM Product Portfolios and Specifications

Table 177. Nanjing SemiDrive Technology(China) Parallel Interface MRAM Sales (Million Units), Revenue (\$ Million), Price (US\$/Unit) and Gross Margin (2021-2026)

Table 178. Nanjing SemiDrive Technology(China) Main Business

Table 179. Nanjing SemiDrive Technology(China) Latest Developments

Table 180. Zhuhai Jieli Technology(China) Basic Information, Parallel Interface MRAM Manufacturing Base, Sales Area and Its Competitors

Table 181. Zhuhai Jieli Technology(China) Parallel Interface MRAM Product Portfolios and Specifications

Table 182. Zhuhai Jieli Technology(China) Parallel Interface MRAM Sales (Million Units), Revenue (\$ Million), Price (US\$/Unit) and Gross Margin (2021-2026)

Table 183. Zhuhai Jieli Technology(China) Main Business

Table 184. Zhuhai Jieli Technology(China) Latest Developments

Table 185. Hangzhou Silan Microelectronic(China) Basic Information, Parallel Interface MRAM Manufacturing Base, Sales Area and Its Competitors

Table 186. Hangzhou Silan Microelectronic(China) Parallel Interface MRAM Product Portfolios and Specifications

Table 187. Hangzhou Silan Microelectronic(China) Parallel Interface MRAM Sales (Million Units), Revenue (\$ Million), Price (US\$/Unit) and Gross Margin (2021-2026)

Table 188. Hangzhou Silan Microelectronic(China) Main Business

Table 189. Hangzhou Silan Microelectronic(China) Latest Developments

## List Of Figures

### LIST OF FIGURES

- Figure 1. Picture of Parallel Interface MRAM
- Figure 2. Parallel Interface MRAM Report Years Considered
- Figure 3. Research Objectives
- Figure 4. Research Methodology
- Figure 5. Research Process and Data Source
- Figure 6. Global Parallel Interface MRAM Sales Growth Rate 2021-2032 (Million Units)
- Figure 7. Global Parallel Interface MRAM Revenue Growth Rate 2021-2032 (\$ millions)
- Figure 8. Parallel Interface MRAM Sales by Geographic Region (2021, 2025 & 2032) & (\$ millions)
- Figure 9. Parallel Interface MRAM Sales Market Share by Country/Region (2025)
- Figure 10. Parallel Interface MRAM Sales Market Share by Country/Region (2021, 2025 & 2032)
- Figure 11. Product Picture of Parallel SRAM-Compatible MRAM
- Figure 12. Product Picture of Parallel Asynchronous MRAM
- Figure 13. Product Picture of Burst Mode Parallel MRAM
- Figure 14. Product Picture of High-Reliability Parallel MRAM
- Figure 15. Global Parallel Interface MRAM Sales Market Share by Interface Protocol in 2026
- Figure 16. Global Parallel Interface MRAM Revenue Market Share by Interface Protocol (2021-2026)
- Figure 17. Product Picture of x8 Parallel MRAM
- Figure 18. Product Picture of x16 Parallel MRAM
- Figure 19. Product Picture of x32 Parallel MRAM
- Figure 20. Global Parallel Interface MRAM Sales Market Share by Parameter Sub-Dimension in 2026
- Figure 21. Global Parallel Interface MRAM Revenue Market Share by Parameter Sub-Dimension (2021-2026)
- Figure 22. Product Picture of Discrete Parallel MRAM IC
- Figure 23. Product Picture of Multi-chip Module Parallel MRAM
- Figure 24. Product Picture of Embedded Parallel MRAM IP
- Figure 25. Global Parallel Interface MRAM Sales Market Share by Product Form in 2026
- Figure 26. Global Parallel Interface MRAM Revenue Market Share by Product Form (2021-2026)
- Figure 27. Parallel Interface MRAM Consumed in Industrial

Figure 28. Global Parallel Interface MRAM Market: Industrial (2021-2026) & (Million Units)

Figure 29. Parallel Interface MRAM Consumed in Automotive

Figure 30. Global Parallel Interface MRAM Market: Automotive (2021-2026) & (Million Units)

Figure 31. Parallel Interface MRAM Consumed in Aerospace & Defense

Figure 32. Global Parallel Interface MRAM Market: Aerospace & Defense (2021-2026) & (Million Units)

Figure 33. Parallel Interface MRAM Consumed in Commercial

Figure 34. Global Parallel Interface MRAM Market: Commercial (2021-2026) & (Million Units)

Figure 35. Parallel Interface MRAM Consumed in Others

Figure 36. Global Parallel Interface MRAM Market: Others (2021-2026) & (Million Units)

Figure 37. Global Parallel Interface MRAM Sale Market Share by Application (2025)

Figure 38. Global Parallel Interface MRAM Revenue Market Share by Application in 2025

Figure 39. Parallel Interface MRAM Sales by Company in 2025 (Million Units)

Figure 40. Global Parallel Interface MRAM Sales Market Share by Company in 2025

Figure 41. Parallel Interface MRAM Revenue by Company in 2025 (\$ millions)

Figure 42. Global Parallel Interface MRAM Revenue Market Share by Company in 2025

Figure 43. Global Parallel Interface MRAM Sales Market Share by Geographic Region (2021-2026)

Figure 44. Global Parallel Interface MRAM Revenue Market Share by Geographic Region in 2025

Figure 45. Americas Parallel Interface MRAM Sales 2021-2026 (Million Units)

Figure 46. Americas Parallel Interface MRAM Revenue 2021-2026 (\$ millions)

Figure 47. APAC Parallel Interface MRAM Sales 2021-2026 (Million Units)

Figure 48. APAC Parallel Interface MRAM Revenue 2021-2026 (\$ millions)

Figure 49. Europe Parallel Interface MRAM Sales 2021-2026 (Million Units)

Figure 50. Europe Parallel Interface MRAM Revenue 2021-2026 (\$ millions)

Figure 51. Middle East & Africa Parallel Interface MRAM Sales 2021-2026 (Million Units)

Figure 52. Middle East & Africa Parallel Interface MRAM Revenue 2021-2026 (\$ millions)

Figure 53. Americas Parallel Interface MRAM Sales Market Share by Country in 2025

Figure 54. Americas Parallel Interface MRAM Revenue Market Share by Country (2021-2026)

Figure 55. Americas Parallel Interface MRAM Sales Market Share by Interface Protocol (2021-2026)

Figure 56. Americas Parallel Interface MRAM Sales Market Share by Application (2021-2026)

Figure 57. United States Parallel Interface MRAM Revenue Growth 2021-2026 (\$ millions)

Figure 58. Canada Parallel Interface MRAM Revenue Growth 2021-2026 (\$ millions)

Figure 59. Mexico Parallel Interface MRAM Revenue Growth 2021-2026 (\$ millions)

Figure 60. Brazil Parallel Interface MRAM Revenue Growth 2021-2026 (\$ millions)

Figure 61. APAC Parallel Interface MRAM Sales Market Share by Region in 2025

Figure 62. APAC Parallel Interface MRAM Revenue Market Share by Region (2021-2026)

Figure 63. APAC Parallel Interface MRAM Sales Market Share by Interface Protocol (2021-2026)

Figure 64. APAC Parallel Interface MRAM Sales Market Share by Application (2021-2026)

Figure 65. China Parallel Interface MRAM Revenue Growth 2021-2026 (\$ millions)

Figure 66. Japan Parallel Interface MRAM Revenue Growth 2021-2026 (\$ millions)

Figure 67. South Korea Parallel Interface MRAM Revenue Growth 2021-2026 (\$ millions)

Figure 68. Southeast Asia Parallel Interface MRAM Revenue Growth 2021-2026 (\$ millions)

Figure 69. India Parallel Interface MRAM Revenue Growth 2021-2026 (\$ millions)

Figure 70. Australia Parallel Interface MRAM Revenue Growth 2021-2026 (\$ millions)

Figure 71. China Taiwan Parallel Interface MRAM Revenue Growth 2021-2026 (\$ millions)

Figure 72. Europe Parallel Interface MRAM Sales Market Share by Country in 2025

Figure 73. Europe Parallel Interface MRAM Revenue Market Share by Country (2021-2026)

Figure 74. Europe Parallel Interface MRAM Sales Market Share by Interface Protocol (2021-2026)

Figure 75. Europe Parallel Interface MRAM Sales Market Share by Application (2021-2026)

Figure 76. Germany Parallel Interface MRAM Revenue Growth 2021-2026 (\$ millions)

Figure 77. France Parallel Interface MRAM Revenue Growth 2021-2026 (\$ millions)

Figure 78. UK Parallel Interface MRAM Revenue Growth 2021-2026 (\$ millions)

Figure 79. Italy Parallel Interface MRAM Revenue Growth 2021-2026 (\$ millions)

Figure 80. Russia Parallel Interface MRAM Revenue Growth 2021-2026 (\$ millions)

Figure 81. Middle East & Africa Parallel Interface MRAM Sales Market Share by Country (2021-2026)

Figure 82. Middle East & Africa Parallel Interface MRAM Sales Market Share by

Interface Protocol (2021-2026)

Figure 83. Middle East & Africa Parallel Interface MRAM Sales Market Share by Application (2021-2026)

Figure 84. Egypt Parallel Interface MRAM Revenue Growth 2021-2026 (\$ millions)

Figure 85. South Africa Parallel Interface MRAM Revenue Growth 2021-2026 (\$ millions)

Figure 86. Israel Parallel Interface MRAM Revenue Growth 2021-2026 (\$ millions)

Figure 87. Turkey Parallel Interface MRAM Revenue Growth 2021-2026 (\$ millions)

Figure 88. GCC Countries Parallel Interface MRAM Revenue Growth 2021-2026 (\$ millions)

Figure 89. Manufacturing Cost Structure Analysis of Parallel Interface MRAM in 2026

Figure 90. Manufacturing Process Analysis of Parallel Interface MRAM

Figure 91. Industry Chain Structure of Parallel Interface MRAM

Figure 92. Channels of Distribution

Figure 93. Global Parallel Interface MRAM Sales Market Forecast by Region (2027-2032)

Figure 94. Global Parallel Interface MRAM Revenue Market Share Forecast by Region (2027-2032)

Figure 95. Global Parallel Interface MRAM Sales Market Share Forecast by Interface Protocol (2027-2032)

Figure 96. Global Parallel Interface MRAM Revenue Market Share Forecast by Interface Protocol (2027-2032)

Figure 97. Global Parallel Interface MRAM Sales Market Share Forecast by Application (2027-2032)

Figure 98. Global Parallel Interface MRAM Revenue Market Share Forecast by Application (2027-2032)

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