

Global MRAM Market Growth 2026-2032

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Abstracts

The global MRAM market size is predicted to grow from US\$ 155 million in 2025 to US\$ 860 million in 2032; it is expected to grow at a CAGR of 26.8% from 2026 to 2032.

Magnetoresistive Random Access Memory (MRAM) is a non-volatile storage technology based on the magnetoresistive effect, with its core unit being a magnetic tunnel junction (MTJ), composed of two ferromagnetic layers sandwiching an insulating tunnel barrier layer. By controlling the magnetization direction of the free layer and the fixed layer to be parallel or antiparallel, the MTJ exhibits a low-resistance state (logic 0) or a high-resistance state (logic 1), thereby enabling data storage. The technological evolution of MRAM has undergone three generations:

First generation: Magnetically driven MRAM, which requires an external magnetic field for writing and has relatively low efficiency.

Second generation: Spin-transfer torque MRAM (STT-MRAM), which flips the magnetic moment using a current perpendicular to the MTJ, has been commercially mass-produced, with speeds approaching SRAM and durability exceeding 1E15 cycles.

Third generation: Spin-orbit torque MRAM (SOT-MRAM) and voltage-controlled magnetic anisotropy MRAM (VCMA-MRAM). Among these, SOT-MRAM uses in-plane current to generate spin-orbit torque to flip the magnetic moment, achieving write speeds of up to 0.4 nanoseconds, with power consumption only 1% of STT-MRAM, and supports in-memory computing, making it the next-generation mainstream technology.

Global MRAM sales reached 32,981,000 units in 2025, with an average price of US\$4.79 per unit.

Development Trends

SOT-MRAM Dominates Next Generation: 0.4ns write speed, 99% power reduction, commercialisation for automotive/industrial applications by 2026, suitable for AI edge computing.

VCMA-MRAM Breakthrough: Voltage-driven, phased-array power consumption, poised to enter mainstream embedded applications by 2028.

3D Stacking + New Materials: Graphene magnetic materials boost performance by 50% and reduce power consumption by 30%; 3D stacking density increases over tenfold.

In-Memory Computing (CIM) Integration: Merging MRAM with computational units enhances AI inference energy efficiency by 10-fold, enabling edge/endpoint AI applications.

Downstream (Applications / Systems)

Replacing traditional storage: eMRAM replaces eFlash (addressing scaling bottlenecks below 28nm) and certain SRAM applications; standalone MRAM replaces NOR Flash and low-capacity DRAM, restructuring the storage hierarchy.

End-device upgrades:

Automotive: eMRAM adopted in ADAS MCUs and autonomous driving domain controllers, enhancing real-time performance and reliability; automotive-grade market set for explosive growth from 2026.

AIoT: Low-power, non-volatile characteristics suit massive sensor nodes, supporting 50 billion IoT device demands.

Data centres: MRAM as cache reduces DRAM power consumption and boosts I/O performance.

System architecture transformation: Integrated compute-storage architectures proliferate, enhancing edge AI computing power while reducing power consumption; non-volatile memory simplifies system design, enabling instant boot and power-off data protection.

LP Information, Inc. (LPI) ' newest research report, the "MRAM Industry Forecast"

looks at past sales and reviews total world MRAM sales in 2025, providing a comprehensive analysis by region and market sector of projected MRAM sales for 2026 through 2032. With MRAM sales broken down by region, market sector and sub-sector, this report provides a detailed analysis in US\$ millions of the world MRAM industry.

This Insight Report provides a comprehensive analysis of the global MRAM landscape and highlights key trends related to product segmentation, company formation, revenue, and market share, latest development, and M&A activity. This report also analyzes the strategies of leading global companies with a focus on MRAM portfolios and capabilities, market entry strategies, market positions, and geographic footprints, to better understand these firms' unique position in an accelerating global MRAM market.

This Insight Report evaluates the key market trends, drivers, and affecting factors shaping the global outlook for MRAM and breaks down the forecast by Type, by Application, geography, and market size to highlight emerging pockets of opportunity. With a transparent methodology based on hundreds of bottom-up qualitative and quantitative market inputs, this study forecast offers a highly nuanced view of the current state and future trajectory in the global MRAM.

This report presents a comprehensive overview, market shares, and growth opportunities of MRAM market by product type, application, key manufacturers and key regions and countries.

Segmentation by Type:

Toggle MRAM

STT-MRAM

Other

Segmentation by morphology:

Embedded MRAM (eMRAM)

Standalone MRAM

Segmentation by Sales Channels:

Direct Sales

Distribution

Segmentation by Application:

Aerospace

Automotive

Factory Automation

Enterprise Storage

Internet of Things

Others

This report also splits the market by region:

Americas

United States

Canada

Mexico

Brazil

APAC

China

Japan

Korea

Southeast Asia

India

Australia

Europe

Germany

France

UK

Italy

Russia

Middle East & Africa

Egypt

South Africa

Israel

Turkey

GCC Countries

The below companies that are profiled have been selected based on inputs gathered from primary experts and analysing the company's coverage, product portfolio, its market penetration.

Everspin Technologies

Avalanche Technology

Honeywell

Renesas

Samsung Electronics

Hikstor Technology

NVE Corporation

KOWIN Technology

Key Questions Addressed in this Report

What is the 10-year outlook for the global MRAM market?

What factors are driving MRAM market growth, globally and by region?

Which technologies are poised for the fastest growth by market and region?

How do MRAM market opportunities vary by end market size?

How does MRAM break out by Type, by Application?

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