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Global Mid-End Design Services Market Growth (Status and Outlook) 2026-2032

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Abstracts

The global Mid-End Design Services market size is predicted to grow from US$ 4311 million in 2025 to US$ 7121 million in 2032; it is expected to grow at a CAGR of 7.5% from 2026 to 2032.

Mid-End design services are engineering services for ASIC and SoC projects that transition RTL deliverables toward manufacturable physical implementation, focusing on synthesizability, testability, timing constraints, low-power intent, formal consistency, and back-end convergence risks. These services typically take chip specifications, RTL, netlists, or existing design assets as inputs and deliver logic synthesis, DFT insertion, static timing analysis, equivalence checking, power constraints, clock and reset domain checks, test structures, gate-level verification, and physically aware optimization. They provide quality-controlled netlists, constraints, test vectors, and design reports to support place and route, signoff, and tape-out. Typical customers include fabless semiconductor companies, system vendors, IP and platform companies, automotive electronics and AI computing chip teams, and semiconductor organizations that require external design capacity and advanced-node engineering expertise. Delivery models include specialized module services, staff augmentation, RTL-to-GDSII collaboration, turnkey ASIC services, and pre-production engineering support. The commercial value lies in reducing rework costs, shortening design cycles, improving PPA convergence quality, increasing test coverage, and addressing engineering risks in complex process nodes and multi-voltage-domain designs earlier in the flow.

Mid-End design services are evolving from conventional engineering outsourcing into a critical quality-control function within the chip development flow. As ASIC and SoC projects become larger, design teams are no longer challenged only by completing RTL code, but by ensuring that RTL can be reliably synthesized, constraints can be

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interpreted by the back-end flow, test structures can meet production-quality requirements, and low-power intent remains consistent across synthesis, verification, and physical implementation. The value of mid-end services is therefore concentrated in the engineering conversion of design deliverables, transforming logic descriptions created through architecture and front-end verification into netlists, constraints, test structures, and convergence reports that back-end teams can execute. For advanced-node, multi-voltage-domain, and high-performance computing chips, mid-end quality directly affects place-and-route iteration cycles, signoff risk, and tape-out schedules. Purchasing mid-end design services is essentially a purchase of measurable engineering certainty, including timing margin, test coverage, power-constraint consistency, formal-equivalence pass rates, and handoff quality.

Demand growth is driven by the combined expansion of multiple downstream applications. AI computing, automotive electronics, data centers, communications networks, and industrial control are increasing the need for customized chips, which typically require high compute capability, high bandwidth, low power, strong reliability, and short time to market. Traditional internal design teams often cannot allocate advanced-node expertise, DFT specialists, STA specialists, low-power engineers, formal-verification engineers, and physical-implementation coordination resources to every project. As a result, external mid-end design services have become an important option for expanding R&D capacity and reducing project risk. At the same time, the development of chiplets, advanced packaging, and the RISC V ecosystem is making chip-integration boundaries more complex. Design-service providers need to address more interface, clock, reset, power-domain, and test-access issues in the mid-end phase. Future competition will depend not only on engineering headcount, but also on flow assets, EDA script libraries, process-node experience, test methodologies, and the depth of collaboration with the foundry ecosystem.

The regional landscape shows a clear pattern of value-chain specialization. Taiwan, South Korea, and Japan have mature foundry, ASIC platform, and SoC design ecosystems, with advantages in advanced nodes, platform-based delivery, and production ramp-up. India has developed a scaled semiconductor engineering-services supply base that can undertake large volumes of RTL, DFT, STA, physical-implementation, and verification-coordination work, making it suitable for global customers seeking flexible R&D capacity. Mainland China is benefiting from expanding domestic chip-design demand, import substitution, automotive electronics, and industrial-chip investment, and is building more design-service capabilities from IP integration to back-end delivery. European and U.S. companies more often serve automotive, industrial, high-reliability, and system-level customers, where project management,

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quality systems, and cross-region delivery are important. Overall, mid-end design services are expected to keep growing with the broader ASIC design-services market and capture higher value in advanced-node, automotive-electronics, AI-accelerator, and multi-chip integration projects.

Report Scope

LPI (LP Information)' newest research report, the “Mid-End Design Services Industry Forecast” looks at past sales and reviews total world Mid-End Design Services sales in 2025, providing a comprehensive analysis by region and market sector of projected Mid-End Design Services sales for 2026 through 2032. With Mid-End Design Services sales broken down by region, market sector and sub-sector, this report provides a detailed analysis in US$ millions of the world Mid-End Design Services industry.

This Insight Report provides a comprehensive analysis of the global Mid-End Design Services landscape and highlights key trends related to product segmentation, company formation, revenue, and market share, latest development, and M&A activity. This report also analyses the strategies of leading global companies with a focus on Mid-End Design Services portfolios and capabilities, market entry strategies, market positions, and geographic footprints, to better understand these firms’ unique position in an accelerating global Mid-End Design Services market.

This Insight Report evaluates the key market trends, drivers, and affecting factors shaping the global outlook for Mid-End Design Services and breaks down the forecast by Service Task, by Application, geography, and market size to highlight emerging pockets of opportunity. With a transparent methodology based on hundreds of bottom-up qualitative and quantitative market inputs, this study forecast offers a highly nuanced view of the current state and future trajectory in the global Mid-End Design Services.

This report presents a comprehensive overview, market shares, and growth opportunities of Mid-End Design Services market by product type, application, key players and key regions and countries.

Segmentation by Service Task:

Logic Synthesis Services

Design for Testability Services

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Static Timing Analysis Services

Low-Power Implementation Services

Formal Equivalence Verification Services

Clock and Reset Domain Check Services

Gate-Level Simulation Services

Other

Segmentation by Delivery Model:

Consulting and Assessment Services

Specialized Module Services

Staff Augmentation Services

Co-Design Services

RTL-to-GDSII Services

Turnkey ASIC Services

Other

Segmentation by Quality Objective:

Timing Closure Services

Power Closure Services

Area Closure Services

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Test Coverage Improvement Services

Manufacturability Improvement Services

Signoff Risk Reduction Services

Other

Segmentation by Application:

AI Computing

Consumer Electronics

Data Centers

Mobile Terminals

Other

This report also splits the market by region:

Americas

United States

Canada

Mexico

Brazil

APAC

China

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- Japan
- Korea
- Southeast Asia
- India
- Australia

Europe

- Germany
- France
- UK
- Italy
- Russia

Middle East & Africa

- Egypt
- South Africa
- Israel
- Turkey
- GCC Countries

The below companies that are profiled have been selected based on inputs gathered from primary experts and analyzing the company's coverage, product portfolio, its market penetration.

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Global Unichip Corporation

Faraday Technology Corporation

Alchip Technologies, Limited

VeriSilicon Microelectronics (Shanghai) Co., Ltd.

Brite Semiconductor (Shanghai) Co., Ltd.

Progate Group Corporation

eTech ASIC, Inc.

Rise Asia Corp. Ltd.

ADTechnology Co., Ltd.

SEMIFIVE Inc.

GAONCHIPS Co., Ltd.

CoAsia SEMI Ltd.

Socionext Inc.

MegaChips Corporation

Arrow Electronics, Inc. (eInfochips)

Tessolve Semiconductor Pvt. Ltd.

MosChip Technologies Limited

SignOff Semiconductors Pvt. Ltd.

Mirafra Technologies

AUMRaj Design Systems Private Limited

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Presto Engineering

ELSYS Design

CoreHW Oy

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