

Global Memory Chip Packaging Substrate Market Growth 2024-2030

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Abstracts

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IC substrate is the core material of the packaging link and has a high technical threshold. IC substrate directly loads the chip, provides an electronic connection between the chip and the PCB motherboard, plays the role of 'connecting the above and the next', and is a necessary core material in IC packaging technology. With the development of semiconductor technology, the characteristic size of IC continues to shrink, and the integration continues to improve. The corresponding IC package towards super multi-pin, narrow pitch, ultra-miniaturization direction in high-level packaging, IC substrate has replaced the traditional lead frame, becoming an indispensable part of the IC package. Memory chip packaging substrate is mainly used for smartphone and tablet computer storage modules and solid state disks.

The global Memory Chip Packaging Substrate market size is projected to grow from US\$ million in 2024 to US\$ million in 2030; it is expected to grow at a CAGR of %from 2024 to 2030.

LP Information, Inc. (LPI) ' newest research report, the "Memory Chip Packaging Substrate Industry Forecast" looks at past sales and reviews total world Memory Chip Packaging Substrate sales in 2023, providing a comprehensive analysis by region and market sector of projected Memory Chip Packaging Substrate sales for 2024 through 2030. With Memory Chip Packaging Substrate sales broken down by region, market sector and sub-sector, this report provides a detailed analysis in US\$ millions of the world Memory Chip Packaging Substrate industry.

This Insight Report provides a comprehensive analysis of the global Memory Chip

Packaging Substrate landscape and highlights key trends related to product segmentation, company formation, revenue, and market share, latest development, and M&A activity. This report also analyzes the strategies of leading global companies with a focus on Memory Chip Packaging Substrate portfolios and capabilities, market entry strategies, market positions, and geographic footprints, to better understand these firms' unique position in an accelerating global Memory Chip Packaging Substrate market.

This Insight Report evaluates the key market trends, drivers, and affecting factors shaping the global outlook for Memory Chip Packaging Substrate and breaks down the forecast by Type, by Application, geography, and market size to highlight emerging pockets of opportunity. With a transparent methodology based on hundreds of bottom-up qualitative and quantitative market inputs, this study forecast offers a highly nuanced view of the current state and future trajectory in the global Memory Chip Packaging Substrate.

The global market for semiconductor was estimated at US\$ 579 billion in the year 2022, is projected to US\$ 790 billion by 2029, growing at a CAGR of 6% during the forecast period. Although some major categories are still double-digit year-over-year growth in 2022, led by Analog with 20.76%, Sensor with 16.31%, and Logic with 14.46% growth, Memory declined with 12.64% year over year. The microprocessor (MPU) and microcontroller (MCU) segments will experience stagnant growth due to weak shipments and investment in notebooks, computers, and standard desktops. In the current market scenario, the growing popularity of IoT-based electronics is stimulating the need for powerful processors and controllers. Hybrid MPUs and MCUs provide real-time embedded processing and control for the topmost IoT-based applications, resulting in significant market growth. The Analog IC segment is expected to grow gradually, while demand from the networking and communications industries is limited. Few of the emerging trends in the growing demand for Analog integrated circuits include signal conversion, automotive-specific Analog applications, and power management. They drive the growing demand for discrete power devices.

This report presents a comprehensive overview, market shares, and growth opportunities of Memory Chip Packaging Substrate market by product type, application, key manufacturers and key regions and countries.

Segmentation by Type:

WB BGA

WB-CSP

Others

Segmentation by Application:

Flash Drive

Solid State Storage

Embedded Storage

Volatile Storage

Others

This report also splits the market by region:

Americas

United States

Canada

Mexico

Brazil

APAC

China

Japan

Korea

Southeast Asia

India

Australia

Europe

Germany

France

UK

Italy

Russia

Middle East & Africa

Egypt

South Africa

Israel

Turkey

GCC Countries

The below companies that are profiled have been selected based on inputs gathered from primary experts and analysing the company's coverage, product portfolio, its market penetration.

Samsung Electro-Mechanics

Simmtech

Daeduck

Korea Circuit

Ibiden

Kyocera

ASE Group

Shinko

Fujitsu Global

Doosan Electronic

Toppan Printing

Unimicron

Kinsus

Nanya

Fastprint Circuit

Shennan Circuits

Key Questions Addressed in this Report

What is the 10-year outlook for the global Memory Chip Packaging Substrate market?

What factors are driving Memory Chip Packaging Substrate market growth, globally and by region?

Which technologies are poised for the fastest growth by market and region?

How do Memory Chip Packaging Substrate market opportunities vary by end market

size?

How does Memory Chip Packaging Substrate break out by Type, by Application?

Contents

1 SCOPE OF THE REPORT

- 1.1 Market Introduction
- 1.2 Years Considered
- 1.3 Research Objectives
- 1.4 Market Research Methodology
- 1.5 Research Process and Data Source
- 1.6 Economic Indicators
- 1.7 Currency Considered
- 1.8 Market Estimation Caveats

2 EXECUTIVE SUMMARY

- 2.1 World Market Overview
 - 2.1.1 Global Memory Chip Packaging Substrate Annual Sales 2019-2030
 - 2.1.2 World Current & Future Analysis for Memory Chip Packaging Substrate by Geographic Region, 2019, 2023 & 2030
 - 2.1.3 World Current & Future Analysis for Memory Chip Packaging Substrate by Country/Region, 2019, 2023 & 2030
- 2.2 Memory Chip Packaging Substrate Segment by Type
 - 2.2.1 WB BGA
 - 2.2.2 WB-CSP
 - 2.2.3 Others
- 2.3 Memory Chip Packaging Substrate Sales by Type
 - 2.3.1 Global Memory Chip Packaging Substrate Sales Market Share by Type (2019-2024)
 - 2.3.2 Global Memory Chip Packaging Substrate Revenue and Market Share by Type (2019-2024)
 - 2.3.3 Global Memory Chip Packaging Substrate Sale Price by Type (2019-2024)
- 2.4 Memory Chip Packaging Substrate Segment by Application
 - 2.4.1 Flash Drive
 - 2.4.2 Solid State Storage
 - 2.4.3 Embedded Storage
 - 2.4.4 Volatile Storage
 - 2.4.5 Others
- 2.5 Memory Chip Packaging Substrate Sales by Application
 - 2.5.1 Global Memory Chip Packaging Substrate Sale Market Share by Application

(2019-2024)

2.5.2 Global Memory Chip Packaging Substrate Revenue and Market Share by Application (2019-2024)

2.5.3 Global Memory Chip Packaging Substrate Sale Price by Application (2019-2024)

3 GLOBAL BY COMPANY

3.1 Global Memory Chip Packaging Substrate Breakdown Data by Company

3.1.1 Global Memory Chip Packaging Substrate Annual Sales by Company (2019-2024)

3.1.2 Global Memory Chip Packaging Substrate Sales Market Share by Company (2019-2024)

3.2 Global Memory Chip Packaging Substrate Annual Revenue by Company (2019-2024)

3.2.1 Global Memory Chip Packaging Substrate Revenue by Company (2019-2024)

3.2.2 Global Memory Chip Packaging Substrate Revenue Market Share by Company (2019-2024)

3.3 Global Memory Chip Packaging Substrate Sale Price by Company

3.4 Key Manufacturers Memory Chip Packaging Substrate Producing Area Distribution, Sales Area, Product Type

3.4.1 Key Manufacturers Memory Chip Packaging Substrate Product Location Distribution

3.4.2 Players Memory Chip Packaging Substrate Products Offered

3.5 Market Concentration Rate Analysis

3.5.1 Competition Landscape Analysis

3.5.2 Concentration Ratio (CR3, CR5 and CR10) & (2019-2024)

3.6 New Products and Potential Entrants

3.7 Market M&A Activity & Strategy

4 WORLD HISTORIC REVIEW FOR MEMORY CHIP PACKAGING SUBSTRATE BY GEOGRAPHIC REGION

4.1 World Historic Memory Chip Packaging Substrate Market Size by Geographic Region (2019-2024)

4.1.1 Global Memory Chip Packaging Substrate Annual Sales by Geographic Region (2019-2024)

4.1.2 Global Memory Chip Packaging Substrate Annual Revenue by Geographic Region (2019-2024)

4.2 World Historic Memory Chip Packaging Substrate Market Size by Country/Region

(2019-2024)

4.2.1 Global Memory Chip Packaging Substrate Annual Sales by Country/Region

(2019-2024)

4.2.2 Global Memory Chip Packaging Substrate Annual Revenue by Country/Region

(2019-2024)

4.3 Americas Memory Chip Packaging Substrate Sales Growth

4.4 APAC Memory Chip Packaging Substrate Sales Growth

4.5 Europe Memory Chip Packaging Substrate Sales Growth

4.6 Middle East & Africa Memory Chip Packaging Substrate Sales Growth

5 AMERICAS

5.1 Americas Memory Chip Packaging Substrate Sales by Country

5.1.1 Americas Memory Chip Packaging Substrate Sales by Country (2019-2024)

5.1.2 Americas Memory Chip Packaging Substrate Revenue by Country (2019-2024)

5.2 Americas Memory Chip Packaging Substrate Sales by Type (2019-2024)

5.3 Americas Memory Chip Packaging Substrate Sales by Application (2019-2024)

5.4 United States

5.5 Canada

5.6 Mexico

5.7 Brazil

6 APAC

6.1 APAC Memory Chip Packaging Substrate Sales by Region

6.1.1 APAC Memory Chip Packaging Substrate Sales by Region (2019-2024)

6.1.2 APAC Memory Chip Packaging Substrate Revenue by Region (2019-2024)

6.2 APAC Memory Chip Packaging Substrate Sales by Type (2019-2024)

6.3 APAC Memory Chip Packaging Substrate Sales by Application (2019-2024)

6.4 China

6.5 Japan

6.6 South Korea

6.7 Southeast Asia

6.8 India

6.9 Australia

6.10 China Taiwan

7 EUROPE

7.1 Europe Memory Chip Packaging Substrate by Country

7.1.1 Europe Memory Chip Packaging Substrate Sales by Country (2019-2024)

7.1.2 Europe Memory Chip Packaging Substrate Revenue by Country (2019-2024)

7.2 Europe Memory Chip Packaging Substrate Sales by Type (2019-2024)

7.3 Europe Memory Chip Packaging Substrate Sales by Application (2019-2024)

7.4 Germany

7.5 France

7.6 UK

7.7 Italy

7.8 Russia

8 MIDDLE EAST & AFRICA

8.1 Middle East & Africa Memory Chip Packaging Substrate by Country

8.1.1 Middle East & Africa Memory Chip Packaging Substrate Sales by Country (2019-2024)

8.1.2 Middle East & Africa Memory Chip Packaging Substrate Revenue by Country (2019-2024)

8.2 Middle East & Africa Memory Chip Packaging Substrate Sales by Type (2019-2024)

8.3 Middle East & Africa Memory Chip Packaging Substrate Sales by Application (2019-2024)

8.4 Egypt

8.5 South Africa

8.6 Israel

8.7 Turkey

8.8 GCC Countries

9 MARKET DRIVERS, CHALLENGES AND TRENDS

9.1 Market Drivers & Growth Opportunities

9.2 Market Challenges & Risks

9.3 Industry Trends

10 MANUFACTURING COST STRUCTURE ANALYSIS

10.1 Raw Material and Suppliers

10.2 Manufacturing Cost Structure Analysis of Memory Chip Packaging Substrate

10.3 Manufacturing Process Analysis of Memory Chip Packaging Substrate

10.4 Industry Chain Structure of Memory Chip Packaging Substrate

11 MARKETING, DISTRIBUTORS AND CUSTOMER

11.1 Sales Channel

11.1.1 Direct Channels

11.1.2 Indirect Channels

11.2 Memory Chip Packaging Substrate Distributors

11.3 Memory Chip Packaging Substrate Customer

12 WORLD FORECAST REVIEW FOR MEMORY CHIP PACKAGING SUBSTRATE BY GEOGRAPHIC REGION

12.1 Global Memory Chip Packaging Substrate Market Size Forecast by Region

12.1.1 Global Memory Chip Packaging Substrate Forecast by Region (2025-2030)

12.1.2 Global Memory Chip Packaging Substrate Annual Revenue Forecast by Region (2025-2030)

12.2 Americas Forecast by Country (2025-2030)

12.3 APAC Forecast by Region (2025-2030)

12.4 Europe Forecast by Country (2025-2030)

12.5 Middle East & Africa Forecast by Country (2025-2030)

12.6 Global Memory Chip Packaging Substrate Forecast by Type (2025-2030)

12.7 Global Memory Chip Packaging Substrate Forecast by Application (2025-2030)

13 KEY PLAYERS ANALYSIS

13.1 Samsung Electro-Mechanics

13.1.1 Samsung Electro-Mechanics Company Information

13.1.2 Samsung Electro-Mechanics Memory Chip Packaging Substrate Product Portfolios and Specifications

13.1.3 Samsung Electro-Mechanics Memory Chip Packaging Substrate Sales, Revenue, Price and Gross Margin (2019-2024)

13.1.4 Samsung Electro-Mechanics Main Business Overview

13.1.5 Samsung Electro-Mechanics Latest Developments

13.2 Simmtech

13.2.1 Simmtech Company Information

13.2.2 Simmtech Memory Chip Packaging Substrate Product Portfolios and Specifications

13.2.3 Simmtech Memory Chip Packaging Substrate Sales, Revenue, Price and Gross Margin (2019-2024)

- 13.2.4 Simmtech Main Business Overview
- 13.2.5 Simmtech Latest Developments
- 13.3 Daeduck
 - 13.3.1 Daeduck Company Information
 - 13.3.2 Daeduck Memory Chip Packaging Substrate Product Portfolios and Specifications
 - 13.3.3 Daeduck Memory Chip Packaging Substrate Sales, Revenue, Price and Gross Margin (2019-2024)
 - 13.3.4 Daeduck Main Business Overview
 - 13.3.5 Daeduck Latest Developments
- 13.4 Korea Circuit
 - 13.4.1 Korea Circuit Company Information
 - 13.4.2 Korea Circuit Memory Chip Packaging Substrate Product Portfolios and Specifications
 - 13.4.3 Korea Circuit Memory Chip Packaging Substrate Sales, Revenue, Price and Gross Margin (2019-2024)
 - 13.4.4 Korea Circuit Main Business Overview
 - 13.4.5 Korea Circuit Latest Developments
- 13.5 Ibiden
 - 13.5.1 Ibiden Company Information
 - 13.5.2 Ibiden Memory Chip Packaging Substrate Product Portfolios and Specifications
 - 13.5.3 Ibiden Memory Chip Packaging Substrate Sales, Revenue, Price and Gross Margin (2019-2024)
 - 13.5.4 Ibiden Main Business Overview
 - 13.5.5 Ibiden Latest Developments
- 13.6 Kyocera
 - 13.6.1 Kyocera Company Information
 - 13.6.2 Kyocera Memory Chip Packaging Substrate Product Portfolios and Specifications
 - 13.6.3 Kyocera Memory Chip Packaging Substrate Sales, Revenue, Price and Gross Margin (2019-2024)
 - 13.6.4 Kyocera Main Business Overview
 - 13.6.5 Kyocera Latest Developments
- 13.7 ASE Group
 - 13.7.1 ASE Group Company Information
 - 13.7.2 ASE Group Memory Chip Packaging Substrate Product Portfolios and Specifications
 - 13.7.3 ASE Group Memory Chip Packaging Substrate Sales, Revenue, Price and Gross Margin (2019-2024)

- 13.7.4 ASE Group Main Business Overview
- 13.7.5 ASE Group Latest Developments
- 13.8 Shinko
 - 13.8.1 Shinko Company Information
 - 13.8.2 Shinko Memory Chip Packaging Substrate Product Portfolios and Specifications
 - 13.8.3 Shinko Memory Chip Packaging Substrate Sales, Revenue, Price and Gross Margin (2019-2024)
 - 13.8.4 Shinko Main Business Overview
 - 13.8.5 Shinko Latest Developments
- 13.9 Fujitsu Global
 - 13.9.1 Fujitsu Global Company Information
 - 13.9.2 Fujitsu Global Memory Chip Packaging Substrate Product Portfolios and Specifications
 - 13.9.3 Fujitsu Global Memory Chip Packaging Substrate Sales, Revenue, Price and Gross Margin (2019-2024)
 - 13.9.4 Fujitsu Global Main Business Overview
 - 13.9.5 Fujitsu Global Latest Developments
- 13.10 Doosan Electronic
 - 13.10.1 Doosan Electronic Company Information
 - 13.10.2 Doosan Electronic Memory Chip Packaging Substrate Product Portfolios and Specifications
 - 13.10.3 Doosan Electronic Memory Chip Packaging Substrate Sales, Revenue, Price and Gross Margin (2019-2024)
 - 13.10.4 Doosan Electronic Main Business Overview
 - 13.10.5 Doosan Electronic Latest Developments
- 13.11 Toppan Printing
 - 13.11.1 Toppan Printing Company Information
 - 13.11.2 Toppan Printing Memory Chip Packaging Substrate Product Portfolios and Specifications
 - 13.11.3 Toppan Printing Memory Chip Packaging Substrate Sales, Revenue, Price and Gross Margin (2019-2024)
 - 13.11.4 Toppan Printing Main Business Overview
 - 13.11.5 Toppan Printing Latest Developments
- 13.12 Unimicron
 - 13.12.1 Unimicron Company Information
 - 13.12.2 Unimicron Memory Chip Packaging Substrate Product Portfolios and Specifications
 - 13.12.3 Unimicron Memory Chip Packaging Substrate Sales, Revenue, Price and Gross Margin (2019-2024)

- 13.12.4 Unimicron Main Business Overview
- 13.12.5 Unimicron Latest Developments
- 13.13 Kinsus
 - 13.13.1 Kinsus Company Information
 - 13.13.2 Kinsus Memory Chip Packaging Substrate Product Portfolios and Specifications
 - 13.13.3 Kinsus Memory Chip Packaging Substrate Sales, Revenue, Price and Gross Margin (2019-2024)
 - 13.13.4 Kinsus Main Business Overview
 - 13.13.5 Kinsus Latest Developments
- 13.14 Nanya
 - 13.14.1 Nanya Company Information
 - 13.14.2 Nanya Memory Chip Packaging Substrate Product Portfolios and Specifications
 - 13.14.3 Nanya Memory Chip Packaging Substrate Sales, Revenue, Price and Gross Margin (2019-2024)
 - 13.14.4 Nanya Main Business Overview
 - 13.14.5 Nanya Latest Developments
- 13.15 Fastprint Circuit
 - 13.15.1 Fastprint Circuit Company Information
 - 13.15.2 Fastprint Circuit Memory Chip Packaging Substrate Product Portfolios and Specifications
 - 13.15.3 Fastprint Circuit Memory Chip Packaging Substrate Sales, Revenue, Price and Gross Margin (2019-2024)
 - 13.15.4 Fastprint Circuit Main Business Overview
 - 13.15.5 Fastprint Circuit Latest Developments
- 13.16 Shennan Circuits
 - 13.16.1 Shennan Circuits Company Information
 - 13.16.2 Shennan Circuits Memory Chip Packaging Substrate Product Portfolios and Specifications
 - 13.16.3 Shennan Circuits Memory Chip Packaging Substrate Sales, Revenue, Price and Gross Margin (2019-2024)
 - 13.16.4 Shennan Circuits Main Business Overview
 - 13.16.5 Shennan Circuits Latest Developments

14 RESEARCH FINDINGS AND CONCLUSION

List Of Tables

LIST OF TABLES

Table 1. Memory Chip Packaging Substrate Annual Sales CAGR by Geographic Region (2019, 2023 & 2030) & (\$ millions)

Table 2. Memory Chip Packaging Substrate Annual Sales CAGR by Country/Region (2019, 2023 & 2030) & (\$ millions)

Table 3. Major Players of WB BGA

Table 4. Major Players of WB-CSP

Table 5. Major Players of Others

Table 6. Global Memory Chip Packaging Substrate Sales by Type (2019-2024) & (K Sqm)

Table 7. Global Memory Chip Packaging Substrate Sales Market Share by Type (2019-2024)

Table 8. Global Memory Chip Packaging Substrate Revenue by Type (2019-2024) & (\$ million)

Table 9. Global Memory Chip Packaging Substrate Revenue Market Share by Type (2019-2024)

Table 10. Global Memory Chip Packaging Substrate Sale Price by Type (2019-2024) & (US\$/Sqm)

Table 11. Global Memory Chip Packaging Substrate Sale by Application (2019-2024) & (K Sqm)

Table 12. Global Memory Chip Packaging Substrate Sale Market Share by Application (2019-2024)

Table 13. Global Memory Chip Packaging Substrate Revenue by Application (2019-2024) & (\$ million)

Table 14. Global Memory Chip Packaging Substrate Revenue Market Share by Application (2019-2024)

Table 15. Global Memory Chip Packaging Substrate Sale Price by Application (2019-2024) & (US\$/Sqm)

Table 16. Global Memory Chip Packaging Substrate Sales by Company (2019-2024) & (K Sqm)

Table 17. Global Memory Chip Packaging Substrate Sales Market Share by Company (2019-2024)

Table 18. Global Memory Chip Packaging Substrate Revenue by Company (2019-2024) & (\$ millions)

Table 19. Global Memory Chip Packaging Substrate Revenue Market Share by Company (2019-2024)

- Table 20. Global Memory Chip Packaging Substrate Sale Price by Company (2019-2024) & (US\$/Sqm)
- Table 21. Key Manufacturers Memory Chip Packaging Substrate Producing Area Distribution and Sales Area
- Table 22. Players Memory Chip Packaging Substrate Products Offered
- Table 23. Memory Chip Packaging Substrate Concentration Ratio (CR3, CR5 and CR10) & (2019-2024)
- Table 24. New Products and Potential Entrants
- Table 25. Market M&A Activity & Strategy
- Table 26. Global Memory Chip Packaging Substrate Sales by Geographic Region (2019-2024) & (K Sqm)
- Table 27. Global Memory Chip Packaging Substrate Sales Market Share Geographic Region (2019-2024)
- Table 28. Global Memory Chip Packaging Substrate Revenue by Geographic Region (2019-2024) & (\$ millions)
- Table 29. Global Memory Chip Packaging Substrate Revenue Market Share by Geographic Region (2019-2024)
- Table 30. Global Memory Chip Packaging Substrate Sales by Country/Region (2019-2024) & (K Sqm)
- Table 31. Global Memory Chip Packaging Substrate Sales Market Share by Country/Region (2019-2024)
- Table 32. Global Memory Chip Packaging Substrate Revenue by Country/Region (2019-2024) & (\$ millions)
- Table 33. Global Memory Chip Packaging Substrate Revenue Market Share by Country/Region (2019-2024)
- Table 34. Americas Memory Chip Packaging Substrate Sales by Country (2019-2024) & (K Sqm)
- Table 35. Americas Memory Chip Packaging Substrate Sales Market Share by Country (2019-2024)
- Table 36. Americas Memory Chip Packaging Substrate Revenue by Country (2019-2024) & (\$ millions)
- Table 37. Americas Memory Chip Packaging Substrate Sales by Type (2019-2024) & (K Sqm)
- Table 38. Americas Memory Chip Packaging Substrate Sales by Application (2019-2024) & (K Sqm)
- Table 39. APAC Memory Chip Packaging Substrate Sales by Region (2019-2024) & (K Sqm)
- Table 40. APAC Memory Chip Packaging Substrate Sales Market Share by Region (2019-2024)

- Table 41. APAC Memory Chip Packaging Substrate Revenue by Region (2019-2024) & (\$ millions)
- Table 42. APAC Memory Chip Packaging Substrate Sales by Type (2019-2024) & (K Sqm)
- Table 43. APAC Memory Chip Packaging Substrate Sales by Application (2019-2024) & (K Sqm)
- Table 44. Europe Memory Chip Packaging Substrate Sales by Country (2019-2024) & (K Sqm)
- Table 45. Europe Memory Chip Packaging Substrate Revenue by Country (2019-2024) & (\$ millions)
- Table 46. Europe Memory Chip Packaging Substrate Sales by Type (2019-2024) & (K Sqm)
- Table 47. Europe Memory Chip Packaging Substrate Sales by Application (2019-2024) & (K Sqm)
- Table 48. Middle East & Africa Memory Chip Packaging Substrate Sales by Country (2019-2024) & (K Sqm)
- Table 49. Middle East & Africa Memory Chip Packaging Substrate Revenue Market Share by Country (2019-2024)
- Table 50. Middle East & Africa Memory Chip Packaging Substrate Sales by Type (2019-2024) & (K Sqm)
- Table 51. Middle East & Africa Memory Chip Packaging Substrate Sales by Application (2019-2024) & (K Sqm)
- Table 52. Key Market Drivers & Growth Opportunities of Memory Chip Packaging Substrate
- Table 53. Key Market Challenges & Risks of Memory Chip Packaging Substrate
- Table 54. Key Industry Trends of Memory Chip Packaging Substrate
- Table 55. Memory Chip Packaging Substrate Raw Material
- Table 56. Key Suppliers of Raw Materials
- Table 57. Memory Chip Packaging Substrate Distributors List
- Table 58. Memory Chip Packaging Substrate Customer List
- Table 59. Global Memory Chip Packaging Substrate Sales Forecast by Region (2025-2030) & (K Sqm)
- Table 60. Global Memory Chip Packaging Substrate Revenue Forecast by Region (2025-2030) & (\$ millions)
- Table 61. Americas Memory Chip Packaging Substrate Sales Forecast by Country (2025-2030) & (K Sqm)
- Table 62. Americas Memory Chip Packaging Substrate Annual Revenue Forecast by Country (2025-2030) & (\$ millions)
- Table 63. APAC Memory Chip Packaging Substrate Sales Forecast by Region

(2025-2030) & (K Sqm)

Table 64. APAC Memory Chip Packaging Substrate Annual Revenue Forecast by Region (2025-2030) & (\$ millions)

Table 65. Europe Memory Chip Packaging Substrate Sales Forecast by Country (2025-2030) & (K Sqm)

Table 66. Europe Memory Chip Packaging Substrate Revenue Forecast by Country (2025-2030) & (\$ millions)

Table 67. Middle East & Africa Memory Chip Packaging Substrate Sales Forecast by Country (2025-2030) & (K Sqm)

Table 68. Middle East & Africa Memory Chip Packaging Substrate Revenue Forecast by Country (2025-2030) & (\$ millions)

Table 69. Global Memory Chip Packaging Substrate Sales Forecast by Type (2025-2030) & (K Sqm)

Table 70. Global Memory Chip Packaging Substrate Revenue Forecast by Type (2025-2030) & (\$ millions)

Table 71. Global Memory Chip Packaging Substrate Sales Forecast by Application (2025-2030) & (K Sqm)

Table 72. Global Memory Chip Packaging Substrate Revenue Forecast by Application (2025-2030) & (\$ millions)

Table 73. Samsung Electro-Mechanics Basic Information, Memory Chip Packaging Substrate Manufacturing Base, Sales Area and Its Competitors

Table 74. Samsung Electro-Mechanics Memory Chip Packaging Substrate Product Portfolios and Specifications

Table 75. Samsung Electro-Mechanics Memory Chip Packaging Substrate Sales (K Sqm), Revenue (\$ Million), Price (US\$/Sqm) and Gross Margin (2019-2024)

Table 76. Samsung Electro-Mechanics Main Business

Table 77. Samsung Electro-Mechanics Latest Developments

Table 78. Simmtech Basic Information, Memory Chip Packaging Substrate Manufacturing Base, Sales Area and Its Competitors

Table 79. Simmtech Memory Chip Packaging Substrate Product Portfolios and Specifications

Table 80. Simmtech Memory Chip Packaging Substrate Sales (K Sqm), Revenue (\$ Million), Price (US\$/Sqm) and Gross Margin (2019-2024)

Table 81. Simmtech Main Business

Table 82. Simmtech Latest Developments

Table 83. Daeduck Basic Information, Memory Chip Packaging Substrate Manufacturing Base, Sales Area and Its Competitors

Table 84. Daeduck Memory Chip Packaging Substrate Product Portfolios and Specifications

Table 85. Daeduck Memory Chip Packaging Substrate Sales (K Sqm), Revenue (\$ Million), Price (US\$/Sqm) and Gross Margin (2019-2024)

Table 86. Daeduck Main Business

Table 87. Daeduck Latest Developments

Table 88. Korea Circuit Basic Information, Memory Chip Packaging Substrate Manufacturing Base, Sales Area and Its Competitors

Table 89. Korea Circuit Memory Chip Packaging Substrate Product Portfolios and Specifications

Table 90. Korea Circuit Memory Chip Packaging Substrate Sales (K Sqm), Revenue (\$ Million), Price (US\$/Sqm) and Gross Margin (2019-2024)

Table 91. Korea Circuit Main Business

Table 92. Korea Circuit Latest Developments

Table 93. Ibiden Basic Information, Memory Chip Packaging Substrate Manufacturing Base, Sales Area and Its Competitors

Table 94. Ibiden Memory Chip Packaging Substrate Product Portfolios and Specifications

Table 95. Ibiden Memory Chip Packaging Substrate Sales (K Sqm), Revenue (\$ Million), Price (US\$/Sqm) and Gross Margin (2019-2024)

Table 96. Ibiden Main Business

Table 97. Ibiden Latest Developments

Table 98. Kyocera Basic Information, Memory Chip Packaging Substrate Manufacturing Base, Sales Area and Its Competitors

Table 99. Kyocera Memory Chip Packaging Substrate Product Portfolios and Specifications

Table 100. Kyocera Memory Chip Packaging Substrate Sales (K Sqm), Revenue (\$ Million), Price (US\$/Sqm) and Gross Margin (2019-2024)

Table 101. Kyocera Main Business

Table 102. Kyocera Latest Developments

Table 103. ASE Group Basic Information, Memory Chip Packaging Substrate Manufacturing Base, Sales Area and Its Competitors

Table 104. ASE Group Memory Chip Packaging Substrate Product Portfolios and Specifications

Table 105. ASE Group Memory Chip Packaging Substrate Sales (K Sqm), Revenue (\$ Million), Price (US\$/Sqm) and Gross Margin (2019-2024)

Table 106. ASE Group Main Business

Table 107. ASE Group Latest Developments

Table 108. Shinko Basic Information, Memory Chip Packaging Substrate Manufacturing Base, Sales Area and Its Competitors

Table 109. Shinko Memory Chip Packaging Substrate Product Portfolios and

Specifications

Table 110. Shinko Memory Chip Packaging Substrate Sales (K Sqm), Revenue (\$ Million), Price (US\$/Sqm) and Gross Margin (2019-2024)

Table 111. Shinko Main Business

Table 112. Shinko Latest Developments

Table 113. Fujitsu Global Basic Information, Memory Chip Packaging Substrate Manufacturing Base, Sales Area and Its Competitors

Table 114. Fujitsu Global Memory Chip Packaging Substrate Product Portfolios and Specifications

Table 115. Fujitsu Global Memory Chip Packaging Substrate Sales (K Sqm), Revenue (\$ Million), Price (US\$/Sqm) and Gross Margin (2019-2024)

Table 116. Fujitsu Global Main Business

Table 117. Fujitsu Global Latest Developments

Table 118. Doosan Electronic Basic Information, Memory Chip Packaging Substrate Manufacturing Base, Sales Area and Its Competitors

Table 119. Doosan Electronic Memory Chip Packaging Substrate Product Portfolios and Specifications

Table 120. Doosan Electronic Memory Chip Packaging Substrate Sales (K Sqm), Revenue (\$ Million), Price (US\$/Sqm) and Gross Margin (2019-2024)

Table 121. Doosan Electronic Main Business

Table 122. Doosan Electronic Latest Developments

Table 123. Toppan Printing Basic Information, Memory Chip Packaging Substrate Manufacturing Base, Sales Area and Its Competitors

Table 124. Toppan Printing Memory Chip Packaging Substrate Product Portfolios and Specifications

Table 125. Toppan Printing Memory Chip Packaging Substrate Sales (K Sqm), Revenue (\$ Million), Price (US\$/Sqm) and Gross Margin (2019-2024)

Table 126. Toppan Printing Main Business

Table 127. Toppan Printing Latest Developments

Table 128. Unimicron Basic Information, Memory Chip Packaging Substrate Manufacturing Base, Sales Area and Its Competitors

Table 129. Unimicron Memory Chip Packaging Substrate Product Portfolios and Specifications

Table 130. Unimicron Memory Chip Packaging Substrate Sales (K Sqm), Revenue (\$ Million), Price (US\$/Sqm) and Gross Margin (2019-2024)

Table 131. Unimicron Main Business

Table 132. Unimicron Latest Developments

Table 133. Kinsus Basic Information, Memory Chip Packaging Substrate Manufacturing Base, Sales Area and Its Competitors

Table 134. Kinsus Memory Chip Packaging Substrate Product Portfolios and Specifications

Table 135. Kinsus Memory Chip Packaging Substrate Sales (K Sqm), Revenue (\$ Million), Price (US\$/Sqm) and Gross Margin (2019-2024)

Table 136. Kinsus Main Business

Table 137. Kinsus Latest Developments

Table 138. Nanya Basic Information, Memory Chip Packaging Substrate Manufacturing Base, Sales Area and Its Competitors

Table 139. Nanya Memory Chip Packaging Substrate Product Portfolios and Specifications

Table 140. Nanya Memory Chip Packaging Substrate Sales (K Sqm), Revenue (\$ Million), Price (US\$/Sqm) and Gross Margin (2019-2024)

Table 141. Nanya Main Business

Table 142. Nanya Latest Developments

Table 143. Fastprint Circuit Basic Information, Memory Chip Packaging Substrate Manufacturing Base, Sales Area and Its Competitors

Table 144. Fastprint Circuit Memory Chip Packaging Substrate Product Portfolios and Specifications

Table 145. Fastprint Circuit Memory Chip Packaging Substrate Sales (K Sqm), Revenue (\$ Million), Price (US\$/Sqm) and Gross Margin (2019-2024)

Table 146. Fastprint Circuit Main Business

Table 147. Fastprint Circuit Latest Developments

Table 148. Shennan Circuits Basic Information, Memory Chip Packaging Substrate Manufacturing Base, Sales Area and Its Competitors

Table 149. Shennan Circuits Memory Chip Packaging Substrate Product Portfolios and Specifications

Table 150. Shennan Circuits Memory Chip Packaging Substrate Sales (K Sqm), Revenue (\$ Million), Price (US\$/Sqm) and Gross Margin (2019-2024)

Table 151. Shennan Circuits Main Business

Table 152. Shennan Circuits Latest Developments

List Of Figures

LIST OF FIGURES

- Figure 1. Picture of Memory Chip Packaging Substrate
- Figure 2. Memory Chip Packaging Substrate Report Years Considered
- Figure 3. Research Objectives
- Figure 4. Research Methodology
- Figure 5. Research Process and Data Source
- Figure 6. Global Memory Chip Packaging Substrate Sales Growth Rate 2019-2030 (K Sqm)
- Figure 7. Global Memory Chip Packaging Substrate Revenue Growth Rate 2019-2030 (\$ millions)
- Figure 8. Memory Chip Packaging Substrate Sales by Geographic Region (2019, 2023 & 2030) & (\$ millions)
- Figure 9. Memory Chip Packaging Substrate Sales Market Share by Country/Region (2023)
- Figure 10. Memory Chip Packaging Substrate Sales Market Share by Country/Region (2019, 2023 & 2030)
- Figure 11. Product Picture of WB BGA
- Figure 12. Product Picture of WB-CSP
- Figure 13. Product Picture of Others
- Figure 14. Global Memory Chip Packaging Substrate Sales Market Share by Type in 2023
- Figure 15. Global Memory Chip Packaging Substrate Revenue Market Share by Type (2019-2024)
- Figure 16. Memory Chip Packaging Substrate Consumed in Flash Drive
- Figure 17. Global Memory Chip Packaging Substrate Market: Flash Drive (2019-2024) & (K Sqm)
- Figure 18. Memory Chip Packaging Substrate Consumed in Solid State Storage
- Figure 19. Global Memory Chip Packaging Substrate Market: Solid State Storage (2019-2024) & (K Sqm)
- Figure 20. Memory Chip Packaging Substrate Consumed in Embedded Storage
- Figure 21. Global Memory Chip Packaging Substrate Market: Embedded Storage (2019-2024) & (K Sqm)
- Figure 22. Memory Chip Packaging Substrate Consumed in Volatile Storage
- Figure 23. Global Memory Chip Packaging Substrate Market: Volatile Storage (2019-2024) & (K Sqm)
- Figure 24. Memory Chip Packaging Substrate Consumed in Others

Figure 25. Global Memory Chip Packaging Substrate Market: Others (2019-2024) & (K Sqm)

Figure 26. Global Memory Chip Packaging Substrate Sale Market Share by Application (2023)

Figure 27. Global Memory Chip Packaging Substrate Revenue Market Share by Application in 2023

Figure 28. Memory Chip Packaging Substrate Sales by Company in 2023 (K Sqm)

Figure 29. Global Memory Chip Packaging Substrate Sales Market Share by Company in 2023

Figure 30. Memory Chip Packaging Substrate Revenue by Company in 2023 (\$ millions)

Figure 31. Global Memory Chip Packaging Substrate Revenue Market Share by Company in 2023

Figure 32. Global Memory Chip Packaging Substrate Sales Market Share by Geographic Region (2019-2024)

Figure 33. Global Memory Chip Packaging Substrate Revenue Market Share by Geographic Region in 2023

Figure 34. Americas Memory Chip Packaging Substrate Sales 2019-2024 (K Sqm)

Figure 35. Americas Memory Chip Packaging Substrate Revenue 2019-2024 (\$ millions)

Figure 36. APAC Memory Chip Packaging Substrate Sales 2019-2024 (K Sqm)

Figure 37. APAC Memory Chip Packaging Substrate Revenue 2019-2024 (\$ millions)

Figure 38. Europe Memory Chip Packaging Substrate Sales 2019-2024 (K Sqm)

Figure 39. Europe Memory Chip Packaging Substrate Revenue 2019-2024 (\$ millions)

Figure 40. Middle East & Africa Memory Chip Packaging Substrate Sales 2019-2024 (K Sqm)

Figure 41. Middle East & Africa Memory Chip Packaging Substrate Revenue 2019-2024 (\$ millions)

Figure 42. Americas Memory Chip Packaging Substrate Sales Market Share by Country in 2023

Figure 43. Americas Memory Chip Packaging Substrate Revenue Market Share by Country (2019-2024)

Figure 44. Americas Memory Chip Packaging Substrate Sales Market Share by Type (2019-2024)

Figure 45. Americas Memory Chip Packaging Substrate Sales Market Share by Application (2019-2024)

Figure 46. United States Memory Chip Packaging Substrate Revenue Growth 2019-2024 (\$ millions)

Figure 47. Canada Memory Chip Packaging Substrate Revenue Growth 2019-2024 (\$

millions)

Figure 48. Mexico Memory Chip Packaging Substrate Revenue Growth 2019-2024 (\$ millions)

Figure 49. Brazil Memory Chip Packaging Substrate Revenue Growth 2019-2024 (\$ millions)

Figure 50. APAC Memory Chip Packaging Substrate Sales Market Share by Region in 2023

Figure 51. APAC Memory Chip Packaging Substrate Revenue Market Share by Region (2019-2024)

Figure 52. APAC Memory Chip Packaging Substrate Sales Market Share by Type (2019-2024)

Figure 53. APAC Memory Chip Packaging Substrate Sales Market Share by Application (2019-2024)

Figure 54. China Memory Chip Packaging Substrate Revenue Growth 2019-2024 (\$ millions)

Figure 55. Japan Memory Chip Packaging Substrate Revenue Growth 2019-2024 (\$ millions)

Figure 56. South Korea Memory Chip Packaging Substrate Revenue Growth 2019-2024 (\$ millions)

Figure 57. Southeast Asia Memory Chip Packaging Substrate Revenue Growth 2019-2024 (\$ millions)

Figure 58. India Memory Chip Packaging Substrate Revenue Growth 2019-2024 (\$ millions)

Figure 59. Australia Memory Chip Packaging Substrate Revenue Growth 2019-2024 (\$ millions)

Figure 60. China Taiwan Memory Chip Packaging Substrate Revenue Growth 2019-2024 (\$ millions)

Figure 61. Europe Memory Chip Packaging Substrate Sales Market Share by Country in 2023

Figure 62. Europe Memory Chip Packaging Substrate Revenue Market Share by Country (2019-2024)

Figure 63. Europe Memory Chip Packaging Substrate Sales Market Share by Type (2019-2024)

Figure 64. Europe Memory Chip Packaging Substrate Sales Market Share by Application (2019-2024)

Figure 65. Germany Memory Chip Packaging Substrate Revenue Growth 2019-2024 (\$ millions)

Figure 66. France Memory Chip Packaging Substrate Revenue Growth 2019-2024 (\$ millions)

Figure 67. UK Memory Chip Packaging Substrate Revenue Growth 2019-2024 (\$ millions)

Figure 68. Italy Memory Chip Packaging Substrate Revenue Growth 2019-2024 (\$ millions)

Figure 69. Russia Memory Chip Packaging Substrate Revenue Growth 2019-2024 (\$ millions)

Figure 70. Middle East & Africa Memory Chip Packaging Substrate Sales Market Share by Country (2019-2024)

Figure 71. Middle East & Africa Memory Chip Packaging Substrate Sales Market Share by Type (2019-2024)

Figure 72. Middle East & Africa Memory Chip Packaging Substrate Sales Market Share by Application (2019-2024)

Figure 73. Egypt Memory Chip Packaging Substrate Revenue Growth 2019-2024 (\$ millions)

Figure 74. South Africa Memory Chip Packaging Substrate Revenue Growth 2019-2024 (\$ millions)

Figure 75. Israel Memory Chip Packaging Substrate Revenue Growth 2019-2024 (\$ millions)

Figure 76. Turkey Memory Chip Packaging Substrate Revenue Growth 2019-2024 (\$ millions)

Figure 77. GCC Countries Memory Chip Packaging Substrate Revenue Growth 2019-2024 (\$ millions)

Figure 78. Manufacturing Cost Structure Analysis of Memory Chip Packaging Substrate in 2023

Figure 79. Manufacturing Process Analysis of Memory Chip Packaging Substrate

Figure 80. Industry Chain Structure of Memory Chip Packaging Substrate

Figure 81. Channels of Distribution

Figure 82. Global Memory Chip Packaging Substrate Sales Market Forecast by Region (2025-2030)

Figure 83. Global Memory Chip Packaging Substrate Revenue Market Share Forecast by Region (2025-2030)

Figure 84. Global Memory Chip Packaging Substrate Sales Market Share Forecast by Type (2025-2030)

Figure 85. Global Memory Chip Packaging Substrate Revenue Market Share Forecast by Type (2025-2030)

Figure 86. Global Memory Chip Packaging Substrate Sales Market Share Forecast by Application (2025-2030)

Figure 87. Global Memory Chip Packaging Substrate Revenue Market Share Forecast by Application (2025-2030)

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