

Global Medical Ultrasound Imaging ASIC Market Growth 2026-2032

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Abstracts

The global Medical Ultrasound Imaging ASIC market size is predicted to grow from US\$ 566 million in 2025 to US\$ 1029 million in 2032; it is expected to grow at a CAGR of 9.0% from 2026 to 2032.

Medical Ultrasound Imaging ASIC refers to application-specific integrated circuits and highly integrated semiconductor devices designed for the signal generation, acquisition, conversion, switching, beamforming, processing and control functions of medical ultrasound imaging systems. The research scope focuses on packaged chips, bare dies, system-in-package devices and customized ASIC solutions used in ultrasound consoles, compact systems, handheld scanners, catheter-based devices, wearable platforms and intelligent probes. Typical product forms include high-voltage transmitter and pulser ASICs, receive analog front ends, transmit/receive switches, high-voltage multiplexers, beamforming processors, signal-control ASICs and integrated ultrasound SoCs. Key technical parameters include channel count, input noise, dynamic range, gain range, sampling performance, transmit voltage, switching speed, power consumption, package dimensions and compatibility with piezoelectric, CMUT or pMUT transducers. These devices convert low-voltage digital control signals into precise high-voltage excitation pulses, amplify and digitize weak echo signals, select transducer elements, coordinate multi-channel timing and support real-time image formation. The study applies a narrow professional scope centered on semiconductor products specifically designed or customized for medical ultrasound imaging applications.

Key Findings

The validated core formal list contains 11 parent-level Medical Ultrasound Imaging ASIC suppliers

Six companies provide standardized commercial ultrasound ICs, two provide custom ASICs and three operate proprietary internal chip platforms

Commercial product coverage centers on receive AFEs, high-voltage transmitters, T/R switches, multiplexers and integrated ultrasound SoCs

Handheld, catheter-based and matrix-probe systems represent the clearest incremental opportunities for higher semiconductor integration

Supply remains concentrated while technology routes diversify across piezoelectric, CMUT and pMUT transducer architectures

Market Trends

The development of Medical Ultrasound Imaging ASIC is moving from discrete signal-chain components toward higher functional integration, lower power consumption and closer coupling between the semiconductor and the transducer. In conventional equipment, improvements continue to focus on lower-noise receive AFEs, higher-density channels, more precise high-voltage transmission, faster switching and reduced board area. In compact and handheld systems, product design increasingly combines transmit, receive, conversion, control and partial beamforming functions within fewer devices, reducing power, size and interconnect complexity. Probe-level integration is also becoming more important as matrix transducers, catheter-based probes and high-element-count arrays require local multiplexing, signal conditioning and data reduction. At the same time, CMUT-on-CMOS and pMUT-based architectures are expanding the role of semiconductor manufacturing from signal processing into acoustic transduction, enabling wafer-level production, broader frequency coverage and software-configurable imaging functions. The long-term direction is therefore not simply greater channel count, but a more integrated, power-efficient and application-specific imaging architecture that combines transducer technology, mixed-signal circuitry, digital processing and system software.

Market Dynamics

Drivers

Demand for Medical Ultrasound Imaging ASIC is supported by the continued expansion of real-time, non-ionizing diagnostic imaging and by the migration of ultrasound from

centralized imaging departments toward point-of-care and portable use. Higher integration allows equipment developers to reduce circuit-board area, power consumption, probe weight and system cost while maintaining image quality and multi-mode functionality. Growth in channel density, Doppler capability, volumetric imaging and intelligent image acquisition also increases the semiconductor content required in each system. For handheld and compact platforms, dedicated AFEs, high-voltage pulsers, switching devices and integrated transceivers are especially important because conventional discrete architectures are difficult to accommodate within strict thermal and size constraints. Demand is further reinforced by equipment manufacturers seeking longer product availability, stable supply, reusable semiconductor platforms and faster development cycles across multiple ultrasound product families.

Restraints

Market expansion is constrained by high development expenditure, relatively long design-in cycles and the limited production scale of many customized medical ASIC programs. Medical ultrasound chips combine low-noise analog acquisition, high-voltage transmission, fast switching, precise timing and digital processing, creating complex trade-offs among image quality, power, die area, package performance and cost. Customized devices may require substantial non-recurring engineering expenditure, dedicated masks, specialized analog or high-voltage processes and application-specific verification before commercial production. Semiconductor qualification must also align with the longer lifecycle, traceability and reliability expectations of medical equipment programs. In addition, specialized chip demand is concentrated among a limited number of ultrasound system and probe manufacturers, which can restrict volume scalability and increase dependence on individual design wins. These factors make economic production more difficult for highly customized, low-volume products than for standardized commercial components.

Opportunities

The most attractive opportunities arise where conventional board-level electronics can be transferred closer to the probe or consolidated into a customized mixed-signal device. High-element-count matrix probes require compact multiplexing, local signal conditioning and efficient data transmission, while catheter-based ultrasound, intracardiac echocardiography and intravascular ultrasound require extremely small packages and low-power front ends. Handheld systems create demand for integrated transceivers, multi-function AFEs and semiconductor-based probes capable of supporting several imaging modes with one device. Wearable and patch ultrasound

may create a further opportunity for ultra-low-power ASICs, although commercial volumes remain less established. CMUT- and pMUT-compatible ASICs also provide opportunities for co-designed transducer and electronics platforms, allowing wafer-level integration and differentiated probe architectures. Suppliers with reusable medical analog IP, high-voltage process capability and experience managing customized silicon programs are positioned to address these emerging applications.

Challenges

The industry faces persistent challenges in balancing imaging performance, power efficiency, package size, product longevity and commercial scale. Ultrasound front ends must process weak echo signals in close proximity to high-voltage transmit pulses, making isolation, recovery time, noise suppression and thermal management critical. The absence of a single standardized architecture across cart-based systems, handheld devices, matrix probes and catheter platforms limits the reuse of some customized designs. New transducer technologies also require close co-development among semiconductor designers, probe manufacturers, system developers and imaging-software teams, increasing project coordination risk. Commercial success depends on securing long-duration design wins, achieving stable manufacturing yield and maintaining supply continuity over medical equipment lifecycles. Regulatory changes at the system or transducer level can additionally require design documentation and verification updates even when the ASIC itself is not marketed as a standalone medical device.

Industry Chain Analysis

The upstream industry chain comprises semiconductor IP, EDA tools, analog and high-voltage process technologies, silicon wafers, specialty foundry capacity, packaging substrates, wafer-level packaging and semiconductor testing. Medical ultrasound applications place particular value on low-noise analog processes, high-voltage device integration, accurate matching, high-density interconnect and packages capable of controlling electrical parasitics. Depending on the supplier model, wafer fabrication may be completed internally by an IDM or outsourced by a fabless and custom ASIC provider, while assembly and testing are frequently performed through specialized external partners. The need to combine high-voltage transmit circuits with sensitive receive electronics means that process selection and package architecture have a direct influence on performance, yield and total device cost.

The midstream consists of standardized medical semiconductor suppliers, custom ASIC

design-and-supply companies and ultrasound system or probe manufacturers with proprietary internal chip platforms. Value is created through ultrasound-specific architecture, reusable circuit IP, transducer-electronics co-design, mixed-signal verification, manufacturing control and long-term supply support. Downstream integration occurs in cart-based systems, compact scanners, handheld devices, intelligent probes, matrix arrays and catheter-based imaging platforms. Development cost is concentrated in engineering labor, IP, masks, validation and qualification, while unit cost is determined by die size, process node, wafer yield, package complexity and testing requirements. Profitability is generally stronger when a design platform can be reused across multiple customers or product generations, whereas one-off low-volume programs face greater pressure to recover non-recurring engineering investment.

Segment Insights

By product type, the most established commercial segments are receive analog front ends and high-voltage transmit-and-switch devices. Receive AFEs integrate low-noise amplification, variable gain, filtering and analog-to-digital conversion, while the transmit chain contains pulsers, transmit/receive switches and high-voltage multiplexers. These categories remain central to conventional cart-based, compact and portable ultrasound architectures. Beamforming ASICs and highly integrated ultrasound SoCs form a more customized segment because their architecture is closely linked to system software, channel configuration and proprietary imaging algorithms. Their unit volumes may be less transparent, but their semiconductor value per design is generally higher because they replace multiple discrete functions and reduce downstream system complexity.

By integration level, single-function ASICs retain an important role where equipment manufacturers require design flexibility, while multi-function front ends and highly integrated SoCs are gaining relevance in power- and space-constrained equipment. By transducer compatibility, piezoelectric-compatible chips continue to support the broad installed base, whereas CMUT- and pMUT-compatible ASICs represent the more differentiated technology opportunity. By channel count, higher-density solutions benefit matrix and advanced imaging platforms, while lower- and medium-channel devices are better aligned with compact, catheter-based and multiplexed systems. The highest-value opportunities are therefore associated with products that simultaneously improve integration, channel density, power efficiency and transducer compatibility rather than with channel expansion alone.

Downstream Market Opportunities

Downstream opportunities are increasingly determined by equipment form factor and probe architecture. Cart-based ultrasound remains an important source of demand for high-performance, high-channel-count AFEs and advanced transmit electronics, while compact systems require similar imaging capability within tighter power and board-space limits. Handheld ultrasound represents a visible incremental opportunity because semiconductor integration can replace multiple probes or circuit boards with a smaller, software-configurable platform. Matrix probes create demand for local multiplexing and probe-level electronics, and catheter-based applications require miniaturized high-voltage and low-noise circuits positioned close to the transducer. Wearable and home-oriented ultrasound could extend the addressable application base over the longer term, but opportunities will depend on clinical validation, workflow integration and regulatory acceptance rather than semiconductor capability alone.

Regional Insights

From a supply-side perspective, North America forms the densest cluster within the validated core formal list, supported by established analog semiconductor companies and emerging semiconductor-based ultrasound platforms. The region combines standardized medical AFEs, high-voltage devices, multiplexers and proprietary probe-chip architectures. Europe has a smaller but diversified supplier base covering standard ultrasound ICs, custom medical ASIC services and internally developed matrix-probe technologies. Japan maintains a specialized position in high-voltage transmit and receive solutions, strengthened by the transfer and integration of medical ultrasound semiconductor assets.

Asia also plays a broader role in semiconductor fabrication, packaging, system manufacturing and ultrasound equipment production. China is developing capabilities in customized ultrasound IC design and silicon-based transducer integration, although the number of independently verified commercial suppliers remains below that of North America. Regional opportunity therefore differs by function: North America and Europe are stronger in product definition, analog IP and proprietary platforms; Japan retains high-voltage and medical semiconductor specialization; and the wider Asian supply chain provides manufacturing scale, packaging capability and proximity to ultrasound equipment production. These roles are complementary because chip design headquarters, wafer fabrication, packaging and final system integration are frequently located in different countries.

Competitive Landscape Analysis

The Medical Ultrasound Imaging ASIC market has a concentrated but multilayered competitive structure. Standardized commercial IC supply is concentrated among established analog semiconductor groups with proven capabilities in low-noise acquisition, high-voltage transmission, switching, data conversion and long-term product support. Within the validated manufacturer pool, Texas Instruments, Analog Devices, STMicroelectronics, MinebeaMitsumi, Microchip Technology and Monolithic Power Systems participate through commercially available ultrasound-focused product portfolios, although their functional coverage differs. A second competitive layer consists of custom ASIC providers such as the SEALSQ-controlled IC'Alps business and Sonosilicon, which compete through application-specific integration, reusable medical IP and close cooperation with probe or equipment developers. A third layer comprises Philips, Butterfly Network and Exo Imaging, where proprietary semiconductor platforms primarily support differentiated probes and imaging systems rather than open-market component sales. Competition therefore does not depend solely on chip price or corporate scale. The principal barriers are ultrasound-specific analog performance, high-voltage process capability, system-level design knowledge, manufacturing reliability, medical product lifecycle support and established customer design-ins. Recent transactions, including MinebeaMitsumi's integration of Socionext's medical ultrasound business and SEALSQ's acquisition of IC'Alps, indicate that companies are strengthening portfolios through complementary receive technology, custom ASIC expertise and broader design-to-supply capabilities.

Report Scope

LP Information, Inc. (LPI) ' newest research report, the "Medical Ultrasound Imaging ASIC Industry Forecast" looks at past sales and reviews total world Medical Ultrasound Imaging ASIC sales in 2025, providing a comprehensive analysis by region and market sector of projected Medical Ultrasound Imaging ASIC sales for 2026 through 2032. With Medical Ultrasound Imaging ASIC sales broken down by region, market sector and sub-sector, this report provides a detailed analysis in US\$ millions of the world Medical Ultrasound Imaging ASIC industry.

This Insight Report provides a comprehensive analysis of the global Medical Ultrasound Imaging ASIC landscape and highlights key trends related to product segmentation, company formation, revenue, and market share, latest development, and M&A activity. This report also analyzes the strategies of leading global companies with a focus on Medical Ultrasound Imaging ASIC portfolios and capabilities, market entry strategies, market positions, and geographic footprints, to better understand these firms' unique position in an accelerating global Medical Ultrasound Imaging ASIC market.

This Insight Report evaluates the key market trends, drivers, and affecting factors shaping the global outlook for Medical Ultrasound Imaging ASIC and breaks down the forecast by Type, by Application, geography, and market size to highlight emerging pockets of opportunity. With a transparent methodology based on hundreds of bottom-up qualitative and quantitative market inputs, this study forecast offers a highly nuanced view of the current state and future trajectory in the global Medical Ultrasound Imaging ASIC.

This report presents a comprehensive overview, market shares, and growth opportunities of Medical Ultrasound Imaging ASIC market by product type, application, key manufacturers and key regions and countries.

Market Segmentation

Segmentation by Type:

High-Voltage Transmitter ASIC

Receive Analog Front-End ASIC

T/R Switch and Multiplexer ASIC

Beamforming ASIC

Integrated Ultrasound ASIC / SoC

Other Specialized ASIC

Segmentation by Integration Level:

Single-Function ASIC

Multi-Function Front-End ASIC

Highly Integrated Ultrasound SoC

Segmentation by Transducer Compatibility:

Piezoelectric Transducer ASIC

CMUT-Compatible ASIC

PMUT-Compatible ASIC

Other Transducer ASIC

Segmentation by Channel Count:

1–8 Channels

9–32 Channels

33–64 Channels

65–128 Channels

More Than 128 Channels

Segmentation by Application:

Cart-Based Ultrasound Systems

Handheld Ultrasound Systems

Catheter-Based Ultrasound Systems

Wearable and Patch Ultrasound Systems

Other

This report also splits the market by region:

Americas

United States

Canada

Mexico

Brazil

APAC

China

Japan

Korea

Southeast Asia

India

Australia

Europe

Germany

France

UK

Italy

Russia

Middle East & Africa

Egypt

South Africa

Israel

Turkey

GCC Countries

The below companies that are profiled have been selected based on inputs gathered from primary experts and analysing the company's coverage, product portfolio, its market penetration.

Texas Instruments Incorporated

Analog Devices, Inc.

STMicroelectronics N.V.

MinebeaMitsumi Inc.

Microchip Technology Incorporated

Monolithic Power Systems, Inc.

SEALSQ Corp

Koninklijke Philips N.V.

Butterfly Network, Inc.

Exo Imaging, Inc.

Sonosilicon

Key Questions Addressed in this Report

What is the 10-year outlook for the global Medical Ultrasound Imaging ASIC market?

What factors are driving Medical Ultrasound Imaging ASIC market growth, globally and by region?

Which technologies are poised for the fastest growth by market and region?

How do Medical Ultrasound Imaging ASIC market opportunities vary by end market size?

How does Medical Ultrasound Imaging ASIC break out by Type, by Application?

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