

Global Integrated Storage and Computing Chip Market Growth (Status and Outlook) 2026-2032

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Abstracts

The global Integrated Storage and Computing Chip market size is predicted to grow from US\$ 226 million in 2025 to US\$ 48777 million in 2032; it is expected to grow at a CAGR of 116.5% from 2026 to 2032.

Integrated Storage and Computing Chip, also known as In-Memory Computing, is an advanced chip architecture that integrates computation directly within memory cells or memory arrays. By performing operations such as multiply-accumulate, logic, or vector calculations at the data storage location, CIM significantly reduces frequent data movement between memory and processors, thereby overcoming the “memory wall” and “power wall” limitations of the traditional von Neumann architecture. These chips are typically implemented using SRAM, DRAM, Flash, or emerging non-volatile memories such as ReRAM, PCM, and MRAM, and are particularly well suited for AI inference, edge computing, and energy-efficient parallel processing, offering substantial improvements in energy efficiency, latency reduction, and overall system performance.

Integrated Storage and Computing Chip market is emerging as a critical segment within the next generation of AI and data-centric semiconductor technologies, driven by the growing limitations of traditional von Neumann architectures. IMC chips integrate computation directly within or near memory arrays, significantly reducing data movement between memory and processors, which in turn lowers latency and power consumption while improving overall system efficiency. This architecture is particularly well-suited for data-intensive workloads such as artificial intelligence, machine learning inference and training, edge computing, high-performance computing (HPC), and real-time analytics.

Market growth is fueled by the rapid expansion of AI applications, especially in neural

networks, recommendation systems, computer vision, and natural language processing, where memory bandwidth and energy efficiency are key bottlenecks. Both digital IMC (based on SRAM, DRAM, or emerging memory) and analog IMC (often using RRAM, PCM, or MRAM) approaches are being actively developed, with analog IMC attracting attention for its high parallelism and energy efficiency in matrix-vector multiplication tasks. However, challenges remain in areas such as precision control, reliability, process variation, and large-scale manufacturability.

From a market structure perspective, the IMC chip ecosystem includes emerging startups, leading semiconductor vendors, foundries, and research institutes, with strong participation from the United States, China, Europe, Japan, and South Korea. In the near to mid term, adoption is expected to accelerate in edge AI and inference-oriented applications, while broader deployment in data centers and large-scale AI training will depend on continued progress in accuracy, software ecosystems, and standardization. Overall, the In-Memory Computing chip market is positioned for long-term growth as AI workloads continue to scale and energy efficiency becomes a defining constraint in semiconductor system design.

LPI (LP Information)' newest research report, the “Integrated Storage and Computing Chip Industry Forecast” looks at past sales and reviews total world Integrated Storage and Computing Chip sales in 2025, providing a comprehensive analysis by region and market sector of projected Integrated Storage and Computing Chip sales for 2026 through 2032. With Integrated Storage and Computing Chip sales broken down by region, market sector and sub-sector, this report provides a detailed analysis in US\$ millions of the world Integrated Storage and Computing Chip industry.

This Insight Report provides a comprehensive analysis of the global Integrated Storage and Computing Chip landscape and highlights key trends related to product segmentation, company formation, revenue, and market share, latest development, and M&A activity. This report also analyses the strategies of leading global companies with a focus on Integrated Storage and Computing Chip portfolios and capabilities, market entry strategies, market positions, and geographic footprints, to better understand these firms' unique position in an accelerating global Integrated Storage and Computing Chip market.

This Insight Report evaluates the key market trends, drivers, and affecting factors shaping the global outlook for Integrated Storage and Computing Chip and breaks down the forecast by Type, by Application, geography, and market size to highlight emerging pockets of opportunity. With a transparent methodology based on hundreds of bottom-

up qualitative and quantitative market inputs, this study forecast offers a highly nuanced view of the current state and future trajectory in the global Integrated Storage and Computing Chip.

This report presents a comprehensive overview, market shares, and growth opportunities of Integrated Storage and Computing Chip market by product type, application, key players and key regions and countries.

Segmentation by Type:

DRAM-PIM

SRAM-PIM

Others

Segmentation by System Architecture:

True In-Memory Computing

Near-Memory Computing

Segmentation by Computing Method:

Digital In-Memory Computing

Analog In-Memory Computing

Segmentation by Application:

Small Computing Power

Large Computing Power

This report also splits the market by region:

Americas

United States

Canada

Mexico

Brazil

APAC

China

Japan

Korea

Southeast Asia

India

Australia

Europe

Germany

France

UK

Italy

Russia

Middle East & Africa

Egypt

South Africa

Israel

Turkey

GCC Countries

The below companies that are profiled have been selected based on inputs gathered from primary experts and analyzing the company's coverage, product portfolio, its market penetration.

Samsung

SK Hynix

Syntiant

Myhtic

D-Matrix

Hangzhou Zhicun (Witmem) Technology

Beijing Pingxin Technology

Shenzhen Reexen Technology

Beijing Houmo Technology

Graphcore

AistarTek

Suzhou Yizhu Intelligent Technology

EnCharge AI

Axelera AI

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