

Global IC Packaging EDA Tools Market Growth (Status and Outlook) 2026-2032

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Abstracts

The global IC Packaging EDA Tools market size is predicted to grow from US\$ 728 million in 2025 to US\$ 1117 million in 2032; it is expected to grow at a CAGR of 6.4% from 2026 to 2032.

This report studies EDA Tools in IC Packaging market.

Electronic Design Automation (EDA) is a specific category of hardware, software, services and processes that use computer-aided design to develop complex electronic systems like printed circuit boards, integrated circuits and microprocessors.

United States market for IC Packaging EDA Tools is estimated to increase from US\$ million in 2025 to US\$ million by 2032, at a CAGR of % from 2026 through 2032.

China market for IC Packaging EDA Tools is estimated to increase from US\$ million in 2025 to US\$ million by 2032, at a CAGR of % from 2026 through 2032.

Europe market for IC Packaging EDA Tools is estimated to increase from US\$ million in 2025 to US\$ million by 2032, at a CAGR of % from 2026 through 2032.

Global key IC Packaging EDA Tools players cover Synopsys (Ansys), Cadence, Siemens EDA, Empyrean Technology, Faraday Dynamics, Ltd., etc. In terms of revenue, the global two largest companies occupied for a share nearly % in 2025.

LPI (LP Information)' newest research report, the 'IC Packaging EDA Tools Industry Forecast' looks at past sales and reviews total world IC Packaging EDA Tools sales in 2025, providing a comprehensive analysis by region and market sector of projected IC

Packaging EDA Tools sales for 2026 through 2032. With IC Packaging EDA Tools sales broken down by region, market sector and sub-sector, this report provides a detailed analysis in US\$ millions of the world IC Packaging EDA Tools industry.

This Insight Report provides a comprehensive analysis of the global IC Packaging EDA Tools landscape and highlights key trends related to product segmentation, company formation, revenue, and market share, latest development, and M&A activity. This report also analyses the strategies of leading global companies with a focus on IC Packaging EDA Tools portfolios and capabilities, market entry strategies, market positions, and geographic footprints, to better understand these firms' unique position in an accelerating global IC Packaging EDA Tools market.

This Insight Report evaluates the key market trends, drivers, and affecting factors shaping the global outlook for IC Packaging EDA Tools and breaks down the forecast by Type, by Application, geography, and market size to highlight emerging pockets of opportunity. With a transparent methodology based on hundreds of bottom-up qualitative and quantitative market inputs, this study forecast offers a highly nuanced view of the current state and future trajectory in the global IC Packaging EDA Tools.

This report presents a comprehensive overview, market shares, and growth opportunities of IC Packaging EDA Tools market by product type, application, key players and key regions and countries.

Segmentation by Type:

IC Packaging Design Tool

IC Simulation Tool

Segmentation by Application:

Automotive & EV/HEV

PV, Wind Power and Power Grid

Industrial Drives

Consumer & White Goods

Rail Transport

Military & Avionics

Others

This report also splits the market by region:

Americas

United States

Canada

Mexico

Brazil

APAC

China

Japan

Korea

Southeast Asia

India

Australia

Europe

Germany

France

UK

Italy

Russia

Middle East & Africa

Egypt

South Africa

Israel

Turkey

GCC Countries

The below companies that are profiled have been selected based on inputs gathered from primary experts and analyzing the company's coverage, product portfolio, its market penetration.

Synopsys (Ansys)

Cadence

Siemens EDA

Empyrean Technology

Faraday Dynamics, Ltd.

Xpeedic Technology

Shanghai RedEDA Co.,Ltd.

Physim Electronic Technology

Detool Technology

Ningbo Vxin Technology

PZEDA

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