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# Global HSMT SerDes Chip Market Growth 2026-2032

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## Abstracts

The global HSMT SerDes Chip market size is predicted to grow from US$ 98 million in 2025 to US$ 358 million in 2032; it is expected to grow at a CAGR of 20.7% from 2026 to 2032.

The HSMT SerDes Chip is a high-speed serial/deserializer designed for automotive and communication applications, supporting the HSMT protocol. It provides low-latency, high-bandwidth data conversion with robust electromagnetic immunity, meeting automotive-grade reliability and signal integrity requirements. It serves as a key foundational component for high-performance in-vehicle networks and domain control systems. In 2024, the production of this product was 36.09 million units, with a unit price of 2.3 USD/unit. The single-line annual capacity was approximately 0.2 million units and the average gross margin was about 50%. Upstream materials and equipment include wafers, substrates, and packaging materials, as well as precision manufacturing tools such as lithography machines, etchers, and ion implanters, with representative suppliers including SUMCO, GlobalWafers, Shin-Etsu, ASML, Applied Materials, Lam Research, and Chinese suppliers such as Shanghai Silicon Industry Group, AMEC, and JCET. The midstream focuses on process and design implementation, covering physical layer IP integration, analog front-end and mixed-signal circuit design, packaging and test flow development, and signal integrity and reliability verification. Downstream applications target commercial vehicles and passenger cars with representative customers such as Daimler, Volvo Trucks, Cummins, SAIC Motor and Geely.

Automotive-grade SerDes chips are positioned for accelerated adoption driven by the proliferation of multi-camera and multi-sensor ADAS/automated driving suites and the shift to high-bandwidth in-vehicle Ethernet architectures. Standardization efforts and open ecosystem initiatives are reducing integration friction and unit costs, while technical requirements for low latency, deterministic links and automotive reliability create a high-barrier moat for qualified suppliers — making SerDes not only a technical

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enabler for next-generation domain controllers but also an attractive, long-term market segment for semiconductor vendors and tier-1 suppliers.

## Report Scope

LP Information, Inc. (LPI) ' newest research report, the “HSMT SerDes Chip Industry Forecast” looks at past sales and reviews total world HSMT SerDes Chip sales in 2025, providing a comprehensive analysis by region and market sector of projected HSMT SerDes Chip sales for 2026 through 2032. With HSMT SerDes Chip sales broken down by region, market sector and sub-sector, this report provides a detailed analysis in US$ millions of the world HSMT SerDes Chip industry.

This Insight Report provides a comprehensive analysis of the global HSMT SerDes Chip landscape and highlights key trends related to product segmentation, company formation, revenue, and market share, latest development, and M&A activity. This report also analyzes the strategies of leading global companies with a focus on HSMT SerDes Chip portfolios and capabilities, market entry strategies, market positions, and geographic footprints, to better understand these firms’ unique position in an accelerating global HSMT SerDes Chip market.

This Insight Report evaluates the key market trends, drivers, and affecting factors shaping the global outlook for HSMT SerDes Chip and breaks down the forecast by Type, by Application, geography, and market size to highlight emerging pockets of opportunity. With a transparent methodology based on hundreds of bottom-up qualitative and quantitative market inputs, this study forecast offers a highly nuanced view of the current state and future trajectory in the global HSMT SerDes Chip.

This report presents a comprehensive overview, market shares, and growth opportunities of HSMT SerDes Chip market by product type, application, key manufacturers and key regions and countries.

## Market Segmentation

### Segmentation by Type:

? 6Gbps

? 6Gbps

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## Segmentation by Channel:

Single-Channel

Multi-Channel

## Segmentation by Process Node:

40nm

28nm

Others

## Segmentation by Body Domain:

Cockpit Domain

In-Vehicle Infotainment Domain

Others

## Segmentation by Application:

Passenger Cars

Commercial Vehicle

This report also splits the market by region:

Americas

United States

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- Canada
- Mexico
- Brazil

APAC

- China
- Japan
- Korea
- Southeast Asia
- India
- Australia

Europe

- Germany
- France
- UK
- Italy
- Russia

Middle East & Africa

- Egypt
- South Africa
- Israel

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Turkey

GCC Countries

The below companies that are profiled have been selected based on inputs gathered from primary experts and analysing the company's coverage, product portfolio, its market penetration.

Analog Devices (Maxim)

Texas Instruments

Inova Semiconductors

Sony Semiconductor

ROHM Semiconductor

THine Electronics

Renesas

Goke Microelectronics

Ruifake Semiconductor

VelinkTech

## Key Questions Addressed in this Report

What is the 10-year outlook for the global HSMT SerDes Chip market?

What factors are driving HSMT SerDes Chip market growth, globally and by region?

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Which technologies are poised for the fastest growth by market and region?

How do HSMT SerDes Chip market opportunities vary by end market size?

How does HSMT SerDes Chip break out by Type, by Application?

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