

Global High-purity Silicon Carbide Powder for Wafer Market Growth 2026-2032

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Abstracts

The global High-purity Silicon Carbide Powder for Wafer market size is predicted to grow from US\$ 129 million in 2025 to US\$ 377 million in 2032; it is expected to grow at a CAGR of 16.3% from 2026 to 2032.

High-purity silicon carbide (SiC) powder for wafer manufacturing is a purified, size-classified SiC feedstock engineered for physical vapor transport (PVT) growth of single-crystal SiC boules. Compared with general abrasive-grade SiC, it specifies ultra-low metallic and oxygen impurities, controlled free-Si/free-C, and an optimized particle-size distribution and packing behavior that stabilize sublimation/recrystallization and increase source utilization in the PVT hot zone.

The key raw materials include carbon powder (graphite), silicon powder, high-purity quartz / silica, chlorosilane precursor, hydrogen. Downstream applications are mainly used in the production of wafers of different sizes, such as 4-inch, 6-inch, and 8-inch.

In 2025, global sales of high-purity silicon carbide powder for wafer reached approximately 776 tons, with an average global market price of around US\$ 170/kg. Production capacity varies significantly among manufacturers, with gross profit margins ranging from approximately 20% to 40%.

Silicon carbide (SiC) is a compound semiconductor composed of silicon and carbide. SiC provides a number of advantages over silicon, including 10x the breakdown electric field strength, 3x the band gap, and enabling a wider range of p- and n-type control required for device construction. The result is breakthrough performance, not possible with silicon, making it the most viable successor for next-generation power devices.

As demand for electric vehicles (EVs) continues to grow, manufacturers are comparing two semiconductor technologies, silicon carbide and silicon, for power electronics applications. Silicon carbide (SiC) provides the high temperature resistance, low power consumption, rigidity, and support for smaller, thinner designs that EV power electronics need. Examples of SiC's current applications include on-board DC/DC converters, off-board DC fast chargers, on-board battery chargers, EV powertrains, and automotive lighting for LEDs.

SiC technology could also drive future EV innovations as battery and motor manufacturers reach the physical limits of existing technologies and need to develop more efficient drivetrains.

Fast demand from downstream industries stimulates the SiC related industries, including the upstream and downstream industries. High purity silicon carbide powder for wafer industry will benefit from this and keeps fast growth in the coming years.

LP Information, Inc. (LPI) ' newest research report, the "High-purity Silicon Carbide Powder for Wafer Industry Forecast" looks at past sales and reviews total world High-purity Silicon Carbide Powder for Wafer sales in 2025, providing a comprehensive analysis by region and market sector of projected High-purity Silicon Carbide Powder for Wafer sales for 2026 through 2032. With High-purity Silicon Carbide Powder for Wafer sales broken down by region, market sector and sub-sector, this report provides a detailed analysis in US\$ millions of the world High-purity Silicon Carbide Powder for Wafer industry.

This Insight Report provides a comprehensive analysis of the global High-purity Silicon Carbide Powder for Wafer landscape and highlights key trends related to product segmentation, company formation, revenue, and market share, latest development, and M&A activity. This report also analyzes the strategies of leading global companies with a focus on High-purity Silicon Carbide Powder for Wafer portfolios and capabilities, market entry strategies, market positions, and geographic footprints, to better understand these firms' unique position in an accelerating global High-purity Silicon Carbide Powder for Wafer market.

This Insight Report evaluates the key market trends, drivers, and affecting factors shaping the global outlook for High-purity Silicon Carbide Powder for Wafer and breaks down the forecast by Type, by Application, geography, and market size to highlight emerging pockets of opportunity. With a transparent methodology based on hundreds of bottom-up qualitative and quantitative market inputs, this study forecast offers a highly

nuanced view of the current state and future trajectory in the global High-purity Silicon Carbide Powder for Wafer.

This report presents a comprehensive overview, market shares, and growth opportunities of High-purity Silicon Carbide Powder for Wafer market by product type, application, key manufacturers and key regions and countries.

Segmentation by Type:

SHS Method

Acheson Method

CVD Method

Segmentation by Purity:

5N

6N

Other

Segmentation by Electrical Behavior:

Conductive (n-Type)

Semi-Insulating Type

Segmentation by Application:

4 Inch Wafer

6 Inch Wafer

8 Inch Wafer

This report also splits the market by region:

Americas

United States

Canada

Mexico

Brazil

APAC

China

Japan

Korea

Southeast Asia

India

Australia

Europe

Germany

France

UK

Italy

Russia

Middle East & Africa

Egypt

South Africa

Israel

Turkey

GCC Countries

The below companies that are profiled have been selected based on inputs gathered from primary experts and analysing the company's coverage, product portfolio, its market penetration.

Wolfspeed

Coherent

SiCrystal

SK Siltron

Resonac

STMicroelectronics

Kymera International

Washington Mills

Shanxi Semisic Crystal

TankeBlue

SICC

Hebei Synlight Semiconductor

Ningbo Alpha Semiconductor

NANOCMS

Zadient

Key Questions Addressed in this Report

What is the 10-year outlook for the global High-purity Silicon Carbide Powder for Wafer market?

What factors are driving High-purity Silicon Carbide Powder for Wafer market growth, globally and by region?

Which technologies are poised for the fastest growth by market and region?

How do High-purity Silicon Carbide Powder for Wafer market opportunities vary by end market size?

How does High-purity Silicon Carbide Powder for Wafer break out by Type, by Application?

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