

# Global High Level Synthesis Compilers Market Growth (Status and Outlook) 2023-2029

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# **Abstracts**

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High-level synthesis (HLS), sometimes referred to as C synthesis, electronic system-level (ESL) synthesis, algorithmic synthesis, or behavioral synthesis, is an automated design process that interprets an algorithmic description of a desired behavior and creates digital hardware that implements that behavior.

LPI (LP Information)' newest research report, the "High Level Synthesis Compilers Industry Forecast" looks at past sales and reviews total world High Level Synthesis Compilers sales in 2022, providing a comprehensive analysis by region and market sector of projected High Level Synthesis Compilers sales for 2023 through 2029. With High Level Synthesis Compilers sales broken down by region, market sector and subsector, this report provides a detailed analysis in US\$ millions of the world High Level Synthesis Compilers industry.

This Insight Report provides a comprehensive analysis of the global High Level Synthesis Compilers landscape and highlights key trends related to product segmentation, company formation, revenue, and market share, latest development, and M&A activity. This report also analyzes the strategies of leading global companies with a focus on High Level Synthesis Compilers portfolios and capabilities, market entry strategies, market positions, and geographic footprints, to better understand these firms' unique position in an accelerating global High Level Synthesis Compilers market.

This Insight Report evaluates the key market trends, drivers, and affecting factors shaping the global outlook for High Level Synthesis Compilers and breaks down the forecast by type, by application, geography, and market size to highlight emerging



pockets of opportunity. With a transparent methodology based on hundreds of bottom-up qualitative and quantitative market inputs, this study forecast offers a highly nuanced view of the current state and future trajectory in the global High Level Synthesis Compilers.

The global High Level Synthesis Compilers market size is projected to grow from US\$ million in 2022 to US\$ million in 2029; it is expected to grow at a CAGR of % from 2023 to 2029.

United States market for High Level Synthesis Compilers is estimated to increase from US\$ million in 2022 to US\$ million by 2029, at a CAGR of % from 2023 through 2029.

China market for High Level Synthesis Compilers is estimated to increase from US\$ million in 2022 to US\$ million by 2029, at a CAGR of % from 2023 through 2029.

Europe market for High Level Synthesis Compilers is estimated to increase from US\$ million in 2022 to US\$ million by 2029, at a CAGR of % from 2023 through 2029.

Global key High Level Synthesis Compilers players cover Intel, Xilinx, Cadence, MathWorks, Siemens, GAUT, Lombiq Technologies, FPGA Cores and Microchip Technology, etc. In terms of revenue, the global two largest companies occupied for a share nearly % in 2022.

This report presents a comprehensive overview, market shares, and growth opportunities of High Level Synthesis Compilers market by product type, application, key players and key regions and countries.

Market Segmentation:
Segmentation by type

C/C++

Matlab

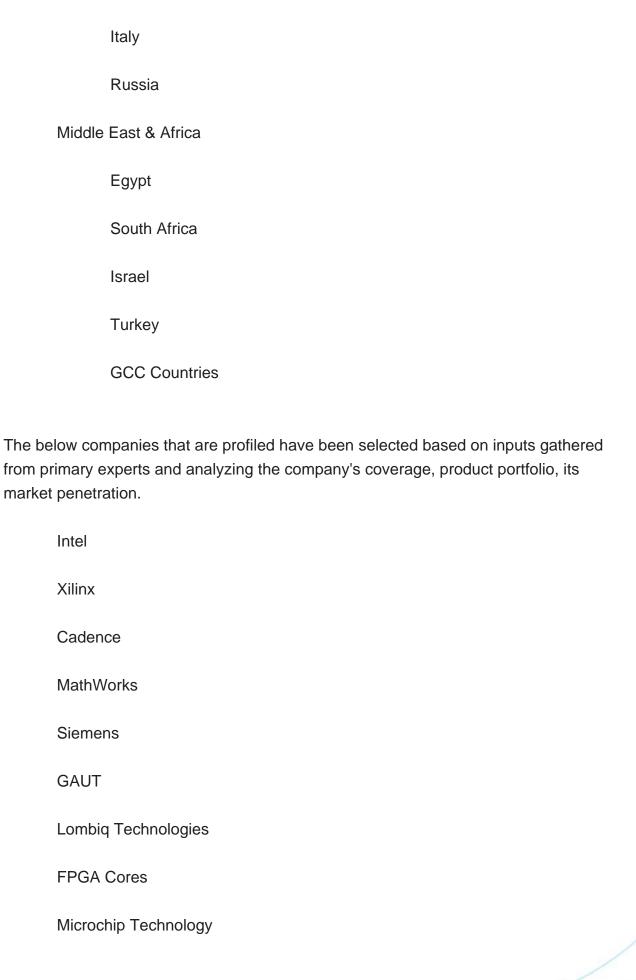
Others

Segmentation by application



Academic Use		
Commercial Use		
This report also splits the market by region:		
Americas		
	United States	
	Canada	
	Mexico	
	Brazil	
APAC		
	China	
	Japan	
	Korea	
	Southeast Asia	
	India	
	Australia	
Europe		
	Germany	
	France	
	UK	







Bluespec

Nikolaos Kavvadias



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