

# Global Gate-All-Around FET (GAAFET) Technology Market Growth 2018-2023

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# **Abstracts**

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Gate-All-Around FET (GAAFET) is a multi-gate device that interpolates more than one gate devices into a single device. GAAFET is silicone nanowire with gate going around it. It is a device, where the gate is placed on all four sides of the channel. These multiple gates are controlled by a single gate electrode.

The major factor that drives the demand of GAAFET technology market is the enhancement of breakdown voltage. Minimized energy losses is also the key driver of the market.

In addition GAAFET also has high efficiency, improved durability, and it supports high input impudence, which in turn fuels the market growth. High fabrication cost majorly restricts the market growth.

However, performance issues such as current leakage and breakdown hamper the market.

Over the next five years, LPI(LP Information) projects that Gate-All-Around FET (GAAFET) Technology will register a xx% CAGR in terms of revenue, reach US\$ xx million by 2023, from US\$ xx million in 2017.

In this report, LP Information covers the present scenario (with the base year being 2017) and the growth prospects of global Gate-All-Around FET (GAAFET) Technology market for 2018-2023.

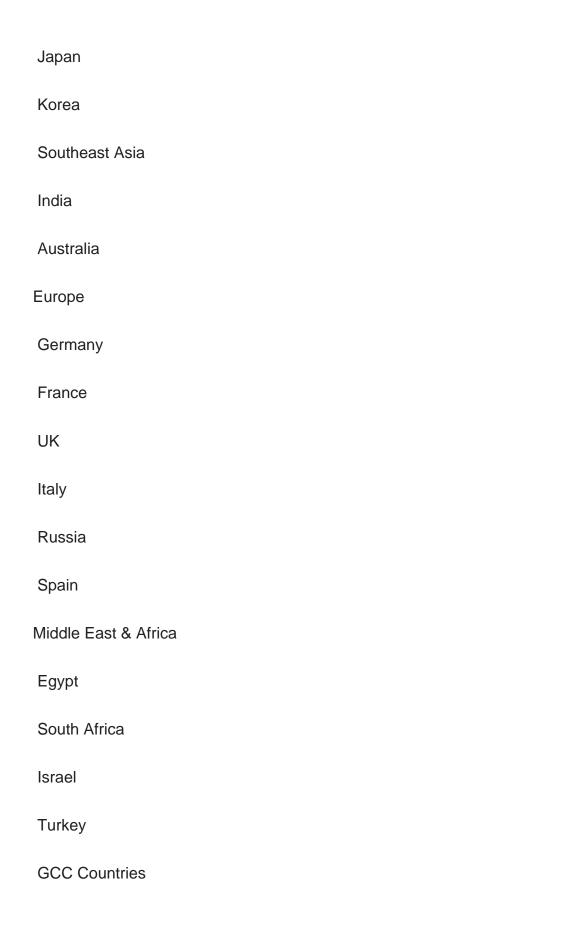
This report presents a comprehensive overview, market shares, and growth opportunities of Gate-All-Around FET (GAAFET) Technology market by product type, application, key manufacturers and key regions.



To calculate the market size, LP Information considers value and volume generated from the sales of the following segments:

Segmentation by product type:			
Т	ype I		
Т	ype II		
Segmentation by application:			
E	Energy & Power		
C	Consumer Electronics		
lr	nverter & UPS		
lr	ndustrial System		
C	Others (Medical Devices & Traction)		
This report also splits the market by region:			
А	Americas		
l	United States		
(	Canada		
ľ	Mexico		
E	Brazil		
А	APAC		
(	China		





The report also presents the market competition landscape and a corresponding detailed analysis of the major vendor/manufacturers in the market. The key



manufacturers	covered	in	this	report:
manadataroro	OO V CI CG		uiio	Toport.

Infineon Technologies AG

Fairchild Semiconductor.

Renesas Electronics Corporation

Digi-Key Electronics

**Toshiba Corporation** 

**IXYS** Corporation

**Power Integration** 

**STMicroelectronics** 

NXP semiconductors

**ABB Group** 

In addition, this report discusses the key drivers influencing market growth, opportunities, the challenges and the risks faced by key manufacturers and the market as a whole. It also analyzes key emerging trends and their impact on present and future development.

#### Research objectives

To study and analyze the global Gate-All-Around FET (GAAFET) Technology consumption (value & volume) by key regions/countries, product type and application, history data from 2013 to 2017, and forecast to 2023.

To understand the structure of Gate-All-Around FET (GAAFET) Technology market by identifying its various subsegments.

Focuses on the key global Gate-All-Around FET (GAAFET) Technology manufacturers, to define, describe and analyze the sales volume, value, market



share, market competition landscape, SWOT analysis and development plans in next few years.

To analyze the Gate-All-Around FET (GAAFET) Technology with respect to individual growth trends, future prospects, and their contribution to the total market.

To share detailed information about the key factors influencing the growth of the market (growth potential, opportunities, drivers, industry-specific challenges and risks).

To project the consumption of Gate-All-Around FET (GAAFET) Technology submarkets, with respect to key regions (along with their respective key countries).

To analyze competitive developments such as expansions, agreements, new product launches, and acquisitions in the market.

To strategically profile the key players and comprehensively analyze their growth strategies.



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