

# Global FPGA and PLD Market Growth 2025-2031

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## Abstracts

The global FPGA and PLD market size is predicted to grow from US\$ million in 2025 to US\$ million in 2031; it is expected to grow at a CAGR of % from 2025 to 2031.

The impact of the latest U.S. tariff measures and the corresponding policy responses from countries worldwide on market competitiveness, regional economic performance, and supply chain configurations will be comprehensively evaluated in this report.

A programmable logic device (PLD) is an electronic component used to build reconfigurable digital circuits. Unlike integrated circuits (IC) which consist of logic gates and have a fixed function, a PLD has an undefined function at the time of manufacture.

United States market for FPGA and PLD is estimated to increase from US\$ million in 2024 to US\$ million by 2031, at a CAGR of % from 2025 through 2031.

China market for FPGA and PLD is estimated to increase from US\$ million in 2024 to US\$ million by 2031, at a CAGR of % from 2025 through 2031.

Europe market for FPGA and PLD is estimated to increase from US\$ million in 2024 to US\$ million by 2031, at a CAGR of % from 2025 through 2031.

Global key FPGA and PLD players cover AMD (Xilinx), Intel (Altera), Microchip Technology, Lattice Semiconductor, Achronix Semiconductor Corporation, etc. In terms of revenue, the global two largest companies occupied for a share nearly % in 2024.

LP Information, Inc. (LPI) ' newest research report, the "FPGA and PLD Industry Forecast" looks at past sales and reviews total world FPGA and PLD sales in 2024, providing a comprehensive analysis by region and market sector of projected FPGA and PLD sales for 2025 through 2031. With FPGA and PLD sales broken down by region,

market sector and sub-sector, this report provides a detailed analysis in US\$ millions of the world FPGA and PLD industry.

This Insight Report provides a comprehensive analysis of the global FPGA and PLD landscape and highlights key trends related to product segmentation, company formation, revenue, and market share, latest development, and M&A activity. This report also analyzes the strategies of leading global companies with a focus on FPGA and PLD portfolios and capabilities, market entry strategies, market positions, and geographic footprints, to better understand these firms' unique position in an accelerating global FPGA and PLD market.

This Insight Report evaluates the key market trends, drivers, and affecting factors shaping the global outlook for FPGA and PLD and breaks down the forecast by Type, by Application, geography, and market size to highlight emerging pockets of opportunity. With a transparent methodology based on hundreds of bottom-up qualitative and quantitative market inputs, this study forecast offers a highly nuanced view of the current state and future trajectory in the global FPGA and PLD.

This report presents a comprehensive overview, market shares, and growth opportunities of FPGA and PLD market by product type, application, key manufacturers and key regions and countries.

### **Segmentation by Type:**

SPLDs

CPLDs

FPGAs

### **Segmentation by Application:**

Telecom

Consumer Electronics

Automotive

Industrial

Military and Aerospace

Data Processing

Others

**This report also splits the market by region:**

Americas

United States

Canada

Mexico

Brazil

APAC

China

Japan

Korea

Southeast Asia

India

Australia

Europe

Germany

France

UK

Italy

Russia

Middle East & Africa

Egypt

South Africa

Israel

Turkey

GCC Countries

The below companies that are profiled have been selected based on inputs gathered from primary experts and analysing the company's coverage, product portfolio, its market penetration.

AMD (Xilinx)

Intel (Altera)

Microchip Technology

Lattice Semiconductor

Achronix Semiconductor Corporation

### **Key Questions Addressed in this Report**

What is the 10-year outlook for the global FPGA and PLD market?

What factors are driving FPGA and PLD market growth, globally and by region?

Which technologies are poised for the fastest growth by market and region?  
How do FPGA and PLD market opportunities vary by end market size?  
How does FPGA and PLD break out by Type, by Application?

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