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Global Flip-Chip Bumping Market Growth 2025-2031

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Abstracts

The global Flip-Chip Bumping market size is predicted to grow from US$ 3171 million in 2025 to US$ 4957 million in 2031; it is expected to grow at a CAGR of 7.7% from 2025 to 2031.

The impact of the latest U.S. tariff measures and the corresponding policy responses from countries worldwide on market competitiveness, regional economic performance, and supply chain configurations will be comprehensively evaluated in this report.

Flip-Chip Bumping is a key interconnect technology in advanced semiconductor packaging that involves creating metallic bumps on the active side of a die, which is then flipped and bonded face-down onto a substrate or interposer. This technique significantly shortens interconnect length compared to traditional wire bonding, offering superior electrical performance, improved thermal dissipation, and higher I/O density. Flip-chip bumps are categorized by material and structure, including solder bumps (e.g., SnAg), copper pillar bumps, CuNiAu bumps, and micro-bumps for fine-pitch applications. These bumps can range from >100 ?m for standard applications down to

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