

Global Edge AI Accelerator Market Growth 2026-2032

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Abstracts

The global Edge AI Accelerator market size is predicted to grow from US\$ 9353 million in 2025 to US\$ 54089 million in 2032; it is expected to grow at a CAGR of 29.5% from 2026 to 2032.

Edge AI accelerators are AI inference acceleration hardware or hardware-software solutions optimized specifically for edge scenarios (non-cloud environments). By leveraging specialized architectures such as NPUs, GPUs, FPGAs, and ASICs, they enable low-latency, high-privacy AI inference on local devices and are widely used in applications such as smart cameras, autonomous driving, industrial robots, and IoT endpoints.

Global shipments of edge AI accelerators are projected to reach 427.59 million units in 2025, with an average price of \$22.36 per unit.

Technology Architecture Trends: Energy Efficiency Revolution and Architectural Reconstruction

NPU-Dominated, Specialization Becomes the Norm

NPUs become the mainstream at the edge, with energy efficiency (TOPS/W) 5-10 times that of GPUs

Diversification from general-purpose NPUs to scenario-specific NPUs: high computing power for automotive, low power consumption for security, and real-time performance for industrial applications

By 2026, mainstream mid-to-high-end edge SoCs will achieve an energy efficiency ratio exceeding 10 TOPS/W, with high-end models reaching 20-50 TOPS/W

Compute-in-Memory (CIM) / Processing-in-Memory (PIM) Break Through the Memory Wall

Alleviates the bottleneck of ?storage speeds far lagging behind compute speeds,? with bandwidth increasing 3?10 times and power consumption dropping by over 50%

Commercialization by 2026: Post-Mo Intelligence, Zhidun, Samsung, Intel, and Alibaba?s PingTouGe are making strategic moves

Widespread adoption of extreme quantization and low-precision computing

Shift from INT8 to INT4/FP4, maintaining precision while doubling computing density and halving power consumption

NVIDIA?s Blackwell edge chip (Jetson T4000) supports FP4, delivering 1,200 TFLOPS of computing power

Support for lightweight large models (1B?7B) to run stably on low-power edge chips

Heterogeneous SoCs become mainstream

Single-chip integration of CPU + NPU + GPU + ISP + security engine

Dynamic computing power scheduling: collaboration between AI inference, video encoding/decoding, and control tasks

Representatives: Gaotong 8295, NVIDIA Orin, Horizon Journey 6, Rockchip RK3588

LP Information, Inc. (LPI) ' newest research report, the ?Edge AI Accelerator Industry Forecast? looks at past sales and reviews total world Edge AI Accelerator sales in 2025, providing a comprehensive analysis by region and market sector of projected Edge AI Accelerator sales for 2026 through 2032. With Edge AI Accelerator sales broken down by region, market sector and sub-sector, this report provides a detailed analysis in US\$ millions of the world Edge AI Accelerator industry.

This Insight Report provides a comprehensive analysis of the global Edge AI Accelerator landscape and highlights key trends related to product segmentation, company formation, revenue, and market share, latest development, and M&A activity.

This report also analyzes the strategies of leading global companies with a focus on Edge AI Accelerator portfolios and capabilities, market entry strategies, market positions, and geographic footprints, to better understand these firms' unique position in an accelerating global Edge AI Accelerator market.

This Insight Report evaluates the key market trends, drivers, and affecting factors shaping the global outlook for Edge AI Accelerator and breaks down the forecast by Type, by Application, geography, and market size to highlight emerging pockets of opportunity. With a transparent methodology based on hundreds of bottom-up qualitative and quantitative market inputs, this study forecast offers a highly nuanced view of the current state and future trajectory in the global Edge AI Accelerator.

This report presents a comprehensive overview, market shares, and growth opportunities of Edge AI Accelerator market by product type, application, key manufacturers and key regions and countries.

Segmentation by Type:

Dedicated Acceleration Chip

IP Core Integration

MCU-Level Acceleration

Heterogeneous Multi-Core

Segmentation by Power Consumption:

30W

Segmentation by Sales Channels:

Direct Sales

Distribution

Segmentation by Application:

Smart Security / Video Analytics

Industrial Vision / AOI / Robotics

Automotive ADAS / In-Vehicle Sensing

Retail / Logistics / Smart Cities

Healthcare / Professional Devices

Audio / Sensing / Multimodal Edge Devices

This report also splits the market by region:

Americas

United States

Canada

Mexico

Brazil

APAC

China

Japan

Korea

Southeast Asia

India

Australia

Europe

Germany

France

UK

Italy

Russia

Middle East & Africa

Egypt

South Africa

Israel

Turkey

GCC Countries

The below companies that are profiled have been selected based on inputs gathered from primary experts and analysing the company's coverage, product portfolio, its market penetration.

Qualcomm?US?

NVIDIA?US?

Intel?US?

NXP?NL?

AMD?US?

Horizon Robotics?CN?

Renesas?JP?

Synaptics?US?

Ambarella?US?

Rockchip Electronics?CN?

Sony Semiconductor Solutions?JP?

STMicroelectronics?NL?

Black Sesame International Holding Limited?CN?

Axera Semiconductor?CN?

Socionext?JP?

MemryX?US?

Cambrian?CN?

Mythic?US?

Axelera AI?NL?

Toshiba Electronic Devices?JP?

Key Questions Addressed in this Report

What is the 10-year outlook for the global Edge AI Accelerator market?

What factors are driving Edge AI Accelerator market growth, globally and by region?

Which technologies are poised for the fastest growth by market and region?

How do Edge AI Accelerator market opportunities vary by end market size?

How does Edge AI Accelerator break out by Type, by Application?

Contents

1 SCOPE OF THE REPORT

- 1.1 Market Introduction
- 1.2 Years Considered
- 1.3 Research Objectives
- 1.4 Market Research Methodology
- 1.5 Research Process and Data Source
- 1.6 Economic Indicators
- 1.7 Currency Considered
- 1.8 Market Estimation Caveats

2 EXECUTIVE SUMMARY

2.1 World Market Overview

- 2.1.1 Global Edge AI Accelerator Annual Sales 2021-2032

- 2.1.2 World Current & Future Analysis for Edge AI Accelerator by Geographic Region, 2021, 2025 & 2032

- 2.1.3 World Current & Future Analysis for Edge AI Accelerator by Country/Region, 2021, 2025 & 2032

2.2 Edge AI Accelerator Segment by Type

- 2.2.1 Dedicated Acceleration Chip

- 2.2.2 IP Core Integration

- 2.2.3 MCU-Level Acceleration

- 2.2.4 Heterogeneous Multi-Core

- 2.2.5 Edge AI Accelerator Sales by Type

- 2.2.5.1 Global Edge AI Accelerator Sales Market Share by Type (2021-2026)

- 2.2.5.2 Global Edge AI Accelerator Revenue and Market Share by Type (2021-2026)

- 2.2.5.3 Global Edge AI Accelerator Sale Price by Type (2021-2026)

2.3 Edge AI Accelerator Segment by Power Consumption

- 2.3.1 30W

- 2.3.5 Edge AI Accelerator Sales by Power Consumption

- 2.3.5.1 Global Edge AI Accelerator Sales Market Share by Power Consumption (2021-2026)

- 2.3.5.2 Global Edge AI Accelerator Revenue and Market Share by Power Consumption (2021-2026)

- 2.3.5.3 Global Edge AI Accelerator Sale Price by Power Consumption (2021-2026)

2.4 Edge AI Accelerator Segment by Sales Channels

2.4.1 Direct Sales

2.4.2 Distribution

2.4.3 Edge AI Accelerator Sales by Sales Channels

2.4.3.1 Global Edge AI Accelerator Sales Market Share by Sales Channels
(2021-2026)

2.4.3.2 Global Edge AI Accelerator Revenue and Market Share by Sales Channels
(2021-2026)

2.4.3.3 Global Edge AI Accelerator Sale Price by Sales Channels (2021-2026)

2.5 Edge AI Accelerator Segment by Application

2.5.1 Smart Security / Video Analytics

2.5.2 Industrial Vision / AOI / Robotics

2.5.3 Automotive ADAS / In-Vehicle Sensing

2.5.4 Retail / Logistics / Smart Cities

2.5.5 Healthcare / Professional Devices

2.5.6 Audio / Sensing / Multimodal Edge Devices

2.5.7 Edge AI Accelerator Sales by Application

2.5.7.1 Global Edge AI Accelerator Sale Market Share by Application (2021-2026)

2.5.7.2 Global Edge AI Accelerator Revenue and Market Share by Application
(2021-2026)

2.5.7.3 Global Edge AI Accelerator Sale Price by Application (2021-2026)

3 GLOBAL BY COMPANY

3.1 Global Edge AI Accelerator Breakdown Data by Company

3.1.1 Global Edge AI Accelerator Annual Sales by Company (2021-2026)

3.1.2 Global Edge AI Accelerator Sales Market Share by Company (2021-2026)

3.2 Global Edge AI Accelerator Annual Revenue by Company (2021-2026)

3.2.1 Global Edge AI Accelerator Revenue by Company (2021-2026)

3.2.2 Global Edge AI Accelerator Revenue Market Share by Company (2021-2026)

3.3 Global Edge AI Accelerator Sale Price by Company

3.4 Key Manufacturers Edge AI Accelerator Producing Area Distribution, Sales Area,
Product Type

3.4.1 Key Manufacturers Edge AI Accelerator Product Location Distribution

3.4.2 Players Edge AI Accelerator Products Offered

3.5 Market Concentration Rate Analysis

3.5.1 Competition Landscape Analysis

3.5.2 Concentration Ratio (CR3, CR5 and CR10) & (2024-2026)

3.6 New Products and Potential Entrants

3.7 Market M&A Activity & Strategy

4 WORLD HISTORIC REVIEW FOR EDGE AI ACCELERATOR BY GEOGRAPHIC REGION

- 4.1 World Historic Edge AI Accelerator Market Size by Geographic Region (2021-2026)
 - 4.1.1 Global Edge AI Accelerator Annual Sales by Geographic Region (2021-2026)
 - 4.1.2 Global Edge AI Accelerator Annual Revenue by Geographic Region (2021-2026)
- 4.2 World Historic Edge AI Accelerator Market Size by Country/Region (2021-2026)
 - 4.2.1 Global Edge AI Accelerator Annual Sales by Country/Region (2021-2026)
 - 4.2.2 Global Edge AI Accelerator Annual Revenue by Country/Region (2021-2026)
- 4.3 Americas Edge AI Accelerator Sales Growth
- 4.4 APAC Edge AI Accelerator Sales Growth
- 4.5 Europe Edge AI Accelerator Sales Growth
- 4.6 Middle East & Africa Edge AI Accelerator Sales Growth

5 AMERICAS

- 5.1 Americas Edge AI Accelerator Sales by Country
 - 5.1.1 Americas Edge AI Accelerator Sales by Country (2021-2026)
 - 5.1.2 Americas Edge AI Accelerator Revenue by Country (2021-2026)
- 5.2 Americas Edge AI Accelerator Sales by Type (2021-2026)
- 5.3 Americas Edge AI Accelerator Sales by Application (2021-2026)
- 5.4 United States
- 5.5 Canada
- 5.6 Mexico
- 5.7 Brazil

6 APAC

- 6.1 APAC Edge AI Accelerator Sales by Region
 - 6.1.1 APAC Edge AI Accelerator Sales by Region (2021-2026)
 - 6.1.2 APAC Edge AI Accelerator Revenue by Region (2021-2026)
- 6.2 APAC Edge AI Accelerator Sales by Type (2021-2026)
- 6.3 APAC Edge AI Accelerator Sales by Application (2021-2026)
- 6.4 China
- 6.5 Japan
- 6.6 South Korea
- 6.7 Southeast Asia
- 6.8 India

6.9 Australia

6.10 China Taiwan

7 EUROPE

7.1 Europe Edge AI Accelerator by Country

7.1.1 Europe Edge AI Accelerator Sales by Country (2021-2026)

7.1.2 Europe Edge AI Accelerator Revenue by Country (2021-2026)

7.2 Europe Edge AI Accelerator Sales by Type (2021-2026)

7.3 Europe Edge AI Accelerator Sales by Application (2021-2026)

7.4 Germany

7.5 France

7.6 UK

7.7 Italy

7.8 Russia

8 MIDDLE EAST & AFRICA

8.1 Middle East & Africa Edge AI Accelerator by Country

8.1.1 Middle East & Africa Edge AI Accelerator Sales by Country (2021-2026)

8.1.2 Middle East & Africa Edge AI Accelerator Revenue by Country (2021-2026)

8.2 Middle East & Africa Edge AI Accelerator Sales by Type (2021-2026)

8.3 Middle East & Africa Edge AI Accelerator Sales by Application (2021-2026)

8.4 Egypt

8.5 South Africa

8.6 Israel

8.7 Turkey

8.8 GCC Countries

9 MARKET DRIVERS, CHALLENGES AND TRENDS

9.1 Market Drivers & Growth Opportunities

9.2 Market Challenges & Risks

9.3 Industry Trends

10 MANUFACTURING COST STRUCTURE ANALYSIS

10.1 Raw Material and Suppliers

10.2 Manufacturing Cost Structure Analysis of Edge AI Accelerator

- 10.3 Manufacturing Process Analysis of Edge AI Accelerator
- 10.4 Industry Chain Structure of Edge AI Accelerator

11 MARKETING, DISTRIBUTORS AND CUSTOMER

- 11.1 Sales Channel
 - 11.1.1 Direct Channels
 - 11.1.2 Indirect Channels
- 11.2 Edge AI Accelerator Distributors
- 11.3 Edge AI Accelerator Customer

12 WORLD FORECAST REVIEW FOR EDGE AI ACCELERATOR BY GEOGRAPHIC REGION

- 12.1 Global Edge AI Accelerator Market Size Forecast by Region
 - 12.1.1 Global Edge AI Accelerator Forecast by Region (2027-2032)
 - 12.1.2 Global Edge AI Accelerator Annual Revenue Forecast by Region (2027-2032)
- 12.2 Americas Forecast by Country (2027-2032)
- 12.3 APAC Forecast by Region (2027-2032)
- 12.4 Europe Forecast by Country (2027-2032)
- 12.5 Middle East & Africa Forecast by Country (2027-2032)
- 12.6 Global Edge AI Accelerator Forecast by Type (2027-2032)
- 12.7 Global Edge AI Accelerator Forecast by Application (2027-2032)

13 KEY PLAYERS ANALYSIS

- 13.1 Qualcomm?US?
 - 13.1.1 Qualcomm?US? Company Information
 - 13.1.2 Qualcomm?US? Edge AI Accelerator Product Portfolios and Specifications
 - 13.1.3 Qualcomm?US? Edge AI Accelerator Sales, Revenue, Price and Gross Margin (2021-2026)
 - 13.1.4 Qualcomm?US? Main Business Overview
 - 13.1.5 Qualcomm?US? Latest Developments
- 13.2 NVIDIA?US?
 - 13.2.1 NVIDIA?US? Company Information
 - 13.2.2 NVIDIA?US? Edge AI Accelerator Product Portfolios and Specifications
 - 13.2.3 NVIDIA?US? Edge AI Accelerator Sales, Revenue, Price and Gross Margin (2021-2026)
 - 13.2.4 NVIDIA?US? Main Business Overview

- 13.2.5 NVIDIA?US? Latest Developments
- 13.3 Intel?US?
 - 13.3.1 Intel?US? Company Information
 - 13.3.2 Intel?US? Edge AI Accelerator Product Portfolios and Specifications
 - 13.3.3 Intel?US? Edge AI Accelerator Sales, Revenue, Price and Gross Margin (2021-2026)
 - 13.3.4 Intel?US? Main Business Overview
 - 13.3.5 Intel?US? Latest Developments
- 13.4 NXP?NL?
 - 13.4.1 NXP?NL? Company Information
 - 13.4.2 NXP?NL? Edge AI Accelerator Product Portfolios and Specifications
 - 13.4.3 NXP?NL? Edge AI Accelerator Sales, Revenue, Price and Gross Margin (2021-2026)
 - 13.4.4 NXP?NL? Main Business Overview
 - 13.4.5 NXP?NL? Latest Developments
- 13.5 AMD?US?
 - 13.5.1 AMD?US? Company Information
 - 13.5.2 AMD?US? Edge AI Accelerator Product Portfolios and Specifications
 - 13.5.3 AMD?US? Edge AI Accelerator Sales, Revenue, Price and Gross Margin (2021-2026)
 - 13.5.4 AMD?US? Main Business Overview
 - 13.5.5 AMD?US? Latest Developments
- 13.6 Horizon Robotics?CN?
 - 13.6.1 Horizon Robotics?CN? Company Information
 - 13.6.2 Horizon Robotics?CN? Edge AI Accelerator Product Portfolios and Specifications
 - 13.6.3 Horizon Robotics?CN? Edge AI Accelerator Sales, Revenue, Price and Gross Margin (2021-2026)
 - 13.6.4 Horizon Robotics?CN? Main Business Overview
 - 13.6.5 Horizon Robotics?CN? Latest Developments
- 13.7 Renesas?JP?
 - 13.7.1 Renesas?JP? Company Information
 - 13.7.2 Renesas?JP? Edge AI Accelerator Product Portfolios and Specifications
 - 13.7.3 Renesas?JP? Edge AI Accelerator Sales, Revenue, Price and Gross Margin (2021-2026)
 - 13.7.4 Renesas?JP? Main Business Overview
 - 13.7.5 Renesas?JP? Latest Developments
- 13.8 Synaptics?US?
 - 13.8.1 Synaptics?US? Company Information

- 13.8.2 Synaptics?US? Edge AI Accelerator Product Portfolios and Specifications
- 13.8.3 Synaptics?US? Edge AI Accelerator Sales, Revenue, Price and Gross Margin (2021-2026)
- 13.8.4 Synaptics?US? Main Business Overview
- 13.8.5 Synaptics?US? Latest Developments
- 13.9 Ambarella?US?
- 13.9.1 Ambarella?US? Company Information
- 13.9.2 Ambarella?US? Edge AI Accelerator Product Portfolios and Specifications
- 13.9.3 Ambarella?US? Edge AI Accelerator Sales, Revenue, Price and Gross Margin (2021-2026)
- 13.9.4 Ambarella?US? Main Business Overview
- 13.9.5 Ambarella?US? Latest Developments
- 13.10 Rockchip Electronics?CN?
- 13.10.1 Rockchip Electronics?CN? Company Information
- 13.10.2 Rockchip Electronics?CN? Edge AI Accelerator Product Portfolios and Specifications
- 13.10.3 Rockchip Electronics?CN? Edge AI Accelerator Sales, Revenue, Price and Gross Margin (2021-2026)
- 13.10.4 Rockchip Electronics?CN? Main Business Overview
- 13.10.5 Rockchip Electronics?CN? Latest Developments
- 13.11 Sony Semiconductor Solutions?JP?
- 13.11.1 Sony Semiconductor Solutions?JP? Company Information
- 13.11.2 Sony Semiconductor Solutions?JP? Edge AI Accelerator Product Portfolios and Specifications
- 13.11.3 Sony Semiconductor Solutions?JP? Edge AI Accelerator Sales, Revenue, Price and Gross Margin (2021-2026)
- 13.11.4 Sony Semiconductor Solutions?JP? Main Business Overview
- 13.11.5 Sony Semiconductor Solutions?JP? Latest Developments
- 13.12 STMicroelectronics?NL?
- 13.12.1 STMicroelectronics?NL? Company Information
- 13.12.2 STMicroelectronics?NL? Edge AI Accelerator Product Portfolios and Specifications
- 13.12.3 STMicroelectronics?NL? Edge AI Accelerator Sales, Revenue, Price and Gross Margin (2021-2026)
- 13.12.4 STMicroelectronics?NL? Main Business Overview
- 13.12.5 STMicroelectronics?NL? Latest Developments
- 13.13 Black Sesame International Holding Limited?CN?
- 13.13.1 Black Sesame International Holding Limited?CN? Company Information
- 13.13.2 Black Sesame International Holding Limited?CN? Edge AI Accelerator Product

Portfolios and Specifications

13.13.3 Black Sesame International Holding Limited?CN? Edge AI Accelerator Sales, Revenue, Price and Gross Margin (2021-2026)

13.13.4 Black Sesame International Holding Limited?CN? Main Business Overview

13.13.5 Black Sesame International Holding Limited?CN? Latest Developments

13.14 Axera Semiconductor?CN?

13.14.1 Axera Semiconductor?CN? Company Information

13.14.2 Axera Semiconductor?CN? Edge AI Accelerator Product Portfolios and Specifications

13.14.3 Axera Semiconductor?CN? Edge AI Accelerator Sales, Revenue, Price and Gross Margin (2021-2026)

13.14.4 Axera Semiconductor?CN? Main Business Overview

13.14.5 Axera Semiconductor?CN? Latest Developments

13.15 Socionext?JP?

13.15.1 Socionext?JP? Company Information

13.15.2 Socionext?JP? Edge AI Accelerator Product Portfolios and Specifications

13.15.3 Socionext?JP? Edge AI Accelerator Sales, Revenue, Price and Gross Margin (2021-2026)

13.15.4 Socionext?JP? Main Business Overview

13.15.5 Socionext?JP? Latest Developments

13.16 MemryX?US?

13.16.1 MemryX?US? Company Information

13.16.2 MemryX?US? Edge AI Accelerator Product Portfolios and Specifications

13.16.3 MemryX?US? Edge AI Accelerator Sales, Revenue, Price and Gross Margin (2021-2026)

13.16.4 MemryX?US? Main Business Overview

13.16.5 MemryX?US? Latest Developments

13.17 Cambrian?CN?

13.17.1 Cambrian?CN? Company Information

13.17.2 Cambrian?CN? Edge AI Accelerator Product Portfolios and Specifications

13.17.3 Cambrian?CN? Edge AI Accelerator Sales, Revenue, Price and Gross Margin (2021-2026)

13.17.4 Cambrian?CN? Main Business Overview

13.17.5 Cambrian?CN? Latest Developments

13.18 Mythic?US?

13.18.1 Mythic?US? Company Information

13.18.2 Mythic?US? Edge AI Accelerator Product Portfolios and Specifications

13.18.3 Mythic?US? Edge AI Accelerator Sales, Revenue, Price and Gross Margin (2021-2026)

13.18.4 Mythic?US? Main Business Overview

13.18.5 Mythic?US? Latest Developments

13.19 Axelera AI?NL?

13.19.1 Axelera AI?NL? Company Information

13.19.2 Axelera AI?NL? Edge AI Accelerator Product Portfolios and Specifications

13.19.3 Axelera AI?NL? Edge AI Accelerator Sales, Revenue, Price and Gross Margin (2021-2026)

13.19.4 Axelera AI?NL? Main Business Overview

13.19.5 Axelera AI?NL? Latest Developments

13.20 Toshiba Electronic Devices?JP?

13.20.1 Toshiba Electronic Devices?JP? Company Information

13.20.2 Toshiba Electronic Devices?JP? Edge AI Accelerator Product Portfolios and Specifications

13.20.3 Toshiba Electronic Devices?JP? Edge AI Accelerator Sales, Revenue, Price and Gross Margin (2021-2026)

13.20.4 Toshiba Electronic Devices?JP? Main Business Overview

13.20.5 Toshiba Electronic Devices?JP? Latest Developments

14 RESEARCH FINDINGS AND CONCLUSION

List Of Tables

LIST OF TABLES

Table 1. Edge AI Accelerator Annual Sales CAGR by Geographic Region (2021, 2025 & 2032) & (\$ millions)

Table 2. Edge AI Accelerator Annual Sales CAGR by Country/Region (2021, 2025 & 2032) & (\$ millions)

Table 3. Major Players of Dedicated Acceleration Chip

Table 4. Major Players of IP Core Integration

Table 5. Major Players of MCU-Level Acceleration

Table 6. Major Players of Heterogeneous Multi-Core

Table 7. Global Edge AI Accelerator Sales by Type (2021-2026) & (Million Units)

Table 8. Global Edge AI Accelerator Sales Market Share by Type (2021-2026)

Table 9. Global Edge AI Accelerator Revenue by Type (2021-2026) & (\$ million)

Table 10. Global Edge AI Accelerator Revenue Market Share by Type (2021-2026)

Table 11. Global Edge AI Accelerator Sale Price by Type (2021-2026) & (US\$/Unit)

Table 12. Major Players of 30W

Table 16. Global Edge AI Accelerator Sales by Power Consumption (2021-2026) & (Million Units)

Table 17. Global Edge AI Accelerator Sales Market Share by Power Consumption (2021-2026)

Table 18. Global Edge AI Accelerator Revenue by Power Consumption (2021-2026) & (\$ million)

Table 19. Global Edge AI Accelerator Revenue Market Share by Power Consumption (2021-2026)

Table 20. Global Edge AI Accelerator Sale Price by Power Consumption (2021-2026) & (US\$/Unit)

Table 21. Major Players of Direct Sales

Table 22. Major Players of Distribution

Table 23. Global Edge AI Accelerator Sales by Sales Channels (2021-2026) & (Million Units)

Table 24. Global Edge AI Accelerator Sales Market Share by Sales Channels (2021-2026)

Table 25. Global Edge AI Accelerator Revenue by Sales Channels (2021-2026) & (\$ million)

Table 26. Global Edge AI Accelerator Revenue Market Share by Sales Channels (2021-2026)

Table 27. Global Edge AI Accelerator Sale Price by Sales Channels (2021-2026) &

(US\$/Unit)

Table 28. Global Edge AI Accelerator Sale by Application (2021-2026) & (Million Units)

Table 29. Global Edge AI Accelerator Sale Market Share by Application (2021-2026)

Table 30. Global Edge AI Accelerator Revenue by Application (2021-2026) & (\$ million)

Table 31. Global Edge AI Accelerator Revenue Market Share by Application (2021-2026)

Table 32. Global Edge AI Accelerator Sale Price by Application (2021-2026) & (US\$/Unit)

Table 33. Global Edge AI Accelerator Sales by Company (2021-2026) & (Million Units)

Table 34. Global Edge AI Accelerator Sales Market Share by Company (2021-2026)

Table 35. Global Edge AI Accelerator Revenue by Company (2021-2026) & (\$ millions)

Table 36. Global Edge AI Accelerator Revenue Market Share by Company (2021-2026)

Table 37. Global Edge AI Accelerator Sale Price by Company (2021-2026) & (US\$/Unit)

Table 38. Key Manufacturers Edge AI Accelerator Producing Area Distribution and Sales Area

Table 39. Players Edge AI Accelerator Products Offered

Table 40. Edge AI Accelerator Concentration Ratio (CR3, CR5 and CR10) & (2024-2026)

Table 41. New Products and Potential Entrants

Table 42. Market M&A Activity & Strategy

Table 43. Global Edge AI Accelerator Sales by Geographic Region (2021-2026) & (Million Units)

Table 44. Global Edge AI Accelerator Sales Market Share Geographic Region (2021-2026)

Table 45. Global Edge AI Accelerator Revenue by Geographic Region (2021-2026) & (\$ millions)

Table 46. Global Edge AI Accelerator Revenue Market Share by Geographic Region (2021-2026)

Table 47. Global Edge AI Accelerator Sales by Country/Region (2021-2026) & (Million Units)

Table 48. Global Edge AI Accelerator Sales Market Share by Country/Region (2021-2026)

Table 49. Global Edge AI Accelerator Revenue by Country/Region (2021-2026) & (\$ millions)

Table 50. Global Edge AI Accelerator Revenue Market Share by Country/Region (2021-2026)

Table 51. Americas Edge AI Accelerator Sales by Country (2021-2026) & (Million Units)

Table 52. Americas Edge AI Accelerator Sales Market Share by Country (2021-2026)

Table 53. Americas Edge AI Accelerator Revenue by Country (2021-2026) & (\$ millions)

Table 54. Americas Edge AI Accelerator Sales by Type (2021-2026) & (Million Units)

Table 55. Americas Edge AI Accelerator Sales by Application (2021-2026) & (Million Units)

Table 56. APAC Edge AI Accelerator Sales by Region (2021-2026) & (Million Units)

Table 57. APAC Edge AI Accelerator Sales Market Share by Region (2021-2026)

Table 58. APAC Edge AI Accelerator Revenue by Region (2021-2026) & (\$ millions)

Table 59. APAC Edge AI Accelerator Sales by Type (2021-2026) & (Million Units)

Table 60. APAC Edge AI Accelerator Sales by Application (2021-2026) & (Million Units)

Table 61. Europe Edge AI Accelerator Sales by Country (2021-2026) & (Million Units)

Table 62. Europe Edge AI Accelerator Revenue by Country (2021-2026) & (\$ millions)

Table 63. Europe Edge AI Accelerator Sales by Type (2021-2026) & (Million Units)

Table 64. Europe Edge AI Accelerator Sales by Application (2021-2026) & (Million Units)

Table 65. Middle East & Africa Edge AI Accelerator Sales by Country (2021-2026) & (Million Units)

Table 66. Middle East & Africa Edge AI Accelerator Revenue Market Share by Country (2021-2026)

Table 67. Middle East & Africa Edge AI Accelerator Sales by Type (2021-2026) & (Million Units)

Table 68. Middle East & Africa Edge AI Accelerator Sales by Application (2021-2026) & (Million Units)

Table 69. Key Market Drivers & Growth Opportunities of Edge AI Accelerator

Table 70. Key Market Challenges & Risks of Edge AI Accelerator

Table 71. Key Industry Trends of Edge AI Accelerator

Table 72. Edge AI Accelerator Raw Material

Table 73. Key Suppliers of Raw Materials

Table 74. Edge AI Accelerator Distributors List

Table 75. Edge AI Accelerator Customer List

Table 76. Global Edge AI Accelerator Sales Forecast by Region (2027-2032) & (Million Units)

Table 77. Global Edge AI Accelerator Revenue Forecast by Region (2027-2032) & (\$ millions)

Table 78. Americas Edge AI Accelerator Sales Forecast by Country (2027-2032) & (Million Units)

Table 79. Americas Edge AI Accelerator Annual Revenue Forecast by Country (2027-2032) & (\$ millions)

Table 80. APAC Edge AI Accelerator Sales Forecast by Region (2027-2032) & (Million Units)

Table 81. APAC Edge AI Accelerator Annual Revenue Forecast by Region (2027-2032)

& (\$ millions)

Table 82. Europe Edge AI Accelerator Sales Forecast by Country (2027-2032) & (Million Units)

Table 83. Europe Edge AI Accelerator Revenue Forecast by Country (2027-2032) & (\$ millions)

Table 84. Middle East & Africa Edge AI Accelerator Sales Forecast by Country (2027-2032) & (Million Units)

Table 85. Middle East & Africa Edge AI Accelerator Revenue Forecast by Country (2027-2032) & (\$ millions)

Table 86. Global Edge AI Accelerator Sales Forecast by Type (2027-2032) & (Million Units)

Table 87. Global Edge AI Accelerator Revenue Forecast by Type (2027-2032) & (\$ millions)

Table 88. Global Edge AI Accelerator Sales Forecast by Application (2027-2032) & (Million Units)

Table 89. Global Edge AI Accelerator Revenue Forecast by Application (2027-2032) & (\$ millions)

Table 90. Qualcomm?US? Basic Information, Edge AI Accelerator Manufacturing Base, Sales Area and Its Competitors

Table 91. Qualcomm?US? Edge AI Accelerator Product Portfolios and Specifications

Table 92. Qualcomm?US? Edge AI Accelerator Sales (Million Units), Revenue (\$ Million), Price (US\$/Unit) and Gross Margin (2021-2026)

Table 93. Qualcomm?US? Main Business

Table 94. Qualcomm?US? Latest Developments

Table 95. NVIDIA?US? Basic Information, Edge AI Accelerator Manufacturing Base, Sales Area and Its Competitors

Table 96. NVIDIA?US? Edge AI Accelerator Product Portfolios and Specifications

Table 97. NVIDIA?US? Edge AI Accelerator Sales (Million Units), Revenue (\$ Million), Price (US\$/Unit) and Gross Margin (2021-2026)

Table 98. NVIDIA?US? Main Business

Table 99. NVIDIA?US? Latest Developments

Table 100. Intel?US? Basic Information, Edge AI Accelerator Manufacturing Base, Sales Area and Its Competitors

Table 101. Intel?US? Edge AI Accelerator Product Portfolios and Specifications

Table 102. Intel?US? Edge AI Accelerator Sales (Million Units), Revenue (\$ Million), Price (US\$/Unit) and Gross Margin (2021-2026)

Table 103. Intel?US? Main Business

Table 104. Intel?US? Latest Developments

Table 105. NXP?NL? Basic Information, Edge AI Accelerator Manufacturing Base,

Sales Area and Its Competitors

Table 106. NXP?NL? Edge AI Accelerator Product Portfolios and Specifications

Table 107. NXP?NL? Edge AI Accelerator Sales (Million Units), Revenue (\$ Million), Price (US\$/Unit) and Gross Margin (2021-2026)

Table 108. NXP?NL? Main Business

Table 109. NXP?NL? Latest Developments

Table 110. AMD?US? Basic Information, Edge AI Accelerator Manufacturing Base, Sales Area and Its Competitors

Table 111. AMD?US? Edge AI Accelerator Product Portfolios and Specifications

Table 112. AMD?US? Edge AI Accelerator Sales (Million Units), Revenue (\$ Million), Price (US\$/Unit) and Gross Margin (2021-2026)

Table 113. AMD?US? Main Business

Table 114. AMD?US? Latest Developments

Table 115. Horizon Robotics?CN? Basic Information, Edge AI Accelerator Manufacturing Base, Sales Area and Its Competitors

Table 116. Horizon Robotics?CN? Edge AI Accelerator Product Portfolios and Specifications

Table 117. Horizon Robotics?CN? Edge AI Accelerator Sales (Million Units), Revenue (\$ Million), Price (US\$/Unit) and Gross Margin (2021-2026)

Table 118. Horizon Robotics?CN? Main Business

Table 119. Horizon Robotics?CN? Latest Developments

Table 120. Renesas?JP? Basic Information, Edge AI Accelerator Manufacturing Base, Sales Area and Its Competitors

Table 121. Renesas?JP? Edge AI Accelerator Product Portfolios and Specifications

Table 122. Renesas?JP? Edge AI Accelerator Sales (Million Units), Revenue (\$ Million), Price (US\$/Unit) and Gross Margin (2021-2026)

Table 123. Renesas?JP? Main Business

Table 124. Renesas?JP? Latest Developments

Table 125. Synaptics?US? Basic Information, Edge AI Accelerator Manufacturing Base, Sales Area and Its Competitors

Table 126. Synaptics?US? Edge AI Accelerator Product Portfolios and Specifications

Table 127. Synaptics?US? Edge AI Accelerator Sales (Million Units), Revenue (\$ Million), Price (US\$/Unit) and Gross Margin (2021-2026)

Table 128. Synaptics?US? Main Business

Table 129. Synaptics?US? Latest Developments

Table 130. Ambarella?US? Basic Information, Edge AI Accelerator Manufacturing Base, Sales Area and Its Competitors

Table 131. Ambarella?US? Edge AI Accelerator Product Portfolios and Specifications

Table 132. Ambarella?US? Edge AI Accelerator Sales (Million Units), Revenue (\$

Million), Price (US\$/Unit) and Gross Margin (2021-2026)

Table 133. Ambarella?US? Main Business

Table 134. Ambarella?US? Latest Developments

Table 135. Rockchip Electronics?CN? Basic Information, Edge AI Accelerator Manufacturing Base, Sales Area and Its Competitors

Table 136. Rockchip Electronics?CN? Edge AI Accelerator Product Portfolios and Specifications

Table 137. Rockchip Electronics?CN? Edge AI Accelerator Sales (Million Units), Revenue (\$ Million), Price (US\$/Unit) and Gross Margin (2021-2026)

Table 138. Rockchip Electronics?CN? Main Business

Table 139. Rockchip Electronics?CN? Latest Developments

Table 140. Sony Semiconductor Solutions?JP? Basic Information, Edge AI Accelerator Manufacturing Base, Sales Area and Its Competitors

Table 141. Sony Semiconductor Solutions?JP? Edge AI Accelerator Product Portfolios and Specifications

Table 142. Sony Semiconductor Solutions?JP? Edge AI Accelerator Sales (Million Units), Revenue (\$ Million), Price (US\$/Unit) and Gross Margin (2021-2026)

Table 143. Sony Semiconductor Solutions?JP? Main Business

Table 144. Sony Semiconductor Solutions?JP? Latest Developments

Table 145. STMicroelectronics?NL? Basic Information, Edge AI Accelerator Manufacturing Base, Sales Area and Its Competitors

Table 146. STMicroelectronics?NL? Edge AI Accelerator Product Portfolios and Specifications

Table 147. STMicroelectronics?NL? Edge AI Accelerator Sales (Million Units), Revenue (\$ Million), Price (US\$/Unit) and Gross Margin (2021-2026)

Table 148. STMicroelectronics?NL? Main Business

Table 149. STMicroelectronics?NL? Latest Developments

Table 150. Black Sesame International Holding Limited?CN? Basic Information, Edge AI Accelerator Manufacturing Base, Sales Area and Its Competitors

Table 151. Black Sesame International Holding Limited?CN? Edge AI Accelerator Product Portfolios and Specifications

Table 152. Black Sesame International Holding Limited?CN? Edge AI Accelerator Sales (Million Units), Revenue (\$ Million), Price (US\$/Unit) and Gross Margin (2021-2026)

Table 153. Black Sesame International Holding Limited?CN? Main Business

Table 154. Black Sesame International Holding Limited?CN? Latest Developments

Table 155. Axera Semiconductor?CN? Basic Information, Edge AI Accelerator Manufacturing Base, Sales Area and Its Competitors

Table 156. Axera Semiconductor?CN? Edge AI Accelerator Product Portfolios and Specifications

- Table 157. Axera Semiconductor?CN? Edge AI Accelerator Sales (Million Units), Revenue (\$ Million), Price (US\$/Unit) and Gross Margin (2021-2026)
- Table 158. Axera Semiconductor?CN? Main Business
- Table 159. Axera Semiconductor?CN? Latest Developments
- Table 160. Socionext?JP? Basic Information, Edge AI Accelerator Manufacturing Base, Sales Area and Its Competitors
- Table 161. Socionext?JP? Edge AI Accelerator Product Portfolios and Specifications
- Table 162. Socionext?JP? Edge AI Accelerator Sales (Million Units), Revenue (\$ Million), Price (US\$/Unit) and Gross Margin (2021-2026)
- Table 163. Socionext?JP? Main Business
- Table 164. Socionext?JP? Latest Developments
- Table 165. MemryX?US? Basic Information, Edge AI Accelerator Manufacturing Base, Sales Area and Its Competitors
- Table 166. MemryX?US? Edge AI Accelerator Product Portfolios and Specifications
- Table 167. MemryX?US? Edge AI Accelerator Sales (Million Units), Revenue (\$ Million), Price (US\$/Unit) and Gross Margin (2021-2026)
- Table 168. MemryX?US? Main Business
- Table 169. MemryX?US? Latest Developments
- Table 170. Cambrian?CN? Basic Information, Edge AI Accelerator Manufacturing Base, Sales Area and Its Competitors
- Table 171. Cambrian?CN? Edge AI Accelerator Product Portfolios and Specifications
- Table 172. Cambrian?CN? Edge AI Accelerator Sales (Million Units), Revenue (\$ Million), Price (US\$/Unit) and Gross Margin (2021-2026)
- Table 173. Cambrian?CN? Main Business
- Table 174. Cambrian?CN? Latest Developments
- Table 175. Mythic?US? Basic Information, Edge AI Accelerator Manufacturing Base, Sales Area and Its Competitors
- Table 176. Mythic?US? Edge AI Accelerator Product Portfolios and Specifications
- Table 177. Mythic?US? Edge AI Accelerator Sales (Million Units), Revenue (\$ Million), Price (US\$/Unit) and Gross Margin (2021-2026)
- Table 178. Mythic?US? Main Business
- Table 179. Mythic?US? Latest Developments
- Table 180. Axelera AI?NL? Basic Information, Edge AI Accelerator Manufacturing Base, Sales Area and Its Competitors
- Table 181. Axelera AI?NL? Edge AI Accelerator Product Portfolios and Specifications
- Table 182. Axelera AI?NL? Edge AI Accelerator Sales (Million Units), Revenue (\$ Million), Price (US\$/Unit) and Gross Margin (2021-2026)
- Table 183. Axelera AI?NL? Main Business
- Table 184. Axelera AI?NL? Latest Developments

Table 185. Toshiba Electronic Devices?JP? Basic Information, Edge AI Accelerator Manufacturing Base, Sales Area and Its Competitors

Table 186. Toshiba Electronic Devices?JP? Edge AI Accelerator Product Portfolios and Specifications

Table 187. Toshiba Electronic Devices?JP? Edge AI Accelerator Sales (Million Units), Revenue (\$ Million), Price (US\$/Unit) and Gross Margin (2021-2026)

Table 188. Toshiba Electronic Devices?JP? Main Business

Table 189. Toshiba Electronic Devices?JP? Latest Developments

List Of Figures

LIST OF FIGURES

Figure 1. Picture of Edge AI Accelerator

Figure 2. Edge AI Accelerator Report Years Considered

Figure 3. Research Objectives

Figure 4. Research Methodology

Figure 5. Research Process and Data Source

Figure 6. Global Edge AI Accelerator Sales Growth Rate 2021-2032 (Million Units)

Figure 7. Global Edge AI Accelerator Revenue Growth Rate 2021-2032 (\$ millions)

Figure 8. Edge AI Accelerator Sales by Geographic Region (2021, 2025 & 2032) & (\$ millions)

Figure 9. Edge AI Accelerator Sales Market Share by Country/Region (2025)

Figure 10. Edge AI Accelerator Sales Market Share by Country/Region (2021, 2025 & 2032)

Figure 11. Product Picture of Dedicated Acceleration Chip

Figure 12. Product Picture of IP Core Integration

Figure 13. Product Picture of MCU-Level Acceleration

Figure 14. Product Picture of Heterogeneous Multi-Core

Figure 15. Global Edge AI Accelerator Sales Market Share by Type in 2026

Figure 16. Global Edge AI Accelerator Revenue Market Share by Type (2021-2026)

Figure 17. Product Picture of 30W

Figure 21. Global Edge AI Accelerator Sales Market Share by Power Consumption in 2026

Figure 22. Global Edge AI Accelerator Revenue Market Share by Power Consumption (2021-2026)

Figure 23. Product Picture of Direct Sales

Figure 24. Product Picture of Distribution

Figure 25. Global Edge AI Accelerator Sales Market Share by Sales Channels in 2026

Figure 26. Global Edge AI Accelerator Revenue Market Share by Sales Channels (2021-2026)

Figure 27. Edge AI Accelerator Consumed in Smart Security / Video Analytics

Figure 28. Global Edge AI Accelerator Market: Smart Security / Video Analytics (2021-2026) & (Million Units)

Figure 29. Edge AI Accelerator Consumed in Industrial Vision / AOI / Robotics

Figure 30. Global Edge AI Accelerator Market: Industrial Vision / AOI / Robotics (2021-2026) & (Million Units)

Figure 31. Edge AI Accelerator Consumed in Automotive ADAS / In-Vehicle Sensing

Figure 32. Global Edge AI Accelerator Market: Automotive ADAS / In-Vehicle Sensing (2021-2026) & (Million Units)

Figure 33. Edge AI Accelerator Consumed in Retail / Logistics / Smart Cities

Figure 34. Global Edge AI Accelerator Market: Retail / Logistics / Smart Cities (2021-2026) & (Million Units)

Figure 35. Edge AI Accelerator Consumed in Healthcare / Professional Devices

Figure 36. Global Edge AI Accelerator Market: Healthcare / Professional Devices (2021-2026) & (Million Units)

Figure 37. Edge AI Accelerator Consumed in Audio / Sensing / Multimodal Edge Devices

Figure 38. Global Edge AI Accelerator Market: Audio / Sensing / Multimodal Edge Devices (2021-2026) & (Million Units)

Figure 39. Global Edge AI Accelerator Sale Market Share by Application (2025)

Figure 40. Global Edge AI Accelerator Revenue Market Share by Application in 2025

Figure 41. Edge AI Accelerator Sales by Company in 2025 (Million Units)

Figure 42. Global Edge AI Accelerator Sales Market Share by Company in 2025

Figure 43. Edge AI Accelerator Revenue by Company in 2025 (\$ millions)

Figure 44. Global Edge AI Accelerator Revenue Market Share by Company in 2025

Figure 45. Global Edge AI Accelerator Sales Market Share by Geographic Region (2021-2026)

Figure 46. Global Edge AI Accelerator Revenue Market Share by Geographic Region in 2025

Figure 47. Americas Edge AI Accelerator Sales 2021-2026 (Million Units)

Figure 48. Americas Edge AI Accelerator Revenue 2021-2026 (\$ millions)

Figure 49. APAC Edge AI Accelerator Sales 2021-2026 (Million Units)

Figure 50. APAC Edge AI Accelerator Revenue 2021-2026 (\$ millions)

Figure 51. Europe Edge AI Accelerator Sales 2021-2026 (Million Units)

Figure 52. Europe Edge AI Accelerator Revenue 2021-2026 (\$ millions)

Figure 53. Middle East & Africa Edge AI Accelerator Sales 2021-2026 (Million Units)

Figure 54. Middle East & Africa Edge AI Accelerator Revenue 2021-2026 (\$ millions)

Figure 55. Americas Edge AI Accelerator Sales Market Share by Country in 2025

Figure 56. Americas Edge AI Accelerator Revenue Market Share by Country (2021-2026)

Figure 57. Americas Edge AI Accelerator Sales Market Share by Type (2021-2026)

Figure 58. Americas Edge AI Accelerator Sales Market Share by Application (2021-2026)

Figure 59. United States Edge AI Accelerator Revenue Growth 2021-2026 (\$ millions)

Figure 60. Canada Edge AI Accelerator Revenue Growth 2021-2026 (\$ millions)

Figure 61. Mexico Edge AI Accelerator Revenue Growth 2021-2026 (\$ millions)

- Figure 62. Brazil Edge AI Accelerator Revenue Growth 2021-2026 (\$ millions)
- Figure 63. APAC Edge AI Accelerator Sales Market Share by Region in 2025
- Figure 64. APAC Edge AI Accelerator Revenue Market Share by Region (2021-2026)
- Figure 65. APAC Edge AI Accelerator Sales Market Share by Type (2021-2026)
- Figure 66. APAC Edge AI Accelerator Sales Market Share by Application (2021-2026)
- Figure 67. China Edge AI Accelerator Revenue Growth 2021-2026 (\$ millions)
- Figure 68. Japan Edge AI Accelerator Revenue Growth 2021-2026 (\$ millions)
- Figure 69. South Korea Edge AI Accelerator Revenue Growth 2021-2026 (\$ millions)
- Figure 70. Southeast Asia Edge AI Accelerator Revenue Growth 2021-2026 (\$ millions)
- Figure 71. India Edge AI Accelerator Revenue Growth 2021-2026 (\$ millions)
- Figure 72. Australia Edge AI Accelerator Revenue Growth 2021-2026 (\$ millions)
- Figure 73. China Taiwan Edge AI Accelerator Revenue Growth 2021-2026 (\$ millions)
- Figure 74. Europe Edge AI Accelerator Sales Market Share by Country in 2025
- Figure 75. Europe Edge AI Accelerator Revenue Market Share by Country (2021-2026)
- Figure 76. Europe Edge AI Accelerator Sales Market Share by Type (2021-2026)
- Figure 77. Europe Edge AI Accelerator Sales Market Share by Application (2021-2026)
- Figure 78. Germany Edge AI Accelerator Revenue Growth 2021-2026 (\$ millions)
- Figure 79. France Edge AI Accelerator Revenue Growth 2021-2026 (\$ millions)
- Figure 80. UK Edge AI Accelerator Revenue Growth 2021-2026 (\$ millions)
- Figure 81. Italy Edge AI Accelerator Revenue Growth 2021-2026 (\$ millions)
- Figure 82. Russia Edge AI Accelerator Revenue Growth 2021-2026 (\$ millions)
- Figure 83. Middle East & Africa Edge AI Accelerator Sales Market Share by Country (2021-2026)
- Figure 84. Middle East & Africa Edge AI Accelerator Sales Market Share by Type (2021-2026)
- Figure 85. Middle East & Africa Edge AI Accelerator Sales Market Share by Application (2021-2026)
- Figure 86. Egypt Edge AI Accelerator Revenue Growth 2021-2026 (\$ millions)
- Figure 87. South Africa Edge AI Accelerator Revenue Growth 2021-2026 (\$ millions)
- Figure 88. Israel Edge AI Accelerator Revenue Growth 2021-2026 (\$ millions)
- Figure 89. Turkey Edge AI Accelerator Revenue Growth 2021-2026 (\$ millions)
- Figure 90. GCC Countries Edge AI Accelerator Revenue Growth 2021-2026 (\$ millions)
- Figure 91. Manufacturing Cost Structure Analysis of Edge AI Accelerator in 2026
- Figure 92. Manufacturing Process Analysis of Edge AI Accelerator
- Figure 93. Industry Chain Structure of Edge AI Accelerator
- Figure 94. Channels of Distribution
- Figure 95. Global Edge AI Accelerator Sales Market Forecast by Region (2027-2032)
- Figure 96. Global Edge AI Accelerator Revenue Market Share Forecast by Region (2027-2032)

Figure 97. Global Edge AI Accelerator Sales Market Share Forecast by Type
(2027-2032)

Figure 98. Global Edge AI Accelerator Revenue Market Share Forecast by Type
(2027-2032)

Figure 99. Global Edge AI Accelerator Sales Market Share Forecast by Application
(2027-2032)

Figure 100. Global Edge AI Accelerator Revenue Market Share Forecast by Application
(2027-2032)

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