

Global EDA Tools for IC Design Market Growth (Status and Outlook) 2024-2030

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Abstracts

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Electronic Design Automation (EDA) is primarily a software business. Very sophisticated and complex software programs function primarily in one of three ways to assist with the design and manufacture of chips:

Simulation tools take a description of a proposed circuit and predict its behavior before is it implemented.

Design tools take a description of a proposed circuit function and assemble the collection of circuit elements that implement that function. This is both a logical process (assemble and connect the circuit elements) and a physical process (create the interconnected geometric shapes that will implement the circuit during manufacturing). These tools are delivered as a combination of fully automated and interactively guided capabilities.

Verification tools examine either the logical or physical representation of the chip to determine if the resultant design is connected correctly and will deliver the required performance.

The global EDA Tools for IC Design market size is projected to grow from US\$ 5646 million in 2024 to US\$ 8207 million in 2030; it is expected to grow at a CAGR of 6.4% from 2024 to 2030.

LPI (LP Information)' newest research report, the "EDA Tools for IC Design Industry Forecast" looks at past sales and reviews total world EDA Tools for IC Design sales in



2022, providing a comprehensive analysis by region and market sector of projected EDA Tools for IC Design sales for 2023 through 2029. With EDA Tools for IC Design sales broken down by region, market sector and sub-sector, this report provides a detailed analysis in US\$ millions of the world EDA Tools for IC Design industry.

This Insight Report provides a comprehensive analysis of the global EDA Tools for IC Design landscape and highlights key trends related to product segmentation, company formation, revenue, and market share, latest development, and M&A activity. This report also analyses the strategies of leading global companies with a focus on EDA Tools for IC Design portfolios and capabilities, market entry strategies, market positions, and geographic footprints, to better understand these firms' unique position in an accelerating global EDA Tools for IC Design market.

This Insight Report evaluates the key market trends, drivers, and affecting factors shaping the global outlook for EDA Tools for IC Design and breaks down the forecast by Type, by Application, geography, and market size to highlight emerging pockets of opportunity. With a transparent methodology based on hundreds of bottom-up qualitative and quantitative market inputs, this study forecast offers a highly nuanced view of the current state and future trajectory in the global EDA Tools for IC Design.

United States market for EDA Tools for IC Design is estimated to increase from US\$ million in 2023 to US\$ million by 2030, at a CAGR of % from 2024 through 2030.

China market for EDA Tools for IC Design is estimated to increase from US\$ million in 2023 to US\$ million by 2030, at a CAGR of % from 2024 through 2030.

Europe market for EDA Tools for IC Design is estimated to increase from US\$ million in 2023 to US\$ million by 2030, at a CAGR of % from 2024 through 2030.

Global key EDA Tools for IC Design players cover Synopsys (Ansys), Cadence, Siemens EDA, Silvaco, Concept Engineering, etc. In terms of revenue, the global two largest companies occupied for a share nearly % in 2023.

This report presents a comprehensive overview, market shares, and growth opportunities of EDA Tools for IC Design market by product type, application, key players and key regions and countries.

Segmentation by Type:



Digital	IC Frontend (FE) Design	
Digital	IC Backend (BE) Design	
Analog	g IC Design	
Segmentation by Application:		
Autom	otive	
IT and	Telecommunications	
Industi	rial Automation	
Consu	mer Electronics	
Health	care Devices	
Others		
This report als	so splits the market by region:	
Americ	cas	
	United States	
	Canada	
	Mexico	
	Brazil	
APAC		
	China	
	Japan	



Korea

	Notea	
	Southeast Asia	
	India	
	Australia	
Europe	9	
	Germany	
	France	
	UK	
	Italy	
	Russia	
Middle East & Africa		
	Egypt	
	South Africa	
	Israel	
	Turkey	
	GCC Countries	
Segmentation by Type:		

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Digital IC Frontend (FE) Design

Digital IC Backend (BE) Design



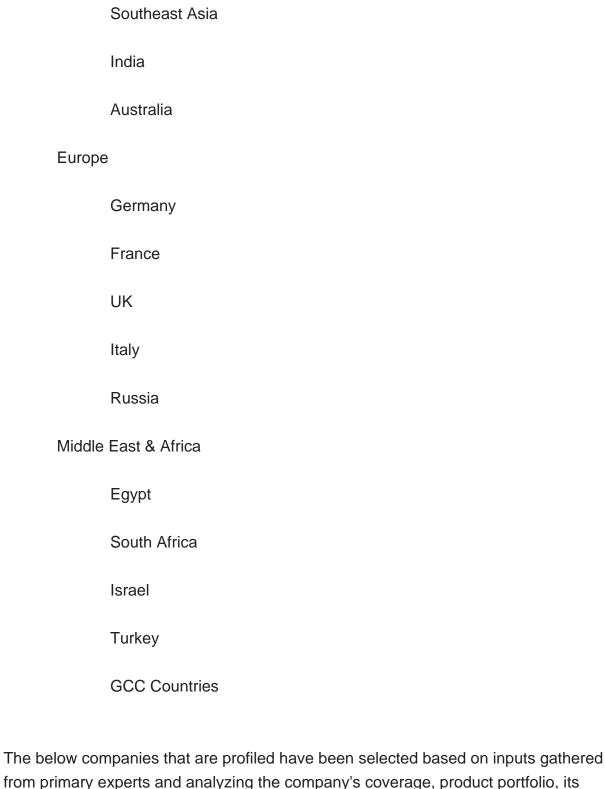
Analog IC Design Segmentation by Application: Automotive IT and Telecommunications **Industrial Automation Consumer Electronics Healthcare Devices** Others This report also splits the market by region: Americas **United States** Canada Mexico Brazil **APAC**

China

Japan

Korea





from primary experts and analyzing the company's coverage, product portfolio, its market penetration.

Synopsys (Ansys)

Cadence



Siemens EDA	
Silvaco	
Concept Engineering	
MunEDA	
Defacto Technologies	
Intento Design	
Agnisys	
AMIQ EDA	
Breker	
Infinisim	
Arteris (Semifore, Inc.)	
Excellicon	
Lorentz Solution	
Empyrean Technology	
Xpeedic Technology	
Semitronix	
Faraday Dynamics, Ltd.	
MircoScape Technology Co., Ltd	
Primarius Technologies	
Arcas-tech Co., Ltd.	



Shanghai UniVista Industrial Software Group	
Shanghai LEDA Technology	
Phlexing Technology	
Robei	
HyperSilicon Co.,Ltd	
S2C Limited.	
X-EPIC	
Huaxin Jushu	
ValiantSec	



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