

# Global Digital ASIC Design Service Market Growth (Status and Outlook) 2026-2032

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## Abstracts

The global Digital ASIC Design Service market size is predicted to grow from US\$ 4720 million in 2025 to US\$ 8271 million in 2032; it is expected to grow at a CAGR of 8.5% from 2026 to 2032.

The "ASIC" in digital ASIC design stands for Application-Specific Integrated Circuits. This term refers to circuits that are designed to perform a specific function. Digital ASIC design is the process of designing an ASIC chip that uses digital logic components, as opposed to analog blue colored computer chip. Digital ASIC chips were developed to improve technologies across multiple industries. They expand design functionality and enable high-end technology companies to design products with more precise specifications.

Digital ASIC design services are witnessing distinct trends, opportunities, and challenges: key trends include the integration of AI-driven EDA tools (which shorten design cycles by over 50% via intelligent layout optimization and defect prediction), the proliferation of Chiplet and "ASIC+SiP" modular integration (enabling system miniaturization to 1.4% of traditional PCB size and 15-25% cost reduction), and the shift toward advanced 3nm/2nm processes alongside demand for low-power designs for edge computing. Opportunities abound in verticals like AI/ML (high-performance inference ASICs), automotive electronics (ISO 26262-compliant chips for autonomous driving), and IoT (customized low-power modules), while policy support for domestic semiconductor autonomy and the outsourcing needs of SMEs (lacking in-house design capabilities) further expand the market. However, the industry faces notable challenges: escalating design complexity in advanced processes (addressing quantum effects, thermal management, and signal integrity at 3nm nodes), reliance on monopolized EDA tools with high licensing costs (up to \$10,000 per workstation monthly) and limited

access to advanced process PDKs, exorbitant upfront investments (3nm tapeout costs \$3.5 billion) and re-spin risks, a shortage of cross-disciplinary talent skilled in AI-EDA integration and heterogeneous design, and fierce competition from IDMs and large Fabless firms offering integrated solutions.

LPI (LP Information)' newest research report, the "Digital ASIC Design Service Industry Forecast" looks at past sales and reviews total world Digital ASIC Design Service sales in 2025, providing a comprehensive analysis by region and market sector of projected Digital ASIC Design Service sales for 2026 through 2032. With Digital ASIC Design Service sales broken down by region, market sector and sub-sector, this report provides a detailed analysis in US\$ millions of the world Digital ASIC Design Service industry.

This Insight Report provides a comprehensive analysis of the global Digital ASIC Design Service landscape and highlights key trends related to product segmentation, company formation, revenue, and market share, latest development, and M&A activity. This report also analyses the strategies of leading global companies with a focus on Digital ASIC Design Service portfolios and capabilities, market entry strategies, market positions, and geographic footprints, to better understand these firms' unique position in an accelerating global Digital ASIC Design Service market.

This Insight Report evaluates the key market trends, drivers, and affecting factors shaping the global outlook for Digital ASIC Design Service and breaks down the forecast by Type, by Application, geography, and market size to highlight emerging pockets of opportunity. With a transparent methodology based on hundreds of bottom-up qualitative and quantitative market inputs, this study forecast offers a highly nuanced view of the current state and future trajectory in the global Digital ASIC Design Service.

This report presents a comprehensive overview, market shares, and growth opportunities of Digital ASIC Design Service market by product type, application, key players and key regions and countries.

Segmentation by Type:

Full-Custom Design Service

Full-Custom Design Service

Segmentation by Application:

Consumer Electronics

Automotive

Medical Equipment

Industrial

Networking and Telecommunications

Other

This report also splits the market by region:

Americas

United States

Canada

Mexico

Brazil

APAC

China

Japan

Korea

Southeast Asia

India

Australia

## Europe

Germany

France

UK

Italy

Russia

## Middle East & Africa

Egypt

South Africa

Israel

Turkey

GCC Countries

The below companies that are profiled have been selected based on inputs gathered from primary experts and analyzing the company's coverage, product portfolio, its market penetration.

ASIC North

CoreHW

eInfochips

TES Electronic

Comport Data

Emtech

IC'Alps SAS

FACET

MICROIP Inc

DNP LSI Design Co., Ltd

Avnet, Inc

EnSilica

Faraday

Intera Group

Device Engineering

Semify

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