

Global CPU Instruction Set Architecture (ISA) Market Growth (Status and Outlook) 2026-2032

<https://marketpublishers.com/r/G752B7A8C827EN.html>

Date: October 2026

Pages: 135

Price: US\$ 3,660.00 (Single User License)

ID: G752B7A8C827EN

Abstracts

The global CPU Instruction Set Architecture (ISA) market size is predicted to grow from US\$ 4745 million in 2025 to US\$ 8207 million in 2032; it is expected to grow at a CAGR of 8.1% from 2026 to 2032.

CPU Instruction Set Architecture (ISA) and Processor IP refer to the architectural specifications that define the software-visible behavior of a processor and the licensable processor cores, architecture licenses, tools, verification environments, and ecosystem support built around those specifications. This market focuses on commercial CPU architecture licensing, processor core IP, architecture support services, and attributable CPU-core royalty revenue. Typical products and models include Arm architecture and CPU IP, RISC-V standard-based processor IP, configurable processor cores, embedded CPU IP, real-time processor IP, high-performance CPU IP, and regional or proprietary ISA ecosystems. The market scope emphasizes architecture standards, RTL or synthesizable IP, soft cores, architecture licenses, software toolchains, validation packages, and long-term ecosystem support used by semiconductor companies to build SoCs for mobile, automotive, IoT, edge AI, data center, storage, networking, and industrial applications.

Key Findings

Arm leads commercial ISA monetization

RISC-V drives open-standard growth

Processor IP demand shifts to customization

Automotive and AIoT accelerate adoption

x86 value remains platform-centered

China expands regional ISA ecosystems

Market Trends

The CPU ISA and Processor IP market is moving from fixed processor-core licensing toward configurable, extensible, and ecosystem-backed processor platforms. Customers increasingly require not only a CPU core, but also toolchains, verification suites, safety packages, reference platforms, compiler support, and long-term software compatibility. Arm continues to monetize a mature licensing and royalty model, while RISC-V is expanding through open-standard adoption and commercial IP implementations. The market is also shifting toward domain-specific customization, where customers use custom instructions, vector extensions, security features, and tightly integrated processor subsystems to optimize AI, automotive, storage, networking, and edge workloads.

Market Dynamics

Drivers

Demand is driven by growth in AIoT, automotive electronics, edge AI, smart sensors, storage controllers, security processors, industrial SoCs, and customized data-center silicon. Chip designers increasingly want processor architectures that reduce time-to-market while allowing differentiation in power, performance, safety, and software compatibility. Regional supply-chain autonomy is also strengthening interest in RISC-V and domestic ISA ecosystems.

Restraints

Market expansion is constrained by software ecosystem maturity, verification complexity, safety certification requirements, and the high cost of building compiler, operating-system, debug, and middleware support. Open ISA availability does not automatically create commercial value; vendors still need validated IP, development tools, customer support, and a long-term roadmap. For proprietary architectures, licensing cost and vendor dependence remain concerns for some customers.

Opportunities

The strongest opportunities are in configurable RISC-V IP, automotive functional-safety cores, AI edge control processors, high-performance RISC-V CPU IP, secure processors, storage and networking control cores, and regionally sovereign computing ecosystems. As SoC architectures become more heterogeneous, processor IP suppliers that can provide complete subsystems, verified extensions, and software stacks can capture more value than vendors selling standalone cores.

Challenges

The industry faces challenges from blurred boundaries between CPU, DSP, NPU, and accelerator IP; uneven RISC-V software maturity across applications; pricing pressure in low-end embedded cores; and uncertainty in private IP vendors' long-term customer traction. Proprietary ISA suppliers must maintain ecosystem consistency while responding to open-standard competition, and open-standard vendors must avoid software fragmentation caused by excessive custom extensions.

Industry Chain Analysis

The upstream layer includes ISA specifications, microarchitecture research, compiler toolchains, RTL design, verification IP, simulation models, debug tools, operating-system enablement, safety documentation, and reference platforms. The most value-sensitive elements are software compatibility, verification maturity, power-performance-area optimization, security features, functional-safety support, and long-term roadmap stability. ISA governance organizations shape the standards layer, while commercial processor IP vendors turn those specifications into silicon-ready implementations.

The midstream consists of CPU IP licensors, configurable processor suppliers, RISC-V IP vendors, architecture license providers, and processor subsystem companies. Downstream customers include fabless semiconductor companies, IDMs, automotive chip suppliers, hyperscalers, AI accelerator developers, storage controller vendors, MCU suppliers, industrial SoC companies, and regional sovereign computing programs. Value is captured through license fees, royalties, support services, customization, software ecosystem enablement, and reference designs.

Segment Insights

By architecture family, Arm remains the largest commercial monetization platform,

while RISC-V is the fastest-growing open-standard ecosystem. x86 remains highly valuable in PCs and servers, but its market value is mostly embedded in processor platforms rather than broad external ISA licensing. Power and LoongArch represent smaller but strategically relevant ecosystems in enterprise and regional computing. By business model, royalty-based and architecture licensing models generate high long-term value, while RISC-V commercial vendors increasingly monetize through configurable processor IP, subsystem licensing, and customization support.

By processor class, microcontroller and embedded processor IP provide broad unit coverage, while application processor and server-class IP represent higher-value but more difficult markets. Automotive, AI edge, security, and storage/networking control cores are attractive because they require more verification, customization, safety support, and long product lifecycles. Configurable and extensible processor IP is gaining share as customers seek workload-specific differentiation without building a CPU ecosystem from scratch.

Downstream Market Opportunities

The strongest downstream opportunities are in automotive domain controllers, AI accelerators requiring control processors, edge AI SoCs, secure IoT devices, storage controllers, smart sensors, industrial control chips, and customized cloud silicon. Customers increasingly evaluate processor IP by ecosystem readiness, verification evidence, compiler support, safety collateral, integration effort, and roadmap continuity. This creates opportunities for suppliers that can deliver not just cores, but complete processor platforms with software and validation support.

Regional Insights

North America is the largest center for commercial processor IP and architecture ecosystems, with major EDA/IP companies, x86 ecosystem holders, RISC-V startups, and AI processor IP suppliers. Europe is anchored by Arm and supported by specialized RISC-V and automotive-oriented IP vendors. China is developing a regional ecosystem around Arm China, LoongArch, RISC-V IP, and domestic processor design programs. Taiwan is important through Andes and the broader embedded semiconductor ecosystem. Japan and South Korea are more significant as customers and SoC implementers than as global CPU ISA/IP licensors.

Competitive Landscape Analysis

The competitive landscape is layered. Arm leads in commercial revenue, licensing maturity, and ecosystem breadth. RISC-V International leads the open-standard layer, while SiFive, Andes, Codaip, Synopsys ARC-V, GF/MIPS, Nuclei, Ventana, and emerging vendors compete to commercialize RISC-V implementations. Intel and AMD maintain x86's platform relevance through software compatibility and hardware ecosystem consistency, while regional architectures such as LoongArch serve sovereignty and localization needs. Future competition will depend less on ISA ownership alone and more on the ability to provide silicon-ready IP, software enablement, verification, safety support, customization, and long-term ecosystem stability.

Report Scope

LPI (LP Information)' newest research report, the "CPU Instruction Set Architecture (ISA) Industry Forecast" looks at past sales and reviews total world CPU Instruction Set Architecture (ISA) sales in 2025, providing a comprehensive analysis by region and market sector of projected CPU Instruction Set Architecture (ISA) sales for 2026 through 2032. With CPU Instruction Set Architecture (ISA) sales broken down by region, market sector and sub-sector, this report provides a detailed analysis in US\$ millions of the world CPU Instruction Set Architecture (ISA) industry.

This Insight Report provides a comprehensive analysis of the global CPU Instruction Set Architecture (ISA) landscape and highlights key trends related to product segmentation, company formation, revenue, and market share, latest development, and M&A activity. This report also analyses the strategies of leading global companies with a focus on CPU Instruction Set Architecture (ISA) portfolios and capabilities, market entry strategies, market positions, and geographic footprints, to better understand these firms' unique position in an accelerating global CPU Instruction Set Architecture (ISA) market.

This Insight Report evaluates the key market trends, drivers, and affecting factors shaping the global outlook for CPU Instruction Set Architecture (ISA) and breaks down the forecast by Architecture Family, by Application, geography, and market size to highlight emerging pockets of opportunity. With a transparent methodology based on hundreds of bottom-up qualitative and quantitative market inputs, this study forecast offers a highly nuanced view of the current state and future trajectory in the global CPU Instruction Set Architecture (ISA).

This report presents a comprehensive overview, market shares, and growth

opportunities of CPU Instruction Set Architecture (ISA) market by product type, application, key players and key regions and countries.

Segmentation by Architecture Family:

Arm Architecture

x86 Architecture

RISC-V Architecture

Power Architecture

Segmentation by Business Model:

Architecture Licensing

Processor Core IP Licensing

Royalty-based Licensing

Open Standard Governance

Segmentation by Processor Class:

Application Processor IP

Microcontroller Processor IP

Real-time Processor IP

DSP-like / Signal Processor IP

High-performance / Server-class IP

Segmentation by Application:

Mobile and Consumer SoCs

Automotive Electronics

IoT and Embedded Control

Data Center and Cloud

This report also splits the market by region:

Americas

United States

Canada

Mexico

Brazil

APAC

China

Japan

Korea

Southeast Asia

India

Australia

Europe

Germany

France

UK

Italy

Russia

Middle East & Africa

Egypt

South Africa

Israel

Turkey

GCC Countries

The below companies that are profiled have been selected based on inputs gathered from primary experts and analyzing the company's coverage, product portfolio, its market penetration.

Arm Holdings plc

RISC-V International

Synopsys, Inc.

Cadence Design Systems, Inc.

SiFive, Inc.

Andes Technology Corporation

Intel Corporation

Advanced Micro Devices, Inc.

Codasip

GlobalFoundries Inc.

Nuclei System Technology

Arm China

IBM Corporation

Loongson Technology Corporation Limited

Ventana Micro Systems

CEVA, Inc.

Contents

1 SCOPE OF THE REPORT

- 1.1 Market Introduction
- 1.2 Years Considered
- 1.3 Research Objectives
- 1.4 Market Research Methodology
- 1.5 Research Process and Data Source
- 1.6 Economic Indicators
- 1.7 Currency Considered
- 1.8 Market Estimation Caveats

2 EXECUTIVE SUMMARY

2.1 World Market Overview

- 2.1.1 Global CPU Instruction Set Architecture (ISA) Market Size (2021-2032)
- 2.1.2 CPU Instruction Set Architecture (ISA) Market Size CAGR by Region (2021 VS 2025 VS 2032)
- 2.1.3 World Current & Future Analysis for CPU Instruction Set Architecture (ISA) by Country/Region (2021, 2025 & 2032)

2.2 CPU Instruction Set Architecture (ISA) Segment by Architecture Family

- 2.2.1 Arm Architecture
- 2.2.2 x86 Architecture
- 2.2.3 RISC-V Architecture
- 2.2.4 Power Architecture
- 2.2.5 CPU Instruction Set Architecture (ISA) Market Size by Architecture Family
 - 2.2.5.1 CPU Instruction Set Architecture (ISA) Market Size CAGR by Architecture Family (2021 VS 2025 VS 2032)
 - 2.2.5.2 Global CPU Instruction Set Architecture (ISA) Market Size Market Share by Architecture Family (2021-2026)

2.3 CPU Instruction Set Architecture (ISA) Segment by Business Model

- 2.3.1 Architecture Licensing
- 2.3.2 Processor Core IP Licensing
- 2.3.3 Royalty-based Licensing
- 2.3.4 Open Standard Governance
- 2.3.5 CPU Instruction Set Architecture (ISA) Market Size by Business Model
 - 2.3.5.1 CPU Instruction Set Architecture (ISA) Market Size CAGR by Business Model (2021 VS 2025 VS 2032)

2.3.5.2 Global CPU Instruction Set Architecture (ISA) Market Size Market Share by Business Model (2021-2026)

2.4 CPU Instruction Set Architecture (ISA) Segment by Processor Class

2.4.1 Application Processor IP

2.4.2 Microcontroller Processor IP

2.4.3 Real-time Processor IP

2.4.4 DSP-like / Signal Processor IP

2.4.5 High-performance / Server-class IP

2.4.6 CPU Instruction Set Architecture (ISA) Market Size by Processor Class

2.4.6.1 CPU Instruction Set Architecture (ISA) Market Size CAGR by Processor Class (2021 VS 2025 VS 2032)

2.4.6.2 Global CPU Instruction Set Architecture (ISA) Market Size Market Share by Processor Class (2021-2026)

2.5 CPU Instruction Set Architecture (ISA) Segment by Application

2.5.1 Mobile and Consumer SoCs

2.5.2 Automotive Electronics

2.5.3 IoT and Embedded Control

2.5.4 Data Center and Cloud

2.5.5 CPU Instruction Set Architecture (ISA) Market Size by Application

2.5.5.1 CPU Instruction Set Architecture (ISA) Market Size CAGR by Application (2021 VS 2025 VS 2032)

2.5.5.2 Global CPU Instruction Set Architecture (ISA) Market Size Market Share by Application (2021-2026)

3 CPU INSTRUCTION SET ARCHITECTURE (ISA) MARKET SIZE BY PLAYER

3.1 CPU Instruction Set Architecture (ISA) Market Size Market Share by Player

3.1.1 Global CPU Instruction Set Architecture (ISA) Revenue by Player (2021-2026)

3.1.2 Global CPU Instruction Set Architecture (ISA) Revenue Market Share by Player (2021-2026)

3.2 Global CPU Instruction Set Architecture (ISA) Key Players Head office and Products Offered

3.3 Market Concentration Rate Analysis

3.3.1 Competition Landscape Analysis

3.3.2 Concentration Ratio (CR3, CR5 and CR10) & (2024-2026)

3.4 New Products and Potential Entrants

3.5 Mergers & Acquisitions, Expansion

4 CPU INSTRUCTION SET ARCHITECTURE (ISA) BY REGION

- 4.1 CPU Instruction Set Architecture (ISA) Market Size by Region (2021-2026)
- 4.2 Global CPU Instruction Set Architecture (ISA) Annual Revenue by Country/Region (2021-2026)
- 4.3 Americas CPU Instruction Set Architecture (ISA) Market Size Growth (2021-2026)
- 4.4 APAC CPU Instruction Set Architecture (ISA) Market Size Growth (2021-2026)
- 4.5 Europe CPU Instruction Set Architecture (ISA) Market Size Growth (2021-2026)
- 4.6 Middle East & Africa CPU Instruction Set Architecture (ISA) Market Size Growth (2021-2026)

5 AMERICAS

- 5.1 Americas CPU Instruction Set Architecture (ISA) Market Size by Country (2021-2026)
- 5.2 Americas CPU Instruction Set Architecture (ISA) Market Size by Architecture Family (2021-2026)
- 5.3 Americas CPU Instruction Set Architecture (ISA) Market Size by Application (2021-2026)
- 5.4 United States
- 5.5 Canada
- 5.6 Mexico
- 5.7 Brazil

6 APAC

- 6.1 APAC CPU Instruction Set Architecture (ISA) Market Size by Region (2021-2026)
- 6.2 APAC CPU Instruction Set Architecture (ISA) Market Size by Architecture Family (2021-2026)
- 6.3 APAC CPU Instruction Set Architecture (ISA) Market Size by Application (2021-2026)
- 6.4 China
- 6.5 Japan
- 6.6 South Korea
- 6.7 Southeast Asia
- 6.8 India
- 6.9 Australia

7 EUROPE

- 7.1 Europe CPU Instruction Set Architecture (ISA) Market Size by Country (2021-2026)
- 7.2 Europe CPU Instruction Set Architecture (ISA) Market Size by Architecture Family (2021-2026)
- 7.3 Europe CPU Instruction Set Architecture (ISA) Market Size by Application (2021-2026)
- 7.4 Germany
- 7.5 France
- 7.6 UK
- 7.7 Italy
- 7.8 Russia

8 MIDDLE EAST & AFRICA

- 8.1 Middle East & Africa CPU Instruction Set Architecture (ISA) by Region (2021-2026)
- 8.2 Middle East & Africa CPU Instruction Set Architecture (ISA) Market Size by Architecture Family (2021-2026)
- 8.3 Middle East & Africa CPU Instruction Set Architecture (ISA) Market Size by Application (2021-2026)
- 8.4 Egypt
- 8.5 South Africa
- 8.6 Israel
- 8.7 Turkey
- 8.8 GCC Countries

9 MARKET DRIVERS, CHALLENGES AND TRENDS

- 9.1 Market Drivers & Growth Opportunities
- 9.2 Market Challenges & Risks
- 9.3 Industry Trends

10 GLOBAL CPU INSTRUCTION SET ARCHITECTURE (ISA) MARKET FORECAST

- 10.1 Global CPU Instruction Set Architecture (ISA) Forecast by Region (2027-2032)
 - 10.1.1 Global CPU Instruction Set Architecture (ISA) Forecast by Region (2027-2032)
 - 10.1.2 Americas CPU Instruction Set Architecture (ISA) Forecast
 - 10.1.3 APAC CPU Instruction Set Architecture (ISA) Forecast
 - 10.1.4 Europe CPU Instruction Set Architecture (ISA) Forecast
 - 10.1.5 Middle East & Africa CPU Instruction Set Architecture (ISA) Forecast
- 10.2 Americas CPU Instruction Set Architecture (ISA) Forecast by Country (2027-2032)

- 10.2.1 United States Market CPU Instruction Set Architecture (ISA) Forecast
- 10.2.2 Canada Market CPU Instruction Set Architecture (ISA) Forecast
- 10.2.3 Mexico Market CPU Instruction Set Architecture (ISA) Forecast
- 10.2.4 Brazil Market CPU Instruction Set Architecture (ISA) Forecast
- 10.3 APAC CPU Instruction Set Architecture (ISA) Forecast by Region (2027-2032)
 - 10.3.1 China CPU Instruction Set Architecture (ISA) Market Forecast
 - 10.3.2 Japan Market CPU Instruction Set Architecture (ISA) Forecast
 - 10.3.3 Korea Market CPU Instruction Set Architecture (ISA) Forecast
 - 10.3.4 Southeast Asia Market CPU Instruction Set Architecture (ISA) Forecast
 - 10.3.5 India Market CPU Instruction Set Architecture (ISA) Forecast
 - 10.3.6 Australia Market CPU Instruction Set Architecture (ISA) Forecast
- 10.4 Europe CPU Instruction Set Architecture (ISA) Forecast by Country (2027-2032)
 - 10.4.1 Germany Market CPU Instruction Set Architecture (ISA) Forecast
 - 10.4.2 France Market CPU Instruction Set Architecture (ISA) Forecast
 - 10.4.3 UK Market CPU Instruction Set Architecture (ISA) Forecast
 - 10.4.4 Italy Market CPU Instruction Set Architecture (ISA) Forecast
 - 10.4.5 Russia Market CPU Instruction Set Architecture (ISA) Forecast
- 10.5 Middle East & Africa CPU Instruction Set Architecture (ISA) Forecast by Region (2027-2032)
 - 10.5.1 Egypt Market CPU Instruction Set Architecture (ISA) Forecast
 - 10.5.2 South Africa Market CPU Instruction Set Architecture (ISA) Forecast
 - 10.5.3 Israel Market CPU Instruction Set Architecture (ISA) Forecast
 - 10.5.4 Turkey Market CPU Instruction Set Architecture (ISA) Forecast
- 10.6 Global CPU Instruction Set Architecture (ISA) Forecast by Architecture Family (2027-2032)
- 10.7 Global CPU Instruction Set Architecture (ISA) Forecast by Application (2027-2032)
 - 10.7.1 GCC Countries Market CPU Instruction Set Architecture (ISA) Forecast

11 KEY PLAYERS ANALYSIS

- 11.1 Arm Holdings plc
 - 11.1.1 Arm Holdings plc Company Information
 - 11.1.2 Arm Holdings plc CPU Instruction Set Architecture (ISA) Product Offered
 - 11.1.3 Arm Holdings plc CPU Instruction Set Architecture (ISA) Revenue, Gross Margin and Market Share (2021-2026)
 - 11.1.4 Arm Holdings plc Main Business Overview
 - 11.1.5 Arm Holdings plc Latest Developments
- 11.2 RISC-V International
 - 11.2.1 RISC-V International Company Information

- 11.2.2 RISC-V International CPU Instruction Set Architecture (ISA) Product Offered
- 11.2.3 RISC-V International CPU Instruction Set Architecture (ISA) Revenue, Gross Margin and Market Share (2021-2026)
- 11.2.4 RISC-V International Main Business Overview
- 11.2.5 RISC-V International Latest Developments
- 11.3 Synopsys, Inc.
 - 11.3.1 Synopsys, Inc. Company Information
 - 11.3.2 Synopsys, Inc. CPU Instruction Set Architecture (ISA) Product Offered
 - 11.3.3 Synopsys, Inc. CPU Instruction Set Architecture (ISA) Revenue, Gross Margin and Market Share (2021-2026)
 - 11.3.4 Synopsys, Inc. Main Business Overview
 - 11.3.5 Synopsys, Inc. Latest Developments
- 11.4 Cadence Design Systems, Inc.
 - 11.4.1 Cadence Design Systems, Inc. Company Information
 - 11.4.2 Cadence Design Systems, Inc. CPU Instruction Set Architecture (ISA) Product Offered
 - 11.4.3 Cadence Design Systems, Inc. CPU Instruction Set Architecture (ISA) Revenue, Gross Margin and Market Share (2021-2026)
 - 11.4.4 Cadence Design Systems, Inc. Main Business Overview
 - 11.4.5 Cadence Design Systems, Inc. Latest Developments
- 11.5 SiFive, Inc.
 - 11.5.1 SiFive, Inc. Company Information
 - 11.5.2 SiFive, Inc. CPU Instruction Set Architecture (ISA) Product Offered
 - 11.5.3 SiFive, Inc. CPU Instruction Set Architecture (ISA) Revenue, Gross Margin and Market Share (2021-2026)
 - 11.5.4 SiFive, Inc. Main Business Overview
 - 11.5.5 SiFive, Inc. Latest Developments
- 11.6 Andes Technology Corporation
 - 11.6.1 Andes Technology Corporation Company Information
 - 11.6.2 Andes Technology Corporation CPU Instruction Set Architecture (ISA) Product Offered
 - 11.6.3 Andes Technology Corporation CPU Instruction Set Architecture (ISA) Revenue, Gross Margin and Market Share (2021-2026)
 - 11.6.4 Andes Technology Corporation Main Business Overview
 - 11.6.5 Andes Technology Corporation Latest Developments
- 11.7 Intel Corporation
 - 11.7.1 Intel Corporation Company Information
 - 11.7.2 Intel Corporation CPU Instruction Set Architecture (ISA) Product Offered
 - 11.7.3 Intel Corporation CPU Instruction Set Architecture (ISA) Revenue, Gross

Margin and Market Share (2021-2026)

11.7.4 Intel Corporation Main Business Overview

11.7.5 Intel Corporation Latest Developments

11.8 Advanced Micro Devices, Inc.

11.8.1 Advanced Micro Devices, Inc. Company Information

11.8.2 Advanced Micro Devices, Inc. CPU Instruction Set Architecture (ISA) Product Offered

11.8.3 Advanced Micro Devices, Inc. CPU Instruction Set Architecture (ISA) Revenue, Gross Margin and Market Share (2021-2026)

11.8.4 Advanced Micro Devices, Inc. Main Business Overview

11.8.5 Advanced Micro Devices, Inc. Latest Developments

11.9 Cudasip

11.9.1 Cudasip Company Information

11.9.2 Cudasip CPU Instruction Set Architecture (ISA) Product Offered

11.9.3 Cudasip CPU Instruction Set Architecture (ISA) Revenue, Gross Margin and Market Share (2021-2026)

11.9.4 Cudasip Main Business Overview

11.9.5 Cudasip Latest Developments

11.10 GlobalFoundries Inc.

11.10.1 GlobalFoundries Inc. Company Information

11.10.2 GlobalFoundries Inc. CPU Instruction Set Architecture (ISA) Product Offered

11.10.3 GlobalFoundries Inc. CPU Instruction Set Architecture (ISA) Revenue, Gross Margin and Market Share (2021-2026)

11.10.4 GlobalFoundries Inc. Main Business Overview

11.10.5 GlobalFoundries Inc. Latest Developments

11.11 Nuclei System Technology

11.11.1 Nuclei System Technology Company Information

11.11.2 Nuclei System Technology CPU Instruction Set Architecture (ISA) Product Offered

11.11.3 Nuclei System Technology CPU Instruction Set Architecture (ISA) Revenue, Gross Margin and Market Share (2021-2026)

11.11.4 Nuclei System Technology Main Business Overview

11.11.5 Nuclei System Technology Latest Developments

11.12 Arm China

11.12.1 Arm China Company Information

11.12.2 Arm China CPU Instruction Set Architecture (ISA) Product Offered

11.12.3 Arm China CPU Instruction Set Architecture (ISA) Revenue, Gross Margin and Market Share (2021-2026)

11.12.4 Arm China Main Business Overview

11.12.5 Arm China Latest Developments

11.13 IBM Corporation

11.13.1 IBM Corporation Company Information

11.13.2 IBM Corporation CPU Instruction Set Architecture (ISA) Product Offered

11.13.3 IBM Corporation CPU Instruction Set Architecture (ISA) Revenue, Gross Margin and Market Share (2021-2026)

11.13.4 IBM Corporation Main Business Overview

11.13.5 IBM Corporation Latest Developments

11.14 Loongson Technology Corporation Limited

11.14.1 Loongson Technology Corporation Limited Company Information

11.14.2 Loongson Technology Corporation Limited CPU Instruction Set Architecture (ISA) Product Offered

11.14.3 Loongson Technology Corporation Limited CPU Instruction Set Architecture (ISA) Revenue, Gross Margin and Market Share (2021-2026)

11.14.4 Loongson Technology Corporation Limited Main Business Overview

11.14.5 Loongson Technology Corporation Limited Latest Developments

11.15 Ventana Micro Systems

11.15.1 Ventana Micro Systems Company Information

11.15.2 Ventana Micro Systems CPU Instruction Set Architecture (ISA) Product Offered

11.15.3 Ventana Micro Systems CPU Instruction Set Architecture (ISA) Revenue, Gross Margin and Market Share (2021-2026)

11.15.4 Ventana Micro Systems Main Business Overview

11.15.5 Ventana Micro Systems Latest Developments

11.16 CEVA, Inc.

11.16.1 CEVA, Inc. Company Information

11.16.2 CEVA, Inc. CPU Instruction Set Architecture (ISA) Product Offered

11.16.3 CEVA, Inc. CPU Instruction Set Architecture (ISA) Revenue, Gross Margin and Market Share (2021-2026)

11.16.4 CEVA, Inc. Main Business Overview

11.16.5 CEVA, Inc. Latest Developments

12 RESEARCH FINDINGS AND CONCLUSION

List Of Tables

LIST OF TABLES

Table 1. CPU Instruction Set Architecture (ISA) Market Size CAGR by Region (2021 VS 2025 VS 2032) & (\$ millions)

Table 2. CPU Instruction Set Architecture (ISA) Annual Sales CAGR by Country/Region (2021, 2025 & 2032) & (\$ millions)

Table 3. Major Players of Arm Architecture

Table 4. Major Players of x86 Architecture

Table 5. Major Players of RISC-V Architecture

Table 6. Major Players of Power Architecture

Table 7. CPU Instruction Set Architecture (ISA) Market Size CAGR by Architecture Family (2021 VS 2025 VS 2032) & (\$ millions)

Table 8. Global CPU Instruction Set Architecture (ISA) Market Size by Architecture Family (2021-2026) & (\$ millions)

Table 9. Global CPU Instruction Set Architecture (ISA) Market Size Market Share by Architecture Family (2021-2026)

Table 10. Major Players of Architecture Licensing

Table 11. Major Players of Processor Core IP Licensing

Table 12. Major Players of Royalty-based Licensing

Table 13. Major Players of Open Standard Governance

Table 14. CPU Instruction Set Architecture (ISA) Market Size CAGR by Business Model (2021 VS 2025 VS 2032) & (\$ millions)

Table 15. Global CPU Instruction Set Architecture (ISA) Market Size by Business Model (2021-2026) & (\$ millions)

Table 16. Global CPU Instruction Set Architecture (ISA) Market Size Market Share by Business Model (2021-2026)

Table 17. Major Players of Application Processor IP

Table 18. Major Players of Microcontroller Processor IP

Table 19. Major Players of Real-time Processor IP

Table 20. Major Players of DSP-like / Signal Processor IP

Table 21. Major Players of High-performance / Server-class IP

Table 22. CPU Instruction Set Architecture (ISA) Market Size CAGR by Processor Class (2021 VS 2025 VS 2032) & (\$ millions)

Table 23. Global CPU Instruction Set Architecture (ISA) Market Size by Processor Class (2021-2026) & (\$ millions)

Table 24. Global CPU Instruction Set Architecture (ISA) Market Size Market Share by Processor Class (2021-2026)

Table 25. CPU Instruction Set Architecture (ISA) Market Size CAGR by Application (2021 VS 2025 VS 2032) & (\$ millions)

Table 26. Global CPU Instruction Set Architecture (ISA) Market Size by Application (2021-2026) & (\$ millions)

Table 27. Global CPU Instruction Set Architecture (ISA) Market Size Market Share by Application (2021-2026)

Table 28. Global CPU Instruction Set Architecture (ISA) Revenue by Player (2021-2026) & (\$ millions)

Table 29. Global CPU Instruction Set Architecture (ISA) Revenue Market Share by Player (2021-2026)

Table 30. CPU Instruction Set Architecture (ISA) Key Players Head office and Products Offered

Table 31. CPU Instruction Set Architecture (ISA) Concentration Ratio (CR3, CR5 and CR10) & (2024-2026)

Table 32. New Products and Potential Entrants

Table 33. Mergers & Acquisitions, Expansion

Table 34. Global CPU Instruction Set Architecture (ISA) Market Size by Region (2021-2026) & (\$ millions)

Table 35. Global CPU Instruction Set Architecture (ISA) Market Size Market Share by Region (2021-2026)

Table 36. Global CPU Instruction Set Architecture (ISA) Revenue by Country/Region (2021-2026) & (\$ millions)

Table 37. Global CPU Instruction Set Architecture (ISA) Revenue Market Share by Country/Region (2021-2026)

Table 38. Americas CPU Instruction Set Architecture (ISA) Market Size by Country (2021-2026) & (\$ millions)

Table 39. Americas CPU Instruction Set Architecture (ISA) Market Size Market Share by Country (2021-2026)

Table 40. Americas CPU Instruction Set Architecture (ISA) Market Size by Architecture Family (2021-2026) & (\$ millions)

Table 41. Americas CPU Instruction Set Architecture (ISA) Market Size Market Share by Architecture Family (2021-2026)

Table 42. Americas CPU Instruction Set Architecture (ISA) Market Size by Application (2021-2026) & (\$ millions)

Table 43. Americas CPU Instruction Set Architecture (ISA) Market Size Market Share by Application (2021-2026)

Table 44. APAC CPU Instruction Set Architecture (ISA) Market Size by Region (2021-2026) & (\$ millions)

Table 45. APAC CPU Instruction Set Architecture (ISA) Market Size Market Share by

Region (2021-2026)

Table 46. APAC CPU Instruction Set Architecture (ISA) Market Size by Architecture Family (2021-2026) & (\$ millions)

Table 47. APAC CPU Instruction Set Architecture (ISA) Market Size by Application (2021-2026) & (\$ millions)

Table 48. Europe CPU Instruction Set Architecture (ISA) Market Size by Country (2021-2026) & (\$ millions)

Table 49. Europe CPU Instruction Set Architecture (ISA) Market Size Market Share by Country (2021-2026)

Table 50. Europe CPU Instruction Set Architecture (ISA) Market Size by Architecture Family (2021-2026) & (\$ millions)

Table 51. Europe CPU Instruction Set Architecture (ISA) Market Size by Application (2021-2026) & (\$ millions)

Table 52. Middle East & Africa CPU Instruction Set Architecture (ISA) Market Size by Region (2021-2026) & (\$ millions)

Table 53. Middle East & Africa CPU Instruction Set Architecture (ISA) Market Size by Architecture Family (2021-2026) & (\$ millions)

Table 54. Middle East & Africa CPU Instruction Set Architecture (ISA) Market Size by Application (2021-2026) & (\$ millions)

Table 55. Key Market Drivers & Growth Opportunities of CPU Instruction Set Architecture (ISA)

Table 56. Key Market Challenges & Risks of CPU Instruction Set Architecture (ISA)

Table 57. Key Industry Trends of CPU Instruction Set Architecture (ISA)

Table 58. Global CPU Instruction Set Architecture (ISA) Market Size Forecast by Region (2027-2032) & (\$ millions)

Table 59. Global CPU Instruction Set Architecture (ISA) Market Size Market Share Forecast by Region (2027-2032)

Table 60. Global CPU Instruction Set Architecture (ISA) Market Size Forecast by Architecture Family (2027-2032) & (\$ millions)

Table 61. Global CPU Instruction Set Architecture (ISA) Market Size Forecast by Application (2027-2032) & (\$ millions)

Table 62. Arm Holdings plc Details, Company Type, CPU Instruction Set Architecture (ISA) Area Served and Its Competitors

Table 63. Arm Holdings plc CPU Instruction Set Architecture (ISA) Product Offered

Table 64. Arm Holdings plc CPU Instruction Set Architecture (ISA) Revenue (\$ million), Gross Margin and Market Share (2021-2026)

Table 65. Arm Holdings plc Main Business

Table 66. Arm Holdings plc Latest Developments

Table 67. RISC-V International Details, Company Type, CPU Instruction Set

Architecture (ISA) Area Served and Its Competitors

Table 68. RISC-V International CPU Instruction Set Architecture (ISA) Product Offered

Table 69. RISC-V International CPU Instruction Set Architecture (ISA) Revenue (\$ million), Gross Margin and Market Share (2021-2026)

Table 70. RISC-V International Main Business

Table 71. RISC-V International Latest Developments

Table 72. Synopsys, Inc. Details, Company Type, CPU Instruction Set Architecture (ISA) Area Served and Its Competitors

Table 73. Synopsys, Inc. CPU Instruction Set Architecture (ISA) Product Offered

Table 74. Synopsys, Inc. CPU Instruction Set Architecture (ISA) Revenue (\$ million), Gross Margin and Market Share (2021-2026)

Table 75. Synopsys, Inc. Main Business

Table 76. Synopsys, Inc. Latest Developments

Table 77. Cadence Design Systems, Inc. Details, Company Type, CPU Instruction Set Architecture (ISA) Area Served and Its Competitors

Table 78. Cadence Design Systems, Inc. CPU Instruction Set Architecture (ISA) Product Offered

Table 79. Cadence Design Systems, Inc. CPU Instruction Set Architecture (ISA) Revenue (\$ million), Gross Margin and Market Share (2021-2026)

Table 80. Cadence Design Systems, Inc. Main Business

Table 81. Cadence Design Systems, Inc. Latest Developments

Table 82. SiFive, Inc. Details, Company Type, CPU Instruction Set Architecture (ISA) Area Served and Its Competitors

Table 83. SiFive, Inc. CPU Instruction Set Architecture (ISA) Product Offered

Table 84. SiFive, Inc. CPU Instruction Set Architecture (ISA) Revenue (\$ million), Gross Margin and Market Share (2021-2026)

Table 85. SiFive, Inc. Main Business

Table 86. SiFive, Inc. Latest Developments

Table 87. Andes Technology Corporation Details, Company Type, CPU Instruction Set Architecture (ISA) Area Served and Its Competitors

Table 88. Andes Technology Corporation CPU Instruction Set Architecture (ISA) Product Offered

Table 89. Andes Technology Corporation CPU Instruction Set Architecture (ISA) Revenue (\$ million), Gross Margin and Market Share (2021-2026)

Table 90. Andes Technology Corporation Main Business

Table 91. Andes Technology Corporation Latest Developments

Table 92. Intel Corporation Details, Company Type, CPU Instruction Set Architecture (ISA) Area Served and Its Competitors

Table 93. Intel Corporation CPU Instruction Set Architecture (ISA) Product Offered

Table 94. Intel Corporation CPU Instruction Set Architecture (ISA) Revenue (\$ million), Gross Margin and Market Share (2021-2026)

Table 95. Intel Corporation Main Business

Table 96. Intel Corporation Latest Developments

Table 97. Advanced Micro Devices, Inc. Details, Company Type, CPU Instruction Set Architecture (ISA) Area Served and Its Competitors

Table 98. Advanced Micro Devices, Inc. CPU Instruction Set Architecture (ISA) Product Offered

Table 99. Advanced Micro Devices, Inc. CPU Instruction Set Architecture (ISA) Revenue (\$ million), Gross Margin and Market Share (2021-2026)

Table 100. Advanced Micro Devices, Inc. Main Business

Table 101. Advanced Micro Devices, Inc. Latest Developments

Table 102. Cudasip Details, Company Type, CPU Instruction Set Architecture (ISA) Area Served and Its Competitors

Table 103. Cudasip CPU Instruction Set Architecture (ISA) Product Offered

Table 104. Cudasip CPU Instruction Set Architecture (ISA) Revenue (\$ million), Gross Margin and Market Share (2021-2026)

Table 105. Cudasip Main Business

Table 106. Cudasip Latest Developments

Table 107. GlobalFoundries Inc. Details, Company Type, CPU Instruction Set Architecture (ISA) Area Served and Its Competitors

Table 108. GlobalFoundries Inc. CPU Instruction Set Architecture (ISA) Product Offered

Table 109. GlobalFoundries Inc. CPU Instruction Set Architecture (ISA) Revenue (\$ million), Gross Margin and Market Share (2021-2026)

Table 110. GlobalFoundries Inc. Main Business

Table 111. GlobalFoundries Inc. Latest Developments

Table 112. Nuclei System Technology Details, Company Type, CPU Instruction Set Architecture (ISA) Area Served and Its Competitors

Table 113. Nuclei System Technology CPU Instruction Set Architecture (ISA) Product Offered

Table 114. Nuclei System Technology CPU Instruction Set Architecture (ISA) Revenue (\$ million), Gross Margin and Market Share (2021-2026)

Table 115. Nuclei System Technology Main Business

Table 116. Nuclei System Technology Latest Developments

Table 117. Arm China Details, Company Type, CPU Instruction Set Architecture (ISA) Area Served and Its Competitors

Table 118. Arm China CPU Instruction Set Architecture (ISA) Product Offered

Table 119. Arm China CPU Instruction Set Architecture (ISA) Revenue (\$ million), Gross Margin and Market Share (2021-2026)

Table 120. Arm China Main Business

Table 121. Arm China Latest Developments

Table 122. IBM Corporation Details, Company Type, CPU Instruction Set Architecture (ISA) Area Served and Its Competitors

Table 123. IBM Corporation CPU Instruction Set Architecture (ISA) Product Offered

Table 124. IBM Corporation CPU Instruction Set Architecture (ISA) Revenue (\$ million), Gross Margin and Market Share (2021-2026)

Table 125. IBM Corporation Main Business

Table 126. IBM Corporation Latest Developments

Table 127. Loongson Technology Corporation Limited Details, Company Type, CPU Instruction Set Architecture (ISA) Area Served and Its Competitors

Table 128. Loongson Technology Corporation Limited CPU Instruction Set Architecture (ISA) Product Offered

Table 129. Loongson Technology Corporation Limited CPU Instruction Set Architecture (ISA) Revenue (\$ million), Gross Margin and Market Share (2021-2026)

Table 130. Loongson Technology Corporation Limited Main Business

Table 131. Loongson Technology Corporation Limited Latest Developments

Table 132. Ventana Micro Systems Details, Company Type, CPU Instruction Set Architecture (ISA) Area Served and Its Competitors

Table 133. Ventana Micro Systems CPU Instruction Set Architecture (ISA) Product Offered

Table 134. Ventana Micro Systems CPU Instruction Set Architecture (ISA) Revenue (\$ million), Gross Margin and Market Share (2021-2026)

Table 135. Ventana Micro Systems Main Business

Table 136. Ventana Micro Systems Latest Developments

Table 137. CEVA, Inc. Details, Company Type, CPU Instruction Set Architecture (ISA) Area Served and Its Competitors

Table 138. CEVA, Inc. CPU Instruction Set Architecture (ISA) Product Offered

Table 139. CEVA, Inc. CPU Instruction Set Architecture (ISA) Revenue (\$ million), Gross Margin and Market Share (2021-2026)

Table 140. CEVA, Inc. Main Business

Table 141. CEVA, Inc. Latest Developments

List Of Figures

LIST OF FIGURES

Figure 1. CPU Instruction Set Architecture (ISA) Report Years Considered

Figure 2. Research Objectives

Figure 3. Research Methodology

Figure 4. Research Process and Data Source

Figure 5. Global CPU Instruction Set Architecture (ISA) Market Size Growth Rate (2021-2032) (\$ millions)

Figure 6. CPU Instruction Set Architecture (ISA) Sales by Geographic Region (2021, 2025 & 2032) & (\$ millions)

Figure 7. CPU Instruction Set Architecture (ISA) Sales Market Share by Country/Region (2025)

Figure 8. CPU Instruction Set Architecture (ISA) Sales Market Share by Country/Region (2021, 2025 & 2032)

Figure 9. Global CPU Instruction Set Architecture (ISA) Market Size Market Share by Architecture Family in 2025

Figure 10. Global CPU Instruction Set Architecture (ISA) Market Size Market Share by Business Model in 2025

Figure 11. Global CPU Instruction Set Architecture (ISA) Market Size Market Share by Processor Class in 2025

Figure 12. CPU Instruction Set Architecture (ISA) in Mobile and Consumer SoCs

Figure 13. Global CPU Instruction Set Architecture (ISA) Market: Mobile and Consumer SoCs (2021-2026) & (\$ millions)

Figure 14. CPU Instruction Set Architecture (ISA) in Automotive Electronics

Figure 15. Global CPU Instruction Set Architecture (ISA) Market: Automotive Electronics (2021-2026) & (\$ millions)

Figure 16. CPU Instruction Set Architecture (ISA) in IoT and Embedded Control

Figure 17. Global CPU Instruction Set Architecture (ISA) Market: IoT and Embedded Control (2021-2026) & (\$ millions)

Figure 18. CPU Instruction Set Architecture (ISA) in Data Center and Cloud

Figure 19. Global CPU Instruction Set Architecture (ISA) Market: Data Center and Cloud (2021-2026) & (\$ millions)

Figure 20. Global CPU Instruction Set Architecture (ISA) Market Size Market Share by Application in 2025

Figure 21. Global CPU Instruction Set Architecture (ISA) Revenue Market Share by Player in 2025

Figure 22. Global CPU Instruction Set Architecture (ISA) Market Size Market Share by

Region (2021-2026)

Figure 23. Americas CPU Instruction Set Architecture (ISA) Market Size 2021-2026 (\$ millions)

Figure 24. APAC CPU Instruction Set Architecture (ISA) Market Size 2021-2026 (\$ millions)

Figure 25. Europe CPU Instruction Set Architecture (ISA) Market Size 2021-2026 (\$ millions)

Figure 26. Middle East & Africa CPU Instruction Set Architecture (ISA) Market Size 2021-2026 (\$ millions)

Figure 27. Americas CPU Instruction Set Architecture (ISA) Value Market Share by Country in 2025

Figure 28. United States CPU Instruction Set Architecture (ISA) Market Size Growth 2021-2026 (\$ millions)

Figure 29. Canada CPU Instruction Set Architecture (ISA) Market Size Growth 2021-2026 (\$ millions)

Figure 30. Mexico CPU Instruction Set Architecture (ISA) Market Size Growth 2021-2026 (\$ millions)

Figure 31. Brazil CPU Instruction Set Architecture (ISA) Market Size Growth 2021-2026 (\$ millions)

Figure 32. APAC CPU Instruction Set Architecture (ISA) Market Size Market Share by Region in 2025

Figure 33. APAC CPU Instruction Set Architecture (ISA) Market Size Market Share by Architecture Family (2021-2026)

Figure 34. APAC CPU Instruction Set Architecture (ISA) Market Size Market Share by Application (2021-2026)

Figure 35. China CPU Instruction Set Architecture (ISA) Market Size Growth 2021-2026 (\$ millions)

Figure 36. Japan CPU Instruction Set Architecture (ISA) Market Size Growth 2021-2026 (\$ millions)

Figure 37. South Korea CPU Instruction Set Architecture (ISA) Market Size Growth 2021-2026 (\$ millions)

Figure 38. Southeast Asia CPU Instruction Set Architecture (ISA) Market Size Growth 2021-2026 (\$ millions)

Figure 39. India CPU Instruction Set Architecture (ISA) Market Size Growth 2021-2026 (\$ millions)

Figure 40. Australia CPU Instruction Set Architecture (ISA) Market Size Growth 2021-2026 (\$ millions)

Figure 41. Europe CPU Instruction Set Architecture (ISA) Market Size Market Share by Country in 2025

Figure 42. Europe CPU Instruction Set Architecture (ISA) Market Size Market Share by Architecture Family (2021-2026)

Figure 43. Europe CPU Instruction Set Architecture (ISA) Market Size Market Share by Application (2021-2026)

Figure 44. Germany CPU Instruction Set Architecture (ISA) Market Size Growth 2021-2026 (\$ millions)

Figure 45. France CPU Instruction Set Architecture (ISA) Market Size Growth 2021-2026 (\$ millions)

Figure 46. UK CPU Instruction Set Architecture (ISA) Market Size Growth 2021-2026 (\$ millions)

Figure 47. Italy CPU Instruction Set Architecture (ISA) Market Size Growth 2021-2026 (\$ millions)

Figure 48. Russia CPU Instruction Set Architecture (ISA) Market Size Growth 2021-2026 (\$ millions)

Figure 49. Middle East & Africa CPU Instruction Set Architecture (ISA) Market Size Market Share by Region (2021-2026)

Figure 50. Middle East & Africa CPU Instruction Set Architecture (ISA) Market Size Market Share by Architecture Family (2021-2026)

Figure 51. Middle East & Africa CPU Instruction Set Architecture (ISA) Market Size Market Share by Application (2021-2026)

Figure 52. Egypt CPU Instruction Set Architecture (ISA) Market Size Growth 2021-2026 (\$ millions)

Figure 53. South Africa CPU Instruction Set Architecture (ISA) Market Size Growth 2021-2026 (\$ millions)

Figure 54. Israel CPU Instruction Set Architecture (ISA) Market Size Growth 2021-2026 (\$ millions)

Figure 55. Turkey CPU Instruction Set Architecture (ISA) Market Size Growth 2021-2026 (\$ millions)

Figure 56. GCC Countries CPU Instruction Set Architecture (ISA) Market Size Growth 2021-2026 (\$ millions)

Figure 57. Americas CPU Instruction Set Architecture (ISA) Market Size 2027-2032 (\$ millions)

Figure 58. APAC CPU Instruction Set Architecture (ISA) Market Size 2027-2032 (\$ millions)

Figure 59. Europe CPU Instruction Set Architecture (ISA) Market Size 2027-2032 (\$ millions)

Figure 60. Middle East & Africa CPU Instruction Set Architecture (ISA) Market Size 2027-2032 (\$ millions)

Figure 61. United States CPU Instruction Set Architecture (ISA) Market Size 2027-2032

(\$ millions)

Figure 62. Canada CPU Instruction Set Architecture (ISA) Market Size 2027-2032 (\$ millions)

Figure 63. Mexico CPU Instruction Set Architecture (ISA) Market Size 2027-2032 (\$ millions)

Figure 64. Brazil CPU Instruction Set Architecture (ISA) Market Size 2027-2032 (\$ millions)

Figure 65. China CPU Instruction Set Architecture (ISA) Market Size 2027-2032 (\$ millions)

Figure 66. Japan CPU Instruction Set Architecture (ISA) Market Size 2027-2032 (\$ millions)

Figure 67. Korea CPU Instruction Set Architecture (ISA) Market Size 2027-2032 (\$ millions)

Figure 68. Southeast Asia CPU Instruction Set Architecture (ISA) Market Size 2027-2032 (\$ millions)

Figure 69. India CPU Instruction Set Architecture (ISA) Market Size 2027-2032 (\$ millions)

Figure 70. Australia CPU Instruction Set Architecture (ISA) Market Size 2027-2032 (\$ millions)

Figure 71. Germany CPU Instruction Set Architecture (ISA) Market Size 2027-2032 (\$ millions)

Figure 72. France CPU Instruction Set Architecture (ISA) Market Size 2027-2032 (\$ millions)

Figure 73. UK CPU Instruction Set Architecture (ISA) Market Size 2027-2032 (\$ millions)

Figure 74. Italy CPU Instruction Set Architecture (ISA) Market Size 2027-2032 (\$ millions)

Figure 75. Russia CPU Instruction Set Architecture (ISA) Market Size 2027-2032 (\$ millions)

Figure 76. Egypt CPU Instruction Set Architecture (ISA) Market Size 2027-2032 (\$ millions)

Figure 77. South Africa CPU Instruction Set Architecture (ISA) Market Size 2027-2032 (\$ millions)

Figure 78. Israel CPU Instruction Set Architecture (ISA) Market Size 2027-2032 (\$ millions)

Figure 79. Turkey CPU Instruction Set Architecture (ISA) Market Size 2027-2032 (\$ millions)

Figure 80. Global CPU Instruction Set Architecture (ISA) Market Size Market Share Forecast by Architecture Family (2027-2032)

Figure 81. Global CPU Instruction Set Architecture (ISA) Market Size Market Share
Forecast by Application (2027-2032)

Figure 82. GCC Countries CPU Instruction Set Architecture (ISA) Market Size
2027-2032 (\$ millions)

I would like to order

Product name: Global CPU Instruction Set Architecture (ISA) Market Growth (Status and Outlook) 2026-2032

Product link: <https://marketpublishers.com/r/G752B7A8C827EN.html>

Price: US\$ 3,660.00 (Single User License / Electronic Delivery)

If you want to order Corporate License or Hard Copy, please, contact our Customer Service:

info@marketpublishers.com

Payment

To pay by Credit Card (Visa, MasterCard, American Express, PayPal), please, click button on product page <https://marketpublishers.com/r/G752B7A8C827EN.html>