

# Global Chip Scale Package (CSP) Market Growth (Status and Outlook) 2025-2031

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## Abstracts

According to this study, the global Chip Scale Package (CSP) market size will reach US\$ million by 2031.

A chip scale package or chip-scale package (CSP) is a type of integrated circuit package.

Originally, CSP was the acronym for chip-size packaging. Since only a few packages are chip size, the meaning of the acronym was adapted to chip-scale packaging. According to IPC's standard J-STD-012, Implementation of Flip Chip and Chip Scale Technology, in order to qualify as chip scale, the package must have an area no greater than 1.2 times that of the die and it must be a single-die, direct surface mountable package. Another criterion that is often applied to qualify these packages as CSPs is their ball pitch should be no more than 1 mm.

Following a strong growth of 26.2 percent in the year 2021, WSTS revised it down to a single digit growth for the worldwide semiconductor market in 2022 with a total size of US\$580 billion, up 4.4 percent. WSTS lowered growth estimation as inflation rises and end markets seeing weaker demand, especially those exposed to consumer spending. While some major categories are still double-digit year-over-year growth in 2022, led by Analog with 20.8 percent, Sensors with 16.3 percent, and Logic with 14.5 percent growth. Memory declined with 12.6 percent year over year. In 2022, all geographical regions showed double-digit growth except Asia Pacific. The largest region, Asia Pacific, declined 2.0 percent. Sales in the Americas were US\$142.1 billion, up 17.0% year-on-year, sales in Europe were US\$53.8 billion, up 12.6% year-on-year, and sales in Japan were US\$48.1 billion, up 10.0% year-on-year. However, sales in the largest Asia-Pacific region were US\$336.2 billion, down 2.0% year-on-year.

LPI (LP Information)' newest research report, the “Chip Scale Package (CSP) Industry Forecast” looks at past sales and reviews total world Chip Scale Package (CSP) sales in 2024, providing a comprehensive analysis by region and market sector of projected Chip Scale Package (CSP) sales for 2025 through 2031. With Chip Scale Package (CSP) sales broken down by region, market sector and sub-sector, this report provides a detailed analysis in US\$ millions of the world Chip Scale Package (CSP) industry.

This Insight Report provides a comprehensive analysis of the global Chip Scale Package (CSP) landscape and highlights key trends related to product segmentation, company formation, revenue, and market share, latest development, and M&A activity. This report also analyses the strategies of leading global companies with a focus on Chip Scale Package (CSP) portfolios and capabilities, market entry strategies, market positions, and geographic footprints, to better understand these firms' unique position in an accelerating global Chip Scale Package (CSP) market.

This Insight Report evaluates the key market trends, drivers, and affecting factors shaping the global outlook for Chip Scale Package (CSP) and breaks down the forecast by Type, by Application, geography, and market size to highlight emerging pockets of opportunity. With a transparent methodology based on hundreds of bottom-up qualitative and quantitative market inputs, this study forecast offers a highly nuanced view of the current state and future trajectory in the global Chip Scale Package (CSP).

This report presents a comprehensive overview, market shares, and growth opportunities of Chip Scale Package (CSP) market by product type, application, key players and key regions and countries.

### **Segmentation by Type:**

Traditional Packaging

Advanced Packaging

### **Segmentation by Application:**

Automotive and Traffic

Consumer Electronics

Communication

Other

**This report also splits the market by region:**

Americas

United States

Canada

Mexico

Brazil

APAC

China

Japan

Korea

Southeast Asia

India

Australia

Europe

Germany

France

UK

Italy

Russia

Middle East & Africa

Egypt

South Africa

Israel

Turkey

GCC Countries

The below companies that are profiled have been selected based on inputs gathered from primary experts and analyzing the company's coverage, product portfolio, its market penetration.

ASE Group

Amkor Technology

JCET

Powertech Technology

TongFu Microelectronics

Tianshui Huatian Technology

UTAC

Chipbond Technology

Hana Micron

OSE

Walton Advanced Engineering

NEPES

Unisem

ChipMOS

Signetics

Carsem

King Yuan ELECTRONICS

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