

# Global Chip Packaging Market Growth (Status and Outlook) 2024-2030

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## Abstracts

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According to our LPI (LP Information) latest study, the global Chip Packaging market size was valued at US\$ 30470 million in 2023. With growing demand in downstream market, the Chip Packaging is forecast to a readjusted size of US\$ 48120 million by 2030 with a CAGR of 6.7% during review period.

The research report highlights the growth potential of the global Chip Packaging market. Chip Packaging are expected to show stable growth in the future market. However, product differentiation, reducing costs, and supply chain optimization remain crucial for the widespread adoption of Chip Packaging. Market players need to invest in research and development, forge strategic partnerships, and align their offerings with evolving consumer preferences to capitalize on the immense opportunities presented by the Chip Packaging market.

Installation of semiconductor integrated circuit chips with shell, plays a part, fixed, sealing, protection chip and enhance the effect of electrical performance, and is the communication bridge of the internal and external circuit chip, chip the contacts with wire is connected to the encapsulation of the shell on the pin of the pins and wires on the PCB and other devices connected. Therefore, encapsulation plays an important role for both CPUs and other LSI integrated circuits. Chip packaging technology has experienced several generations of changes, from DIP, QFP, PGA, BGA, to CSP and then to MCM, technical indicators generation by generation advanced, including chip area and packaging area ratio is more and more close to 1, the application frequency is more and more high, temperature performance is better and better. The number of pins is increased, the pin spacing is reduced, the weight is reduced, the reliability is

improved, the use is more convenient and so on

The global chip package market reached \$25,007.3 million in 2019 and is expected to reach \$35,419.2 million in 2026, with a compound annual growth rate (CAGR) of 6.72%.

By type breakdown, all markets for chip packages can be divided into the following categories:

The first category is mainly traditional packaging, which occupies a relatively large share of the global market, accounting for about 56%.

The other major category is advanced packaging, and for many companies, advanced packaging technology is very attractive, advanced packaging workers. Technology is at the heart of all semiconductor manufacturing processes today. For all semiconductor companies, advanced packaging technology is responding to the direct impact of big trends such as 5G, artificial intelligence and the Internet of Things. Advanced packaging occupies 45% of the market share.

From the regional point of view, Taiwan market output market share is larger, the market accounted for 44%, the next few years will maintain a stable growth. China and the US, with 34 per cent and 11 per cent of the output market respectively, will continue to play a significant role. Any change in China, Taiwan may affect the development trend of chip packaging.

The chip packaging market consists of a group of well-known brand manufacturers and new entrants. The leading global players in the chip packaging market are ASE Group, Amkor Technology, JCET, SPIL, Powertech Technology, Tong Fu Micro Power, Tianshui Huatian Technology, UTAC, Chipbond Technology, Hana Micron and so on. These top 10 companies now account for more than 78% of the total market share.

Key Features:

The report on Chip Packaging market reflects various aspects and provide valuable insights into the industry.

**Market Size and Growth:** The research report provide an overview of the current size and growth of the Chip Packaging market. It may include historical data, market segmentation by Type (e.g., Traditional Packaging, Advanced Packaging), and regional breakdowns.

**Market Drivers and Challenges:** The report can identify and analyse the factors driving the growth of the Chip Packaging market, such as government regulations, environmental concerns, technological advancements, and changing consumer preferences. It can also highlight the challenges faced by the industry, including infrastructure limitations, range anxiety, and high upfront costs.

**Competitive Landscape:** The research report provides analysis of the competitive landscape within the Chip Packaging market. It includes profiles of key players, their market share, strategies, and product offerings. The report can also highlight emerging players and their potential impact on the market.

**Technological Developments:** The research report can delve into the latest technological developments in the Chip Packaging industry. This include advancements in Chip Packaging technology, Chip Packaging new entrants, Chip Packaging new investment, and other innovations that are shaping the future of Chip Packaging.

**Downstream Procumbent Preference:** The report can shed light on customer procumbent behaviour and adoption trends in the Chip Packaging market. It includes factors influencing customer ' purchasing decisions, preferences for Chip Packaging product.

**Government Policies and Incentives:** The research report analyse the impact of government policies and incentives on the Chip Packaging market. This may include an assessment of regulatory frameworks, subsidies, tax incentives, and other measures aimed at promoting Chip Packaging market. The report also evaluates the effectiveness of these policies in driving market growth.

**Environmental Impact and Sustainability:** The research report assess the environmental impact and sustainability aspects of the Chip Packaging market.

**Market Forecasts and Future Outlook:** Based on the analysis conducted, the research report provide market forecasts and outlook for the Chip Packaging industry. This includes projections of market size, growth rates, regional trends, and predictions on technological advancements and policy developments.

**Recommendations and Opportunities:** The report conclude with recommendations for industry stakeholders, policymakers, and investors. It highlights potential opportunities for market players to capitalize on emerging trends, overcome challenges, and

contribute to the growth and development of the Chip Packaging market.

Market Segmentation:

Chip Packaging market is split by Type and by Application. For the period 2019-2030, the growth among segments provides accurate calculations and forecasts for consumption value by Type, and by Application in terms of value.

Segmentation by type

Traditional Packaging

Advanced Packaging

Segmentation by application

Automotive and Traffic

Consumer Electronics

Communication

Other

This report also splits the market by region:

Americas

United States

Canada

Mexico

Brazil

APAC

China

Japan

Korea

Southeast Asia

India

Australia

Europe

Germany

France

UK

Italy

Russia

Middle East & Africa

Egypt

South Africa

Israel

Turkey

GCC Countries

The below companies that are profiled have been selected based on inputs gathered

from primary experts and analyzing the company's coverage, product portfolio, its market penetration.

ASE Group

Amkor Technology

JCET

Siliconware Precision Industries

Powertech Technology

TongFu Microelectronics

Tianshui Huatian Technology

UTAC

Chipbond Technology

Hana Micron

OSE

Walton Advanced Engineering

NEPES

Unisem

ChipMOS

Signetics

Carsem

King Yuan ELECTRONICS

## Contents

### 1 SCOPE OF THE REPORT

- 1.1 Market Introduction
- 1.2 Years Considered
- 1.3 Research Objectives
- 1.4 Market Research Methodology
- 1.5 Research Process and Data Source
- 1.6 Economic Indicators
- 1.7 Currency Considered
- 1.8 Market Estimation Caveats

### 2 EXECUTIVE SUMMARY

- 2.1 World Market Overview
  - 2.1.1 Global Chip Packaging Market Size 2019-2030
  - 2.1.2 Chip Packaging Market Size CAGR by Region 2019 VS 2023 VS 2030
- 2.2 Chip Packaging Segment by Type
  - 2.2.1 Traditional Packaging
  - 2.2.2 Advanced Packaging
- 2.3 Chip Packaging Market Size by Type
  - 2.3.1 Chip Packaging Market Size CAGR by Type (2019 VS 2023 VS 2030)
  - 2.3.2 Global Chip Packaging Market Size Market Share by Type (2019-2024)
- 2.4 Chip Packaging Segment by Application
  - 2.4.1 Automotive and Traffic
  - 2.4.2 Consumer Electronics
  - 2.4.3 Communication
  - 2.4.4 Other
- 2.5 Chip Packaging Market Size by Application
  - 2.5.1 Chip Packaging Market Size CAGR by Application (2019 VS 2023 VS 2030)
  - 2.5.2 Global Chip Packaging Market Size Market Share by Application (2019-2024)

### 3 CHIP PACKAGING MARKET SIZE BY PLAYER

- 3.1 Chip Packaging Market Size Market Share by Players
  - 3.1.1 Global Chip Packaging Revenue by Players (2019-2024)
  - 3.1.2 Global Chip Packaging Revenue Market Share by Players (2019-2024)
- 3.2 Global Chip Packaging Key Players Head office and Products Offered

### 3.3 Market Concentration Rate Analysis

#### 3.3.1 Competition Landscape Analysis

#### 3.3.2 Concentration Ratio (CR3, CR5 and CR10) & (2022-2024)

### 3.4 New Products and Potential Entrants

### 3.5 Mergers & Acquisitions, Expansion

## 4 CHIP PACKAGING BY REGIONS

### 4.1 Chip Packaging Market Size by Regions (2019-2024)

### 4.2 Americas Chip Packaging Market Size Growth (2019-2024)

### 4.3 APAC Chip Packaging Market Size Growth (2019-2024)

### 4.4 Europe Chip Packaging Market Size Growth (2019-2024)

### 4.5 Middle East & Africa Chip Packaging Market Size Growth (2019-2024)

## 5 AMERICAS

### 5.1 Americas Chip Packaging Market Size by Country (2019-2024)

### 5.2 Americas Chip Packaging Market Size by Type (2019-2024)

### 5.3 Americas Chip Packaging Market Size by Application (2019-2024)

### 5.4 United States

### 5.5 Canada

### 5.6 Mexico

### 5.7 Brazil

## 6 APAC

### 6.1 APAC Chip Packaging Market Size by Region (2019-2024)

### 6.2 APAC Chip Packaging Market Size by Type (2019-2024)

### 6.3 APAC Chip Packaging Market Size by Application (2019-2024)

### 6.4 China

### 6.5 Japan

### 6.6 Korea

### 6.7 Southeast Asia

### 6.8 India

### 6.9 Australia

## 7 EUROPE

### 7.1 Europe Chip Packaging by Country (2019-2024)



- 7.2 Europe Chip Packaging Market Size by Type (2019-2024)
- 7.3 Europe Chip Packaging Market Size by Application (2019-2024)
- 7.4 Germany
- 7.5 France
- 7.6 UK
- 7.7 Italy
- 7.8 Russia

## **8 MIDDLE EAST & AFRICA**

- 8.1 Middle East & Africa Chip Packaging by Region (2019-2024)
- 8.2 Middle East & Africa Chip Packaging Market Size by Type (2019-2024)
- 8.3 Middle East & Africa Chip Packaging Market Size by Application (2019-2024)
- 8.4 Egypt
- 8.5 South Africa
- 8.6 Israel
- 8.7 Turkey
- 8.8 GCC Countries

## **9 MARKET DRIVERS, CHALLENGES AND TRENDS**

- 9.1 Market Drivers & Growth Opportunities
- 9.2 Market Challenges & Risks
- 9.3 Industry Trends

## **10 GLOBAL CHIP PACKAGING MARKET FORECAST**

- 10.1 Global Chip Packaging Forecast by Regions (2025-2030)
  - 10.1.1 Global Chip Packaging Forecast by Regions (2025-2030)
  - 10.1.2 Americas Chip Packaging Forecast
  - 10.1.3 APAC Chip Packaging Forecast
  - 10.1.4 Europe Chip Packaging Forecast
  - 10.1.5 Middle East & Africa Chip Packaging Forecast
- 10.2 Americas Chip Packaging Forecast by Country (2025-2030)
  - 10.2.1 United States Chip Packaging Market Forecast
  - 10.2.2 Canada Chip Packaging Market Forecast
  - 10.2.3 Mexico Chip Packaging Market Forecast
  - 10.2.4 Brazil Chip Packaging Market Forecast
- 10.3 APAC Chip Packaging Forecast by Region (2025-2030)

- 10.3.1 China Chip Packaging Market Forecast
- 10.3.2 Japan Chip Packaging Market Forecast
- 10.3.3 Korea Chip Packaging Market Forecast
- 10.3.4 Southeast Asia Chip Packaging Market Forecast
- 10.3.5 India Chip Packaging Market Forecast
- 10.3.6 Australia Chip Packaging Market Forecast
- 10.4 Europe Chip Packaging Forecast by Country (2025-2030)
  - 10.4.1 Germany Chip Packaging Market Forecast
  - 10.4.2 France Chip Packaging Market Forecast
  - 10.4.3 UK Chip Packaging Market Forecast
  - 10.4.4 Italy Chip Packaging Market Forecast
  - 10.4.5 Russia Chip Packaging Market Forecast
- 10.5 Middle East & Africa Chip Packaging Forecast by Region (2025-2030)
  - 10.5.1 Egypt Chip Packaging Market Forecast
  - 10.5.2 South Africa Chip Packaging Market Forecast
  - 10.5.3 Israel Chip Packaging Market Forecast
  - 10.5.4 Turkey Chip Packaging Market Forecast
  - 10.5.5 GCC Countries Chip Packaging Market Forecast
- 10.6 Global Chip Packaging Forecast by Type (2025-2030)
- 10.7 Global Chip Packaging Forecast by Application (2025-2030)

## **11 KEY PLAYERS ANALYSIS**

- 11.1 ASE Group
  - 11.1.1 ASE Group Company Information
  - 11.1.2 ASE Group Chip Packaging Product Offered
  - 11.1.3 ASE Group Chip Packaging Revenue, Gross Margin and Market Share (2019-2024)
  - 11.1.4 ASE Group Main Business Overview
  - 11.1.5 ASE Group Latest Developments
- 11.2 Amkor Technology
  - 11.2.1 Amkor Technology Company Information
  - 11.2.2 Amkor Technology Chip Packaging Product Offered
  - 11.2.3 Amkor Technology Chip Packaging Revenue, Gross Margin and Market Share (2019-2024)
  - 11.2.4 Amkor Technology Main Business Overview
  - 11.2.5 Amkor Technology Latest Developments
- 11.3 JCET
  - 11.3.1 JCET Company Information

- 11.3.2 JCET Chip Packaging Product Offered
- 11.3.3 JCET Chip Packaging Revenue, Gross Margin and Market Share (2019-2024)
- 11.3.4 JCET Main Business Overview
- 11.3.5 JCET Latest Developments
- 11.4 Siliconware Precision Industries
  - 11.4.1 Siliconware Precision Industries Company Information
  - 11.4.2 Siliconware Precision Industries Chip Packaging Product Offered
  - 11.4.3 Siliconware Precision Industries Chip Packaging Revenue, Gross Margin and Market Share (2019-2024)
  - 11.4.4 Siliconware Precision Industries Main Business Overview
  - 11.4.5 Siliconware Precision Industries Latest Developments
- 11.5 Powertech Technology
  - 11.5.1 Powertech Technology Company Information
  - 11.5.2 Powertech Technology Chip Packaging Product Offered
  - 11.5.3 Powertech Technology Chip Packaging Revenue, Gross Margin and Market Share (2019-2024)
  - 11.5.4 Powertech Technology Main Business Overview
  - 11.5.5 Powertech Technology Latest Developments
- 11.6 TongFu Microelectronics
  - 11.6.1 TongFu Microelectronics Company Information
  - 11.6.2 TongFu Microelectronics Chip Packaging Product Offered
  - 11.6.3 TongFu Microelectronics Chip Packaging Revenue, Gross Margin and Market Share (2019-2024)
  - 11.6.4 TongFu Microelectronics Main Business Overview
  - 11.6.5 TongFu Microelectronics Latest Developments
- 11.7 Tianshui Huatian Technology
  - 11.7.1 Tianshui Huatian Technology Company Information
  - 11.7.2 Tianshui Huatian Technology Chip Packaging Product Offered
  - 11.7.3 Tianshui Huatian Technology Chip Packaging Revenue, Gross Margin and Market Share (2019-2024)
  - 11.7.4 Tianshui Huatian Technology Main Business Overview
  - 11.7.5 Tianshui Huatian Technology Latest Developments
- 11.8 UTAC
  - 11.8.1 UTAC Company Information
  - 11.8.2 UTAC Chip Packaging Product Offered
  - 11.8.3 UTAC Chip Packaging Revenue, Gross Margin and Market Share (2019-2024)
  - 11.8.4 UTAC Main Business Overview
  - 11.8.5 UTAC Latest Developments
- 11.9 Chipbond Technology

- 11.9.1 Chipbond Technology Company Information
- 11.9.2 Chipbond Technology Chip Packaging Product Offered
- 11.9.3 Chipbond Technology Chip Packaging Revenue, Gross Margin and Market Share (2019-2024)
- 11.9.4 Chipbond Technology Main Business Overview
- 11.9.5 Chipbond Technology Latest Developments
- 11.10 Hana Micron
  - 11.10.1 Hana Micron Company Information
  - 11.10.2 Hana Micron Chip Packaging Product Offered
  - 11.10.3 Hana Micron Chip Packaging Revenue, Gross Margin and Market Share (2019-2024)
  - 11.10.4 Hana Micron Main Business Overview
  - 11.10.5 Hana Micron Latest Developments
- 11.11 OSE
  - 11.11.1 OSE Company Information
  - 11.11.2 OSE Chip Packaging Product Offered
  - 11.11.3 OSE Chip Packaging Revenue, Gross Margin and Market Share (2019-2024)
  - 11.11.4 OSE Main Business Overview
  - 11.11.5 OSE Latest Developments
- 11.12 Walton Advanced Engineering
  - 11.12.1 Walton Advanced Engineering Company Information
  - 11.12.2 Walton Advanced Engineering Chip Packaging Product Offered
  - 11.12.3 Walton Advanced Engineering Chip Packaging Revenue, Gross Margin and Market Share (2019-2024)
  - 11.12.4 Walton Advanced Engineering Main Business Overview
  - 11.12.5 Walton Advanced Engineering Latest Developments
- 11.13 NEPES
  - 11.13.1 NEPES Company Information
  - 11.13.2 NEPES Chip Packaging Product Offered
  - 11.13.3 NEPES Chip Packaging Revenue, Gross Margin and Market Share (2019-2024)
  - 11.13.4 NEPES Main Business Overview
  - 11.13.5 NEPES Latest Developments
- 11.14 Unisem
  - 11.14.1 Unisem Company Information
  - 11.14.2 Unisem Chip Packaging Product Offered
  - 11.14.3 Unisem Chip Packaging Revenue, Gross Margin and Market Share (2019-2024)
  - 11.14.4 Unisem Main Business Overview

11.14.5 Unisem Latest Developments

## 11.15 ChipMOS

11.15.1 ChipMOS Company Information

11.15.2 ChipMOS Chip Packaging Product Offered

11.15.3 ChipMOS Chip Packaging Revenue, Gross Margin and Market Share  
(2019-2024)

11.15.4 ChipMOS Main Business Overview

11.15.5 ChipMOS Latest Developments

## 11.16 Signetics

11.16.1 Signetics Company Information

11.16.2 Signetics Chip Packaging Product Offered

11.16.3 Signetics Chip Packaging Revenue, Gross Margin and Market Share  
(2019-2024)

11.16.4 Signetics Main Business Overview

11.16.5 Signetics Latest Developments

## 11.17 Carsem

11.17.1 Carsem Company Information

11.17.2 Carsem Chip Packaging Product Offered

11.17.3 Carsem Chip Packaging Revenue, Gross Margin and Market Share  
(2019-2024)

11.17.4 Carsem Main Business Overview

11.17.5 Carsem Latest Developments

## 11.18 King Yuan ELECTRONICS

11.18.1 King Yuan ELECTRONICS Company Information

11.18.2 King Yuan ELECTRONICS Chip Packaging Product Offered

11.18.3 King Yuan ELECTRONICS Chip Packaging Revenue, Gross Margin and  
Market Share (2019-2024)

11.18.4 King Yuan ELECTRONICS Main Business Overview

11.18.5 King Yuan ELECTRONICS Latest Developments

## **12 RESEARCH FINDINGS AND CONCLUSION**

## List Of Tables

### LIST OF TABLES

- Table 1. Chip Packaging Market Size CAGR by Region (2019 VS 2023 VS 2030) & (\$ Millions)
- Table 2. Major Players of Traditional Packaging
- Table 3. Major Players of Advanced Packaging
- Table 4. Chip Packaging Market Size CAGR by Type (2019 VS 2023 VS 2030) & (\$ Millions)
- Table 5. Global Chip Packaging Market Size by Type (2019-2024) & (\$ Millions)
- Table 6. Global Chip Packaging Market Size Market Share by Type (2019-2024)
- Table 7. Chip Packaging Market Size CAGR by Application (2019 VS 2023 VS 2030) & (\$ Millions)
- Table 8. Global Chip Packaging Market Size by Application (2019-2024) & (\$ Millions)
- Table 9. Global Chip Packaging Market Size Market Share by Application (2019-2024)
- Table 10. Global Chip Packaging Revenue by Players (2019-2024) & (\$ Millions)
- Table 11. Global Chip Packaging Revenue Market Share by Player (2019-2024)
- Table 12. Chip Packaging Key Players Head office and Products Offered
- Table 13. Chip Packaging Concentration Ratio (CR3, CR5 and CR10) & (2022-2024)
- Table 14. New Products and Potential Entrants
- Table 15. Mergers & Acquisitions, Expansion
- Table 16. Global Chip Packaging Market Size by Regions 2019-2024 & (\$ Millions)
- Table 17. Global Chip Packaging Market Size Market Share by Regions (2019-2024)
- Table 18. Global Chip Packaging Revenue by Country/Region (2019-2024) & (\$ millions)
- Table 19. Global Chip Packaging Revenue Market Share by Country/Region (2019-2024)
- Table 20. Americas Chip Packaging Market Size by Country (2019-2024) & (\$ Millions)
- Table 21. Americas Chip Packaging Market Size Market Share by Country (2019-2024)
- Table 22. Americas Chip Packaging Market Size by Type (2019-2024) & (\$ Millions)
- Table 23. Americas Chip Packaging Market Size Market Share by Type (2019-2024)
- Table 24. Americas Chip Packaging Market Size by Application (2019-2024) & (\$ Millions)
- Table 25. Americas Chip Packaging Market Size Market Share by Application (2019-2024)
- Table 26. APAC Chip Packaging Market Size by Region (2019-2024) & (\$ Millions)
- Table 27. APAC Chip Packaging Market Size Market Share by Region (2019-2024)
- Table 28. APAC Chip Packaging Market Size by Type (2019-2024) & (\$ Millions)

- Table 29. APAC Chip Packaging Market Size Market Share by Type (2019-2024)
- Table 30. APAC Chip Packaging Market Size by Application (2019-2024) & (\$ Millions)
- Table 31. APAC Chip Packaging Market Size Market Share by Application (2019-2024)
- Table 32. Europe Chip Packaging Market Size by Country (2019-2024) & (\$ Millions)
- Table 33. Europe Chip Packaging Market Size Market Share by Country (2019-2024)
- Table 34. Europe Chip Packaging Market Size by Type (2019-2024) & (\$ Millions)
- Table 35. Europe Chip Packaging Market Size Market Share by Type (2019-2024)
- Table 36. Europe Chip Packaging Market Size by Application (2019-2024) & (\$ Millions)
- Table 37. Europe Chip Packaging Market Size Market Share by Application (2019-2024)
- Table 38. Middle East & Africa Chip Packaging Market Size by Region (2019-2024) & (\$ Millions)
- Table 39. Middle East & Africa Chip Packaging Market Size Market Share by Region (2019-2024)
- Table 40. Middle East & Africa Chip Packaging Market Size by Type (2019-2024) & (\$ Millions)
- Table 41. Middle East & Africa Chip Packaging Market Size Market Share by Type (2019-2024)
- Table 42. Middle East & Africa Chip Packaging Market Size by Application (2019-2024) & (\$ Millions)
- Table 43. Middle East & Africa Chip Packaging Market Size Market Share by Application (2019-2024)
- Table 44. Key Market Drivers & Growth Opportunities of Chip Packaging
- Table 45. Key Market Challenges & Risks of Chip Packaging
- Table 46. Key Industry Trends of Chip Packaging
- Table 47. Global Chip Packaging Market Size Forecast by Regions (2025-2030) & (\$ Millions)
- Table 48. Global Chip Packaging Market Size Market Share Forecast by Regions (2025-2030)
- Table 49. Global Chip Packaging Market Size Forecast by Type (2025-2030) & (\$ Millions)
- Table 50. Global Chip Packaging Market Size Forecast by Application (2025-2030) & (\$ Millions)
- Table 51. ASE Group Details, Company Type, Chip Packaging Area Served and Its Competitors
- Table 52. ASE Group Chip Packaging Product Offered
- Table 53. ASE Group Chip Packaging Revenue (\$ million), Gross Margin and Market Share (2019-2024)
- Table 54. ASE Group Main Business

Table 55. ASE Group Latest Developments

Table 56. Amkor Technology Details, Company Type, Chip Packaging Area Served and Its Competitors

Table 57. Amkor Technology Chip Packaging Product Offered

Table 58. Amkor Technology Main Business

Table 59. Amkor Technology Chip Packaging Revenue (\$ million), Gross Margin and Market Share (2019-2024)

Table 60. Amkor Technology Latest Developments

Table 61. JCET Details, Company Type, Chip Packaging Area Served and Its Competitors

Table 62. JCET Chip Packaging Product Offered

Table 63. JCET Main Business

Table 64. JCET Chip Packaging Revenue (\$ million), Gross Margin and Market Share (2019-2024)

Table 65. JCET Latest Developments

Table 66. Siliconware Precision Industries Details, Company Type, Chip Packaging Area Served and Its Competitors

Table 67. Siliconware Precision Industries Chip Packaging Product Offered

Table 68. Siliconware Precision Industries Main Business

Table 69. Siliconware Precision Industries Chip Packaging Revenue (\$ million), Gross Margin and Market Share (2019-2024)

Table 70. Siliconware Precision Industries Latest Developments

Table 71. Powertech Technology Details, Company Type, Chip Packaging Area Served and Its Competitors

Table 72. Powertech Technology Chip Packaging Product Offered

Table 73. Powertech Technology Main Business

Table 74. Powertech Technology Chip Packaging Revenue (\$ million), Gross Margin and Market Share (2019-2024)

Table 75. Powertech Technology Latest Developments

Table 76. TongFu Microelectronics Details, Company Type, Chip Packaging Area Served and Its Competitors

Table 77. TongFu Microelectronics Chip Packaging Product Offered

Table 78. TongFu Microelectronics Main Business

Table 79. TongFu Microelectronics Chip Packaging Revenue (\$ million), Gross Margin and Market Share (2019-2024)

Table 80. TongFu Microelectronics Latest Developments

Table 81. Tianshui Huatian Technology Details, Company Type, Chip Packaging Area Served and Its Competitors

Table 82. Tianshui Huatian Technology Chip Packaging Product Offered



- Table 83. Tianshui Huatian Technology Main Business
- Table 84. Tianshui Huatian Technology Chip Packaging Revenue (\$ million), Gross Margin and Market Share (2019-2024)
- Table 85. Tianshui Huatian Technology Latest Developments
- Table 86. UTAC Details, Company Type, Chip Packaging Area Served and Its Competitors
- Table 87. UTAC Chip Packaging Product Offered
- Table 88. UTAC Main Business
- Table 89. UTAC Chip Packaging Revenue (\$ million), Gross Margin and Market Share (2019-2024)
- Table 90. UTAC Latest Developments
- Table 91. Chipbond Technology Details, Company Type, Chip Packaging Area Served and Its Competitors
- Table 92. Chipbond Technology Chip Packaging Product Offered
- Table 93. Chipbond Technology Main Business
- Table 94. Chipbond Technology Chip Packaging Revenue (\$ million), Gross Margin and Market Share (2019-2024)
- Table 95. Chipbond Technology Latest Developments
- Table 96. Hana Micron Details, Company Type, Chip Packaging Area Served and Its Competitors
- Table 97. Hana Micron Chip Packaging Product Offered
- Table 98. Hana Micron Main Business
- Table 99. Hana Micron Chip Packaging Revenue (\$ million), Gross Margin and Market Share (2019-2024)
- Table 100. Hana Micron Latest Developments
- Table 101. OSE Details, Company Type, Chip Packaging Area Served and Its Competitors
- Table 102. OSE Chip Packaging Product Offered
- Table 103. OSE Chip Packaging Revenue (\$ million), Gross Margin and Market Share (2019-2024)
- Table 104. OSE Main Business
- Table 105. OSE Latest Developments
- Table 106. Walton Advanced Engineering Details, Company Type, Chip Packaging Area Served and Its Competitors
- Table 107. Walton Advanced Engineering Chip Packaging Product Offered
- Table 108. Walton Advanced Engineering Main Business
- Table 109. Walton Advanced Engineering Chip Packaging Revenue (\$ million), Gross Margin and Market Share (2019-2024)
- Table 110. Walton Advanced Engineering Latest Developments

Table 111. NEPES Details, Company Type, Chip Packaging Area Served and Its Competitors

Table 112. NEPES Chip Packaging Product Offered

Table 113. NEPES Main Business

Table 114. NEPES Chip Packaging Revenue (\$ million), Gross Margin and Market Share (2019-2024)

Table 115. NEPES Latest Developments

Table 116. Unisem Details, Company Type, Chip Packaging Area Served and Its Competitors

Table 117. Unisem Chip Packaging Product Offered

Table 118. Unisem Main Business

Table 119. Unisem Chip Packaging Revenue (\$ million), Gross Margin and Market Share (2019-2024)

Table 120. Unisem Latest Developments

Table 121. ChipMOS Details, Company Type, Chip Packaging Area Served and Its Competitors

Table 122. ChipMOS Chip Packaging Product Offered

Table 123. ChipMOS Main Business

Table 124. ChipMOS Chip Packaging Revenue (\$ million), Gross Margin and Market Share (2019-2024)

Table 125. ChipMOS Latest Developments

Table 126. Signetics Details, Company Type, Chip Packaging Area Served and Its Competitors

Table 127. Signetics Chip Packaging Product Offered

Table 128. Signetics Main Business

Table 129. Signetics Chip Packaging Revenue (\$ million), Gross Margin and Market Share (2019-2024)

Table 130. Signetics Latest Developments

Table 131. Carsem Details, Company Type, Chip Packaging Area Served and Its Competitors

Table 132. Carsem Chip Packaging Product Offered

Table 133. Carsem Main Business

Table 134. Carsem Chip Packaging Revenue (\$ million), Gross Margin and Market Share (2019-2024)

Table 135. Carsem Latest Developments

Table 136. King Yuan ELECTRONICS Details, Company Type, Chip Packaging Area Served and Its Competitors

Table 137. King Yuan ELECTRONICS Chip Packaging Product Offered

Table 138. King Yuan ELECTRONICS Main Business

Table 139. King Yuan ELECTRONICS Chip Packaging Revenue (\$ million), Gross Margin and Market Share (2019-2024)

Table 140. King Yuan ELECTRONICS Latest Developments

## List Of Figures

### LIST OF FIGURES

- Figure 1. Chip Packaging Report Years Considered
- Figure 2. Research Objectives
- Figure 3. Research Methodology
- Figure 4. Research Process and Data Source
- Figure 5. Global Chip Packaging Market Size Growth Rate 2019-2030 (\$ Millions)
- Figure 6. Chip Packaging Sales by Geographic Region (2019, 2023 & 2030) & (\$ millions)
- Figure 7. Chip Packaging Sales Market Share by Country/Region (2023)
- Figure 8. Chip Packaging Sales Market Share by Country/Region (2019, 2023 & 2030)
- Figure 9. Global Chip Packaging Market Size Market Share by Type in 2023
- Figure 10. Chip Packaging in Automotive and Traffic
- Figure 11. Global Chip Packaging Market: Automotive and Traffic (2019-2024) & (\$ Millions)
- Figure 12. Chip Packaging in Consumer Electronics
- Figure 13. Global Chip Packaging Market: Consumer Electronics (2019-2024) & (\$ Millions)
- Figure 14. Chip Packaging in Communication
- Figure 15. Global Chip Packaging Market: Communication (2019-2024) & (\$ Millions)
- Figure 16. Chip Packaging in Other
- Figure 17. Global Chip Packaging Market: Other (2019-2024) & (\$ Millions)
- Figure 18. Global Chip Packaging Market Size Market Share by Application in 2023
- Figure 19. Global Chip Packaging Revenue Market Share by Player in 2023
- Figure 20. Global Chip Packaging Market Size Market Share by Regions (2019-2024)
- Figure 21. Americas Chip Packaging Market Size 2019-2024 (\$ Millions)
- Figure 22. APAC Chip Packaging Market Size 2019-2024 (\$ Millions)
- Figure 23. Europe Chip Packaging Market Size 2019-2024 (\$ Millions)
- Figure 24. Middle East & Africa Chip Packaging Market Size 2019-2024 (\$ Millions)
- Figure 25. Americas Chip Packaging Value Market Share by Country in 2023
- Figure 26. United States Chip Packaging Market Size Growth 2019-2024 (\$ Millions)
- Figure 27. Canada Chip Packaging Market Size Growth 2019-2024 (\$ Millions)
- Figure 28. Mexico Chip Packaging Market Size Growth 2019-2024 (\$ Millions)
- Figure 29. Brazil Chip Packaging Market Size Growth 2019-2024 (\$ Millions)
- Figure 30. APAC Chip Packaging Market Size Market Share by Region in 2023
- Figure 31. APAC Chip Packaging Market Size Market Share by Type in 2023
- Figure 32. APAC Chip Packaging Market Size Market Share by Application in 2023

- Figure 33. China Chip Packaging Market Size Growth 2019-2024 (\$ Millions)
- Figure 34. Japan Chip Packaging Market Size Growth 2019-2024 (\$ Millions)
- Figure 35. Korea Chip Packaging Market Size Growth 2019-2024 (\$ Millions)
- Figure 36. Southeast Asia Chip Packaging Market Size Growth 2019-2024 (\$ Millions)
- Figure 37. India Chip Packaging Market Size Growth 2019-2024 (\$ Millions)
- Figure 38. Australia Chip Packaging Market Size Growth 2019-2024 (\$ Millions)
- Figure 39. Europe Chip Packaging Market Size Market Share by Country in 2023
- Figure 40. Europe Chip Packaging Market Size Market Share by Type (2019-2024)
- Figure 41. Europe Chip Packaging Market Size Market Share by Application (2019-2024)
- Figure 42. Germany Chip Packaging Market Size Growth 2019-2024 (\$ Millions)
- Figure 43. France Chip Packaging Market Size Growth 2019-2024 (\$ Millions)
- Figure 44. UK Chip Packaging Market Size Growth 2019-2024 (\$ Millions)
- Figure 45. Italy Chip Packaging Market Size Growth 2019-2024 (\$ Millions)
- Figure 46. Russia Chip Packaging Market Size Growth 2019-2024 (\$ Millions)
- Figure 47. Middle East & Africa Chip Packaging Market Size Market Share by Region (2019-2024)
- Figure 48. Middle East & Africa Chip Packaging Market Size Market Share by Type (2019-2024)
- Figure 49. Middle East & Africa Chip Packaging Market Size Market Share by Application (2019-2024)
- Figure 50. Egypt Chip Packaging Market Size Growth 2019-2024 (\$ Millions)
- Figure 51. South Africa Chip Packaging Market Size Growth 2019-2024 (\$ Millions)
- Figure 52. Israel Chip Packaging Market Size Growth 2019-2024 (\$ Millions)
- Figure 53. Turkey Chip Packaging Market Size Growth 2019-2024 (\$ Millions)
- Figure 54. GCC Country Chip Packaging Market Size Growth 2019-2024 (\$ Millions)
- Figure 55. Americas Chip Packaging Market Size 2025-2030 (\$ Millions)
- Figure 56. APAC Chip Packaging Market Size 2025-2030 (\$ Millions)
- Figure 57. Europe Chip Packaging Market Size 2025-2030 (\$ Millions)
- Figure 58. Middle East & Africa Chip Packaging Market Size 2025-2030 (\$ Millions)
- Figure 59. United States Chip Packaging Market Size 2025-2030 (\$ Millions)
- Figure 60. Canada Chip Packaging Market Size 2025-2030 (\$ Millions)
- Figure 61. Mexico Chip Packaging Market Size 2025-2030 (\$ Millions)
- Figure 62. Brazil Chip Packaging Market Size 2025-2030 (\$ Millions)
- Figure 63. China Chip Packaging Market Size 2025-2030 (\$ Millions)
- Figure 64. Japan Chip Packaging Market Size 2025-2030 (\$ Millions)
- Figure 65. Korea Chip Packaging Market Size 2025-2030 (\$ Millions)
- Figure 66. Southeast Asia Chip Packaging Market Size 2025-2030 (\$ Millions)
- Figure 67. India Chip Packaging Market Size 2025-2030 (\$ Millions)

- Figure 68. Australia Chip Packaging Market Size 2025-2030 (\$ Millions)
- Figure 69. Germany Chip Packaging Market Size 2025-2030 (\$ Millions)
- Figure 70. France Chip Packaging Market Size 2025-2030 (\$ Millions)
- Figure 71. UK Chip Packaging Market Size 2025-2030 (\$ Millions)
- Figure 72. Italy Chip Packaging Market Size 2025-2030 (\$ Millions)
- Figure 73. Russia Chip Packaging Market Size 2025-2030 (\$ Millions)
- Figure 74. Spain Chip Packaging Market Size 2025-2030 (\$ Millions)
- Figure 75. Egypt Chip Packaging Market Size 2025-2030 (\$ Millions)
- Figure 76. South Africa Chip Packaging Market Size 2025-2030 (\$ Millions)
- Figure 77. Israel Chip Packaging Market Size 2025-2030 (\$ Millions)
- Figure 78. Turkey Chip Packaging Market Size 2025-2030 (\$ Millions)
- Figure 79. GCC Countries Chip Packaging Market Size 2025-2030 (\$ Millions)
- Figure 80. Global Chip Packaging Market Size Market Share Forecast by Type (2025-2030)
- Figure 81. Global Chip Packaging Market Size Market Share Forecast by Application (2025-2030)

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