

Global Chip Level Sintered Silver Paste Market Growth 2026-2032

<https://marketpublishers.com/r/GB3576CD2D76EN.html>

Date: October 2026

Pages: 110

Price: US\$ 3,660.00 (Single User License)

ID: GB3576CD2D76EN

Abstracts

The global Chip Level Sintered Silver Paste market size is predicted to grow from US\$ 177 million in 2025 to US\$ 335 million in 2032; it is expected to grow at a CAGR of 9.5% from 2026 to 2032.

Chip level sintered silver paste is a silver based low-temperature sintering material used for semiconductor chip mounting and high reliability packaging interconnection. It is usually composed of nano silver powder, micro silver powder or micro nano composite silver powder, organic carrier, dispersant, rheological modifier, surface modifier, and sintering aid components. Its core function is to form a dense silver sintered connection layer through pressure sintering or pressureless sintering at lower process temperatures, achieving high thermal conductivity, high electrical conductivity, and high reliability connection between the chip and DBC substrate, AMB substrate, metal substrate, lead frame, copper clip, or heat dissipation structure. Compared with traditional solder, the silver connection layer formed by chip level sintered silver paste has a higher melting point, lower thermal resistance, stronger high-temperature resistance, and better power cycle reliability, which is particularly suitable for SiC power devices, GaN devices, IGBT modules MOSFET? Scenarios such as power ICs, automotive grade power modules, high-power LEDs, photovoltaic energy storage inverters, AI server power supplies, and aerospace high reliability electronic packaging. In 2025, global Chip Level Sintered Silver Paste production reached approximately 62.38 MT, with an average global market price of around US\$ 2,896 per kg. The annual production capacity of chip level sintered silver paste is 100 tons, with a gross profit margin of about 45%.

The upstream mainly includes nano silver powder, silver salt precursor, organic carrier, solvent, dispersant, rheological agent, surface modifier, sintering aid material,

packaging material, low-temperature storage and transportation material, sintering equipment, printing and dispensing equipment, and reliability testing equipment.

The downstream is mainly used for SiC power modules, GaN devices, IGBT modules MOSFET? Power ICs, automotive inverters, on-board OBC, DC DC, DBC substrates, AMB substrates, high-power LEDs, photovoltaic energy storage inverters, AI server power supplies, and aerospace high reliability electronic packaging.

Nano silver powder accounts for about 55% to 70% of the total cost, organic carriers, dispersants, rheological agents, surface modifiers, and sintering aids account for about 8% to 15%, research and development verification, reliability testing, and customer certification account for about 6% to 12%, production and manufacturing, clean environment, equipment depreciation, and yield loss account for about 7% to 12%, and packaging, warehousing, low-temperature transportation, quality management, and sales management account for about 5% to 10%.

In the field of semiconductor packaging, chip heat dissipation has always been a key challenge restricting the development of high-performance devices. As a new type of lead-free interconnect material, sintered silver paste has become a "secret weapon" for solving the heat dissipation problem of high-power chips due to its high thermal conductivity, high reliability, and low-temperature process compatibility. The sintered silver paste achieves a leapfrog improvement in chip heat dissipation performance through the solid-state diffusion mechanism of nano silver particles. Its high thermal conductivity, low-temperature process compatibility, and reliability make it an irreplaceable heat dissipation solution in fields such as new energy vehicles, 5G communication, AI chips, etc. Despite the challenges of cost and process control, with the advancement of materials science and manufacturing processes, sintered silver paste is expected to further promote the development of semiconductor packaging technology towards high power density, miniaturization, and green direction, reshaping the global semiconductor industry landscape.

LP Information, Inc. (LPI) ' newest research report, the "Chip Level Sintered Silver Paste Industry Forecast" looks at past sales and reviews total world Chip Level Sintered Silver Paste sales in 2025, providing a comprehensive analysis by region and market sector of projected Chip Level Sintered Silver Paste sales for 2026 through 2032. With Chip Level Sintered Silver Paste sales broken down by region, market sector and sub-sector, this report provides a detailed analysis in US\$ millions of the world Chip Level Sintered Silver Paste industry.

This Insight Report provides a comprehensive analysis of the global Chip Level Sintered Silver Paste landscape and highlights key trends related to product segmentation, company formation, revenue, and market share, latest development, and M&A activity. This report also analyzes the strategies of leading global companies with a focus on Chip Level Sintered Silver Paste portfolios and capabilities, market entry strategies, market positions, and geographic footprints, to better understand these firms' unique position in an accelerating global Chip Level Sintered Silver Paste market.

This Insight Report evaluates the key market trends, drivers, and affecting factors shaping the global outlook for Chip Level Sintered Silver Paste and breaks down the forecast by Type, by Application, geography, and market size to highlight emerging pockets of opportunity. With a transparent methodology based on hundreds of bottom-up qualitative and quantitative market inputs, this study forecast offers a highly nuanced view of the current state and future trajectory in the global Chip Level Sintered Silver Paste.

This report presents a comprehensive overview, market shares, and growth opportunities of Chip Level Sintered Silver Paste market by product type, application, key manufacturers and key regions and countries.

Segmentation by Type:

Pressure Sintering Paste

Pressureless Sintering Paste

Segmentation by Product Form:

Printed Sintered Silver Paste

Glue Type Sintered Silver Paste

Segmentation by Sintering Time:

Sintering Time?5min

Sintering Time>5min

Segmentation by Application:

New Energy Vehicle Power Module

AI Chip

5G Communication & Data Center

Consumer Electronics

Aerospace and Military Industry

Other

This report also splits the market by region:

Americas

United States

Canada

Mexico

Brazil

APAC

China

Japan

Korea

Southeast Asia

India

Australia

Europe

Germany

France

UK

Italy

Russia

Middle East & Africa

Egypt

South Africa

Israel

Turkey

GCC Countries

The below companies that are profiled have been selected based on inputs gathered from primary experts and analysing the company's coverage, product portfolio, its market penetration.

Heraeus Electronics

Indium Corporation

Nihon Superior

MacDermid Alpha

Henkel

KYOCERA Corporation

NAMICS CORPORATION

TANAKA PRECIOUS METAL GROUP

Bando Chemical Industries

NBE Tech

Shenzhen Xinyuan New Materials

Solderwell Advanced Materials

Shenzhen Jufeng Solder

Qinglian Technology

Shenzhen Advanced Joining Technology

Key Questions Addressed in this Report

What is the 10-year outlook for the global Chip Level Sintered Silver Paste market?

What factors are driving Chip Level Sintered Silver Paste market growth, globally and by region?

Which technologies are poised for the fastest growth by market and region?

How do Chip Level Sintered Silver Paste market opportunities vary by end market size?

How does Chip Level Sintered Silver Paste break out by Type, by Application?

Contents

1 SCOPE OF THE REPORT

- 1.1 Market Introduction
- 1.2 Years Considered
- 1.3 Research Objectives
- 1.4 Market Research Methodology
- 1.5 Research Process and Data Source
- 1.6 Economic Indicators
- 1.7 Currency Considered
- 1.8 Market Estimation Caveats

2 EXECUTIVE SUMMARY

2.1 World Market Overview

- 2.1.1 Global Chip Level Sintered Silver Paste Annual Sales 2021-2032
- 2.1.2 World Current & Future Analysis for Chip Level Sintered Silver Paste by Geographic Region, 2021, 2025 & 2032
- 2.1.3 World Current & Future Analysis for Chip Level Sintered Silver Paste by Country/Region, 2021, 2025 & 2032
- 2.2 Chip Level Sintered Silver Paste Segment by Type
 - 2.2.1 Pressure Sintering Paste
 - 2.2.2 Pressureless Sintering Paste
 - 2.2.3 Chip Level Sintered Silver Paste Sales by Type
 - 2.2.3.1 Global Chip Level Sintered Silver Paste Sales Market Share by Type (2021-2026)
 - 2.2.3.2 Global Chip Level Sintered Silver Paste Revenue and Market Share by Type (2021-2026)
 - 2.2.3.3 Global Chip Level Sintered Silver Paste Sale Price by Type (2021-2026)
- 2.3 Chip Level Sintered Silver Paste Segment by Product Form
 - 2.3.1 Printed Sintered Silver Paste
 - 2.3.2 Glue Type Sintered Silver Paste
 - 2.3.3 Chip Level Sintered Silver Paste Sales by Product Form
 - 2.3.3.1 Global Chip Level Sintered Silver Paste Sales Market Share by Product Form (2021-2026)
 - 2.3.3.2 Global Chip Level Sintered Silver Paste Revenue and Market Share by Product Form (2021-2026)
 - 2.3.3.3 Global Chip Level Sintered Silver Paste Sale Price by Product Form

(2021-2026)

2.4 Chip Level Sintered Silver Paste Segment by Sintering Time

2.4.1 Sintering Time≤5min

2.4.2 Sintering Time>5min

2.4.3 Chip Level Sintered Silver Paste Sales by Sintering Time

2.4.3.1 Global Chip Level Sintered Silver Paste Sales Market Share by Sintering Time (2021-2026)

2.4.3.2 Global Chip Level Sintered Silver Paste Revenue and Market Share by Sintering Time (2021-2026)

2.4.3.3 Global Chip Level Sintered Silver Paste Sale Price by Sintering Time (2021-2026)

2.5 Chip Level Sintered Silver Paste Segment by Application

2.5.1 New Energy Vehicle Power Module

2.5.2 AI Chip

2.5.3 5G Communication & Data Center

2.5.4 Consumer Electronics

2.5.5 Aerospace and Military Industry

2.5.6 Other

2.5.7 Chip Level Sintered Silver Paste Sales by Application

2.5.7.1 Global Chip Level Sintered Silver Paste Sale Market Share by Application (2021-2026)

2.5.7.2 Global Chip Level Sintered Silver Paste Revenue and Market Share by Application (2021-2026)

2.5.7.3 Global Chip Level Sintered Silver Paste Sale Price by Application (2021-2026)

3 GLOBAL BY COMPANY

3.1 Global Chip Level Sintered Silver Paste Breakdown Data by Company

3.1.1 Global Chip Level Sintered Silver Paste Annual Sales by Company (2021-2026)

3.1.2 Global Chip Level Sintered Silver Paste Sales Market Share by Company (2021-2026)

3.2 Global Chip Level Sintered Silver Paste Annual Revenue by Company (2021-2026)

3.2.1 Global Chip Level Sintered Silver Paste Revenue by Company (2021-2026)

3.2.2 Global Chip Level Sintered Silver Paste Revenue Market Share by Company (2021-2026)

3.3 Global Chip Level Sintered Silver Paste Sale Price by Company

3.4 Key Manufacturers Chip Level Sintered Silver Paste Producing Area Distribution, Sales Area, Product Type

- 3.4.1 Key Manufacturers Chip Level Sintered Silver Paste Product Location Distribution
- 3.4.2 Players Chip Level Sintered Silver Paste Products Offered
- 3.5 Market Concentration Rate Analysis
 - 3.5.1 Competition Landscape Analysis
 - 3.5.2 Concentration Ratio (CR3, CR5 and CR10) & (2024-2026)
- 3.6 New Products and Potential Entrants
- 3.7 Market M&A Activity & Strategy

4 WORLD HISTORIC REVIEW FOR CHIP LEVEL SINTERED SILVER PASTE BY GEOGRAPHIC REGION

- 4.1 World Historic Chip Level Sintered Silver Paste Market Size by Geographic Region (2021-2026)
 - 4.1.1 Global Chip Level Sintered Silver Paste Annual Sales by Geographic Region (2021-2026)
 - 4.1.2 Global Chip Level Sintered Silver Paste Annual Revenue by Geographic Region (2021-2026)
- 4.2 World Historic Chip Level Sintered Silver Paste Market Size by Country/Region (2021-2026)
 - 4.2.1 Global Chip Level Sintered Silver Paste Annual Sales by Country/Region (2021-2026)
 - 4.2.2 Global Chip Level Sintered Silver Paste Annual Revenue by Country/Region (2021-2026)
- 4.3 Americas Chip Level Sintered Silver Paste Sales Growth
- 4.4 APAC Chip Level Sintered Silver Paste Sales Growth
- 4.5 Europe Chip Level Sintered Silver Paste Sales Growth
- 4.6 Middle East & Africa Chip Level Sintered Silver Paste Sales Growth

5 AMERICAS

- 5.1 Americas Chip Level Sintered Silver Paste Sales by Country
 - 5.1.1 Americas Chip Level Sintered Silver Paste Sales by Country (2021-2026)
 - 5.1.2 Americas Chip Level Sintered Silver Paste Revenue by Country (2021-2026)
- 5.2 Americas Chip Level Sintered Silver Paste Sales by Type (2021-2026)
- 5.3 Americas Chip Level Sintered Silver Paste Sales by Application (2021-2026)
- 5.4 United States
- 5.5 Canada
- 5.6 Mexico

5.7 Brazil

6 APAC

6.1 APAC Chip Level Sintered Silver Paste Sales by Region

6.1.1 APAC Chip Level Sintered Silver Paste Sales by Region (2021-2026)

6.1.2 APAC Chip Level Sintered Silver Paste Revenue by Region (2021-2026)

6.2 APAC Chip Level Sintered Silver Paste Sales by Type (2021-2026)

6.3 APAC Chip Level Sintered Silver Paste Sales by Application (2021-2026)

6.4 China

6.5 Japan

6.6 South Korea

6.7 Southeast Asia

6.8 India

6.9 Australia

6.10 China Taiwan

7 EUROPE

7.1 Europe Chip Level Sintered Silver Paste by Country

7.1.1 Europe Chip Level Sintered Silver Paste Sales by Country (2021-2026)

7.1.2 Europe Chip Level Sintered Silver Paste Revenue by Country (2021-2026)

7.2 Europe Chip Level Sintered Silver Paste Sales by Type (2021-2026)

7.3 Europe Chip Level Sintered Silver Paste Sales by Application (2021-2026)

7.4 Germany

7.5 France

7.6 UK

7.7 Italy

7.8 Russia

8 MIDDLE EAST & AFRICA

8.1 Middle East & Africa Chip Level Sintered Silver Paste by Country

8.1.1 Middle East & Africa Chip Level Sintered Silver Paste Sales by Country (2021-2026)

8.1.2 Middle East & Africa Chip Level Sintered Silver Paste Revenue by Country (2021-2026)

8.2 Middle East & Africa Chip Level Sintered Silver Paste Sales by Type (2021-2026)

8.3 Middle East & Africa Chip Level Sintered Silver Paste Sales by Application

(2021-2026)

8.4 Egypt

8.5 South Africa

8.6 Israel

8.7 Turkey

8.8 GCC Countries

9 MARKET DRIVERS, CHALLENGES AND TRENDS

9.1 Market Drivers & Growth Opportunities

9.2 Market Challenges & Risks

9.3 Industry Trends

10 MANUFACTURING COST STRUCTURE ANALYSIS

10.1 Raw Material and Suppliers

10.2 Manufacturing Cost Structure Analysis of Chip Level Sintered Silver Paste

10.3 Manufacturing Process Analysis of Chip Level Sintered Silver Paste

10.4 Industry Chain Structure of Chip Level Sintered Silver Paste

11 MARKETING, DISTRIBUTORS AND CUSTOMER

11.1 Sales Channel

11.1.1 Direct Channels

11.1.2 Indirect Channels

11.2 Chip Level Sintered Silver Paste Distributors

11.3 Chip Level Sintered Silver Paste Customer

12 WORLD FORECAST REVIEW FOR CHIP LEVEL SINTERED SILVER PASTE BY GEOGRAPHIC REGION

12.1 Global Chip Level Sintered Silver Paste Market Size Forecast by Region

12.1.1 Global Chip Level Sintered Silver Paste Forecast by Region (2027-2032)

12.1.2 Global Chip Level Sintered Silver Paste Annual Revenue Forecast by Region (2027-2032)

12.2 Americas Forecast by Country (2027-2032)

12.3 APAC Forecast by Region (2027-2032)

12.4 Europe Forecast by Country (2027-2032)

12.5 Middle East & Africa Forecast by Country (2027-2032)

12.6 Global Chip Level Sintered Silver Paste Forecast by Type (2027-2032)

12.7 Global Chip Level Sintered Silver Paste Forecast by Application (2027-2032)

13 KEY PLAYERS ANALYSIS

13.1 Heraeus Electronics

13.1.1 Heraeus Electronics Company Information

13.1.2 Heraeus Electronics Chip Level Sintered Silver Paste Product Portfolios and Specifications

13.1.3 Heraeus Electronics Chip Level Sintered Silver Paste Sales, Revenue, Price and Gross Margin (2021-2026)

13.1.4 Heraeus Electronics Main Business Overview

13.1.5 Heraeus Electronics Latest Developments

13.2 Indium Corporation

13.2.1 Indium Corporation Company Information

13.2.2 Indium Corporation Chip Level Sintered Silver Paste Product Portfolios and Specifications

13.2.3 Indium Corporation Chip Level Sintered Silver Paste Sales, Revenue, Price and Gross Margin (2021-2026)

13.2.4 Indium Corporation Main Business Overview

13.2.5 Indium Corporation Latest Developments

13.3 Nihon Superior

13.3.1 Nihon Superior Company Information

13.3.2 Nihon Superior Chip Level Sintered Silver Paste Product Portfolios and Specifications

13.3.3 Nihon Superior Chip Level Sintered Silver Paste Sales, Revenue, Price and Gross Margin (2021-2026)

13.3.4 Nihon Superior Main Business Overview

13.3.5 Nihon Superior Latest Developments

13.4 MacDermid Alpha

13.4.1 MacDermid Alpha Company Information

13.4.2 MacDermid Alpha Chip Level Sintered Silver Paste Product Portfolios and Specifications

13.4.3 MacDermid Alpha Chip Level Sintered Silver Paste Sales, Revenue, Price and Gross Margin (2021-2026)

13.4.4 MacDermid Alpha Main Business Overview

13.4.5 MacDermid Alpha Latest Developments

13.5 Henkel

13.5.1 Henkel Company Information

- 13.5.2 Henkel Chip Level Sintered Silver Paste Product Portfolios and Specifications
- 13.5.3 Henkel Chip Level Sintered Silver Paste Sales, Revenue, Price and Gross Margin (2021-2026)
- 13.5.4 Henkel Main Business Overview
- 13.5.5 Henkel Latest Developments
- 13.6 KYOCERA Corporation
 - 13.6.1 KYOCERA Corporation Company Information
 - 13.6.2 KYOCERA Corporation Chip Level Sintered Silver Paste Product Portfolios and Specifications
 - 13.6.3 KYOCERA Corporation Chip Level Sintered Silver Paste Sales, Revenue, Price and Gross Margin (2021-2026)
 - 13.6.4 KYOCERA Corporation Main Business Overview
 - 13.6.5 KYOCERA Corporation Latest Developments
- 13.7 NAMICS CORPORATION
 - 13.7.1 NAMICS CORPORATION Company Information
 - 13.7.2 NAMICS CORPORATION Chip Level Sintered Silver Paste Product Portfolios and Specifications
 - 13.7.3 NAMICS CORPORATION Chip Level Sintered Silver Paste Sales, Revenue, Price and Gross Margin (2021-2026)
 - 13.7.4 NAMICS CORPORATION Main Business Overview
 - 13.7.5 NAMICS CORPORATION Latest Developments
- 13.8 TANAKA PRECIOUS METAL GROUP
 - 13.8.1 TANAKA PRECIOUS METAL GROUP Company Information
 - 13.8.2 TANAKA PRECIOUS METAL GROUP Chip Level Sintered Silver Paste Product Portfolios and Specifications
 - 13.8.3 TANAKA PRECIOUS METAL GROUP Chip Level Sintered Silver Paste Sales, Revenue, Price and Gross Margin (2021-2026)
 - 13.8.4 TANAKA PRECIOUS METAL GROUP Main Business Overview
 - 13.8.5 TANAKA PRECIOUS METAL GROUP Latest Developments
- 13.9 Bando Chemical Industries
 - 13.9.1 Bando Chemical Industries Company Information
 - 13.9.2 Bando Chemical Industries Chip Level Sintered Silver Paste Product Portfolios and Specifications
 - 13.9.3 Bando Chemical Industries Chip Level Sintered Silver Paste Sales, Revenue, Price and Gross Margin (2021-2026)
 - 13.9.4 Bando Chemical Industries Main Business Overview
 - 13.9.5 Bando Chemical Industries Latest Developments
- 13.10 NBE Tech
 - 13.10.1 NBE Tech Company Information

13.10.2 NBE Tech Chip Level Sintered Silver Paste Product Portfolios and Specifications

13.10.3 NBE Tech Chip Level Sintered Silver Paste Sales, Revenue, Price and Gross Margin (2021-2026)

13.10.4 NBE Tech Main Business Overview

13.10.5 NBE Tech Latest Developments

13.11 Shenzhen Xinyuan New Materials

13.11.1 Shenzhen Xinyuan New Materials Company Information

13.11.2 Shenzhen Xinyuan New Materials Chip Level Sintered Silver Paste Product Portfolios and Specifications

13.11.3 Shenzhen Xinyuan New Materials Chip Level Sintered Silver Paste Sales, Revenue, Price and Gross Margin (2021-2026)

13.11.4 Shenzhen Xinyuan New Materials Main Business Overview

13.11.5 Shenzhen Xinyuan New Materials Latest Developments

13.12 Solderwell Advanced Materials

13.12.1 Solderwell Advanced Materials Company Information

13.12.2 Solderwell Advanced Materials Chip Level Sintered Silver Paste Product Portfolios and Specifications

13.12.3 Solderwell Advanced Materials Chip Level Sintered Silver Paste Sales, Revenue, Price and Gross Margin (2021-2026)

13.12.4 Solderwell Advanced Materials Main Business Overview

13.12.5 Solderwell Advanced Materials Latest Developments

13.13 Shenzhen Jufeng Solder

13.13.1 Shenzhen Jufeng Solder Company Information

13.13.2 Shenzhen Jufeng Solder Chip Level Sintered Silver Paste Product Portfolios and Specifications

13.13.3 Shenzhen Jufeng Solder Chip Level Sintered Silver Paste Sales, Revenue, Price and Gross Margin (2021-2026)

13.13.4 Shenzhen Jufeng Solder Main Business Overview

13.13.5 Shenzhen Jufeng Solder Latest Developments

13.14 Qinglian Technology

13.14.1 Qinglian Technology Company Information

13.14.2 Qinglian Technology Chip Level Sintered Silver Paste Product Portfolios and Specifications

13.14.3 Qinglian Technology Chip Level Sintered Silver Paste Sales, Revenue, Price and Gross Margin (2021-2026)

13.14.4 Qinglian Technology Main Business Overview

13.14.5 Qinglian Technology Latest Developments

13.15 Shenzhen Advanced Joining Technology

- 13.15.1 Shenzhen Advanced Joining Technology Company Information
- 13.15.2 Shenzhen Advanced Joining Technology Chip Level Sintered Silver Paste Product Portfolios and Specifications
- 13.15.3 Shenzhen Advanced Joining Technology Chip Level Sintered Silver Paste Sales, Revenue, Price and Gross Margin (2021-2026)
- 13.15.4 Shenzhen Advanced Joining Technology Main Business Overview
- 13.15.5 Shenzhen Advanced Joining Technology Latest Developments

14 RESEARCH FINDINGS AND CONCLUSION

List Of Tables

LIST OF TABLES

Table 1. Chip Level Sintered Silver Paste Annual Sales CAGR by Geographic Region (2021, 2025 & 2032) & (\$ millions)

Table 2. Chip Level Sintered Silver Paste Annual Sales CAGR by Country/Region (2021, 2025 & 2032) & (\$ millions)

Table 3. Major Players of Pressure Sintering Paste

Table 4. Major Players of Pressureless Sintering Paste

Table 5. Global Chip Level Sintered Silver Paste Sales by Type (2021-2026) & (Tons)

Table 6. Global Chip Level Sintered Silver Paste Sales Market Share by Type (2021-2026)

Table 7. Global Chip Level Sintered Silver Paste Revenue by Type (2021-2026) & (\$ million)

Table 8. Global Chip Level Sintered Silver Paste Revenue Market Share by Type (2021-2026)

Table 9. Global Chip Level Sintered Silver Paste Sale Price by Type (2021-2026) & (US\$/Kg)

Table 10. Major Players of Printed Sintered Silver Paste

Table 11. Major Players of Glue Type Sintered Silver Paste

Table 12. Global Chip Level Sintered Silver Paste Sales by Product Form (2021-2026) & (Tons)

Table 13. Global Chip Level Sintered Silver Paste Sales Market Share by Product Form (2021-2026)

Table 14. Global Chip Level Sintered Silver Paste Revenue by Product Form (2021-2026) & (\$ million)

Table 15. Global Chip Level Sintered Silver Paste Revenue Market Share by Product Form (2021-2026)

Table 16. Global Chip Level Sintered Silver Paste Sale Price by Product Form (2021-2026) & (US\$/Kg)

Table 17. Major Players of Sintering Time≤5min

Table 18. Major Players of Sintering Time>5min

Table 19. Global Chip Level Sintered Silver Paste Sales by Sintering Time (2021-2026) & (Tons)

Table 20. Global Chip Level Sintered Silver Paste Sales Market Share by Sintering Time (2021-2026)

Table 21. Global Chip Level Sintered Silver Paste Revenue by Sintering Time (2021-2026) & (\$ million)

Table 22. Global Chip Level Sintered Silver Paste Revenue Market Share by Sintering Time (2021-2026)

Table 23. Global Chip Level Sintered Silver Paste Sale Price by Sintering Time (2021-2026) & (US\$/Kg)

Table 24. Global Chip Level Sintered Silver Paste Sale by Application (2021-2026) & (Tons)

Table 25. Global Chip Level Sintered Silver Paste Sale Market Share by Application (2021-2026)

Table 26. Global Chip Level Sintered Silver Paste Revenue by Application (2021-2026) & (\$ million)

Table 27. Global Chip Level Sintered Silver Paste Revenue Market Share by Application (2021-2026)

Table 28. Global Chip Level Sintered Silver Paste Sale Price by Application (2021-2026) & (US\$/Kg)

Table 29. Global Chip Level Sintered Silver Paste Sales by Company (2021-2026) & (Tons)

Table 30. Global Chip Level Sintered Silver Paste Sales Market Share by Company (2021-2026)

Table 31. Global Chip Level Sintered Silver Paste Revenue by Company (2021-2026) & (\$ millions)

Table 32. Global Chip Level Sintered Silver Paste Revenue Market Share by Company (2021-2026)

Table 33. Global Chip Level Sintered Silver Paste Sale Price by Company (2021-2026) & (US\$/Kg)

Table 34. Key Manufacturers Chip Level Sintered Silver Paste Producing Area Distribution and Sales Area

Table 35. Players Chip Level Sintered Silver Paste Products Offered

Table 36. Chip Level Sintered Silver Paste Concentration Ratio (CR3, CR5 and CR10) & (2024-2026)

Table 37. New Products and Potential Entrants

Table 38. Market M&A Activity & Strategy

Table 39. Global Chip Level Sintered Silver Paste Sales by Geographic Region (2021-2026) & (Tons)

Table 40. Global Chip Level Sintered Silver Paste Sales Market Share Geographic Region (2021-2026)

Table 41. Global Chip Level Sintered Silver Paste Revenue by Geographic Region (2021-2026) & (\$ millions)

Table 42. Global Chip Level Sintered Silver Paste Revenue Market Share by Geographic Region (2021-2026)

Table 43. Global Chip Level Sintered Silver Paste Sales by Country/Region (2021-2026) & (Tons)

Table 44. Global Chip Level Sintered Silver Paste Sales Market Share by Country/Region (2021-2026)

Table 45. Global Chip Level Sintered Silver Paste Revenue by Country/Region (2021-2026) & (\$ millions)

Table 46. Global Chip Level Sintered Silver Paste Revenue Market Share by Country/Region (2021-2026)

Table 47. Americas Chip Level Sintered Silver Paste Sales by Country (2021-2026) & (Tons)

Table 48. Americas Chip Level Sintered Silver Paste Sales Market Share by Country (2021-2026)

Table 49. Americas Chip Level Sintered Silver Paste Revenue by Country (2021-2026) & (\$ millions)

Table 50. Americas Chip Level Sintered Silver Paste Sales by Type (2021-2026) & (Tons)

Table 51. Americas Chip Level Sintered Silver Paste Sales by Application (2021-2026) & (Tons)

Table 52. APAC Chip Level Sintered Silver Paste Sales by Region (2021-2026) & (Tons)

Table 53. APAC Chip Level Sintered Silver Paste Sales Market Share by Region (2021-2026)

Table 54. APAC Chip Level Sintered Silver Paste Revenue by Region (2021-2026) & (\$ millions)

Table 55. APAC Chip Level Sintered Silver Paste Sales by Type (2021-2026) & (Tons)

Table 56. APAC Chip Level Sintered Silver Paste Sales by Application (2021-2026) & (Tons)

Table 57. Europe Chip Level Sintered Silver Paste Sales by Country (2021-2026) & (Tons)

Table 58. Europe Chip Level Sintered Silver Paste Revenue by Country (2021-2026) & (\$ millions)

Table 59. Europe Chip Level Sintered Silver Paste Sales by Type (2021-2026) & (Tons)

Table 60. Europe Chip Level Sintered Silver Paste Sales by Application (2021-2026) & (Tons)

Table 61. Middle East & Africa Chip Level Sintered Silver Paste Sales by Country (2021-2026) & (Tons)

Table 62. Middle East & Africa Chip Level Sintered Silver Paste Revenue Market Share by Country (2021-2026)

Table 63. Middle East & Africa Chip Level Sintered Silver Paste Sales by Type

(2021-2026) & (Tons)

Table 64. Middle East & Africa Chip Level Sintered Silver Paste Sales by Application
(2021-2026) & (Tons)

Table 65. Key Market Drivers & Growth Opportunities of Chip Level Sintered Silver
Paste

Table 66. Key Market Challenges & Risks of Chip Level Sintered Silver Paste

Table 67. Key Industry Trends of Chip Level Sintered Silver Paste

Table 68. Chip Level Sintered Silver Paste Raw Material

Table 69. Key Suppliers of Raw Materials

Table 70. Chip Level Sintered Silver Paste Distributors List

Table 71. Chip Level Sintered Silver Paste Customer List

Table 72. Global Chip Level Sintered Silver Paste Sales Forecast by Region
(2027-2032) & (Tons)

Table 73. Global Chip Level Sintered Silver Paste Revenue Forecast by Region
(2027-2032) & (\$ millions)

Table 74. Americas Chip Level Sintered Silver Paste Sales Forecast by Country
(2027-2032) & (Tons)

Table 75. Americas Chip Level Sintered Silver Paste Annual Revenue Forecast by
Country (2027-2032) & (\$ millions)

Table 76. APAC Chip Level Sintered Silver Paste Sales Forecast by Region
(2027-2032) & (Tons)

Table 77. APAC Chip Level Sintered Silver Paste Annual Revenue Forecast by Region
(2027-2032) & (\$ millions)

Table 78. Europe Chip Level Sintered Silver Paste Sales Forecast by Country
(2027-2032) & (Tons)

Table 79. Europe Chip Level Sintered Silver Paste Revenue Forecast by Country
(2027-2032) & (\$ millions)

Table 80. Middle East & Africa Chip Level Sintered Silver Paste Sales Forecast by
Country (2027-2032) & (Tons)

Table 81. Middle East & Africa Chip Level Sintered Silver Paste Revenue Forecast by
Country (2027-2032) & (\$ millions)

Table 82. Global Chip Level Sintered Silver Paste Sales Forecast by Type (2027-2032)
& (Tons)

Table 83. Global Chip Level Sintered Silver Paste Revenue Forecast by Type
(2027-2032) & (\$ millions)

Table 84. Global Chip Level Sintered Silver Paste Sales Forecast by Application
(2027-2032) & (Tons)

Table 85. Global Chip Level Sintered Silver Paste Revenue Forecast by Application
(2027-2032) & (\$ millions)

Table 86. Heraeus Electronics Basic Information, Chip Level Sintered Silver Paste Manufacturing Base, Sales Area and Its Competitors

Table 87. Heraeus Electronics Chip Level Sintered Silver Paste Product Portfolios and Specifications

Table 88. Heraeus Electronics Chip Level Sintered Silver Paste Sales (Tons), Revenue (\$ Million), Price (US\$/Kg) and Gross Margin (2021-2026)

Table 89. Heraeus Electronics Main Business

Table 90. Heraeus Electronics Latest Developments

Table 91. Indium Corporation Basic Information, Chip Level Sintered Silver Paste Manufacturing Base, Sales Area and Its Competitors

Table 92. Indium Corporation Chip Level Sintered Silver Paste Product Portfolios and Specifications

Table 93. Indium Corporation Chip Level Sintered Silver Paste Sales (Tons), Revenue (\$ Million), Price (US\$/Kg) and Gross Margin (2021-2026)

Table 94. Indium Corporation Main Business

Table 95. Indium Corporation Latest Developments

Table 96. Nihon Superior Basic Information, Chip Level Sintered Silver Paste Manufacturing Base, Sales Area and Its Competitors

Table 97. Nihon Superior Chip Level Sintered Silver Paste Product Portfolios and Specifications

Table 98. Nihon Superior Chip Level Sintered Silver Paste Sales (Tons), Revenue (\$ Million), Price (US\$/Kg) and Gross Margin (2021-2026)

Table 99. Nihon Superior Main Business

Table 100. Nihon Superior Latest Developments

Table 101. MacDermid Alpha Basic Information, Chip Level Sintered Silver Paste Manufacturing Base, Sales Area and Its Competitors

Table 102. MacDermid Alpha Chip Level Sintered Silver Paste Product Portfolios and Specifications

Table 103. MacDermid Alpha Chip Level Sintered Silver Paste Sales (Tons), Revenue (\$ Million), Price (US\$/Kg) and Gross Margin (2021-2026)

Table 104. MacDermid Alpha Main Business

Table 105. MacDermid Alpha Latest Developments

Table 106. Henkel Basic Information, Chip Level Sintered Silver Paste Manufacturing Base, Sales Area and Its Competitors

Table 107. Henkel Chip Level Sintered Silver Paste Product Portfolios and Specifications

Table 108. Henkel Chip Level Sintered Silver Paste Sales (Tons), Revenue (\$ Million), Price (US\$/Kg) and Gross Margin (2021-2026)

Table 109. Henkel Main Business

Table 110. Henkel Latest Developments

Table 111. KYOCERA Corporation Basic Information, Chip Level Sintered Silver Paste Manufacturing Base, Sales Area and Its Competitors

Table 112. KYOCERA Corporation Chip Level Sintered Silver Paste Product Portfolios and Specifications

Table 113. KYOCERA Corporation Chip Level Sintered Silver Paste Sales (Tons), Revenue (\$ Million), Price (US\$/Kg) and Gross Margin (2021-2026)

Table 114. KYOCERA Corporation Main Business

Table 115. KYOCERA Corporation Latest Developments

Table 116. NAMICS CORPORATION Basic Information, Chip Level Sintered Silver Paste Manufacturing Base, Sales Area and Its Competitors

Table 117. NAMICS CORPORATION Chip Level Sintered Silver Paste Product Portfolios and Specifications

Table 118. NAMICS CORPORATION Chip Level Sintered Silver Paste Sales (Tons), Revenue (\$ Million), Price (US\$/Kg) and Gross Margin (2021-2026)

Table 119. NAMICS CORPORATION Main Business

Table 120. NAMICS CORPORATION Latest Developments

Table 121. TANAKA PRECIOUS METAL GROUP Basic Information, Chip Level Sintered Silver Paste Manufacturing Base, Sales Area and Its Competitors

Table 122. TANAKA PRECIOUS METAL GROUP Chip Level Sintered Silver Paste Product Portfolios and Specifications

Table 123. TANAKA PRECIOUS METAL GROUP Chip Level Sintered Silver Paste Sales (Tons), Revenue (\$ Million), Price (US\$/Kg) and Gross Margin (2021-2026)

Table 124. TANAKA PRECIOUS METAL GROUP Main Business

Table 125. TANAKA PRECIOUS METAL GROUP Latest Developments

Table 126. Bando Chemical Industries Basic Information, Chip Level Sintered Silver Paste Manufacturing Base, Sales Area and Its Competitors

Table 127. Bando Chemical Industries Chip Level Sintered Silver Paste Product Portfolios and Specifications

Table 128. Bando Chemical Industries Chip Level Sintered Silver Paste Sales (Tons), Revenue (\$ Million), Price (US\$/Kg) and Gross Margin (2021-2026)

Table 129. Bando Chemical Industries Main Business

Table 130. Bando Chemical Industries Latest Developments

Table 131. NBE Tech Basic Information, Chip Level Sintered Silver Paste Manufacturing Base, Sales Area and Its Competitors

Table 132. NBE Tech Chip Level Sintered Silver Paste Product Portfolios and Specifications

Table 133. NBE Tech Chip Level Sintered Silver Paste Sales (Tons), Revenue (\$ Million), Price (US\$/Kg) and Gross Margin (2021-2026)

Table 134. NBE Tech Main Business

Table 135. NBE Tech Latest Developments

Table 136. Shenzhen Xinyuan New Materials Basic Information, Chip Level Sintered Silver Paste Manufacturing Base, Sales Area and Its Competitors

Table 137. Shenzhen Xinyuan New Materials Chip Level Sintered Silver Paste Product Portfolios and Specifications

Table 138. Shenzhen Xinyuan New Materials Chip Level Sintered Silver Paste Sales (Tons), Revenue (\$ Million), Price (US\$/Kg) and Gross Margin (2021-2026)

Table 139. Shenzhen Xinyuan New Materials Main Business

Table 140. Shenzhen Xinyuan New Materials Latest Developments

Table 141. Solderwell Advanced Materials Basic Information, Chip Level Sintered Silver Paste Manufacturing Base, Sales Area and Its Competitors

Table 142. Solderwell Advanced Materials Chip Level Sintered Silver Paste Product Portfolios and Specifications

Table 143. Solderwell Advanced Materials Chip Level Sintered Silver Paste Sales (Tons), Revenue (\$ Million), Price (US\$/Kg) and Gross Margin (2021-2026)

Table 144. Solderwell Advanced Materials Main Business

Table 145. Solderwell Advanced Materials Latest Developments

Table 146. Shenzhen Jufeng Solder Basic Information, Chip Level Sintered Silver Paste Manufacturing Base, Sales Area and Its Competitors

Table 147. Shenzhen Jufeng Solder Chip Level Sintered Silver Paste Product Portfolios and Specifications

Table 148. Shenzhen Jufeng Solder Chip Level Sintered Silver Paste Sales (Tons), Revenue (\$ Million), Price (US\$/Kg) and Gross Margin (2021-2026)

Table 149. Shenzhen Jufeng Solder Main Business

Table 150. Shenzhen Jufeng Solder Latest Developments

Table 151. Qinglian Technology Basic Information, Chip Level Sintered Silver Paste Manufacturing Base, Sales Area and Its Competitors

Table 152. Qinglian Technology Chip Level Sintered Silver Paste Product Portfolios and Specifications

Table 153. Qinglian Technology Chip Level Sintered Silver Paste Sales (Tons), Revenue (\$ Million), Price (US\$/Kg) and Gross Margin (2021-2026)

Table 154. Qinglian Technology Main Business

Table 155. Qinglian Technology Latest Developments

Table 156. Shenzhen Advanced Joining Technology Basic Information, Chip Level Sintered Silver Paste Manufacturing Base, Sales Area and Its Competitors

Table 157. Shenzhen Advanced Joining Technology Chip Level Sintered Silver Paste Product Portfolios and Specifications

Table 158. Shenzhen Advanced Joining Technology Chip Level Sintered Silver Paste

Sales (Tons), Revenue (\$ Million), Price (US\$/Kg) and Gross Margin (2021-2026)

Table 159. Shenzhen Advanced Joining Technology Main Business

Table 160. Shenzhen Advanced Joining Technology Latest Developments

List Of Figures

LIST OF FIGURES

- Figure 1. Picture of Chip Level Sintered Silver Paste
- Figure 2. Chip Level Sintered Silver Paste Report Years Considered
- Figure 3. Research Objectives
- Figure 4. Research Methodology
- Figure 5. Research Process and Data Source
- Figure 6. Global Chip Level Sintered Silver Paste Sales Growth Rate 2021-2032 (Tons)
- Figure 7. Global Chip Level Sintered Silver Paste Revenue Growth Rate 2021-2032 (\$ millions)
- Figure 8. Chip Level Sintered Silver Paste Sales by Geographic Region (2021, 2025 & 2032) & (\$ millions)
- Figure 9. Chip Level Sintered Silver Paste Sales Market Share by Country/Region (2025)
- Figure 10. Chip Level Sintered Silver Paste Sales Market Share by Country/Region (2021, 2025 & 2032)
- Figure 11. Product Picture of Pressure Sintering Paste
- Figure 12. Product Picture of Pressureless Sintering Paste
- Figure 13. Global Chip Level Sintered Silver Paste Sales Market Share by Type in 2026
- Figure 14. Global Chip Level Sintered Silver Paste Revenue Market Share by Type (2021-2026)
- Figure 15. Product Picture of Printed Sintered Silver Paste
- Figure 16. Product Picture of Glue Type Sintered Silver Paste
- Figure 17. Global Chip Level Sintered Silver Paste Sales Market Share by Product Form in 2026
- Figure 18. Global Chip Level Sintered Silver Paste Revenue Market Share by Product Form (2021-2026)
- Figure 19. Product Picture of Sintering Time?5min
- Figure 20. Product Picture of Sintering Time>5min
- Figure 21. Global Chip Level Sintered Silver Paste Sales Market Share by Sintering Time in 2026
- Figure 22. Global Chip Level Sintered Silver Paste Revenue Market Share by Sintering Time (2021-2026)
- Figure 23. Chip Level Sintered Silver Paste Consumed in New Energy Vehicle Power Module
- Figure 24. Global Chip Level Sintered Silver Paste Market: New Energy Vehicle Power Module (2021-2026) & (Tons)

- Figure 25. Chip Level Sintered Silver Paste Consumed in AI Chip
- Figure 26. Global Chip Level Sintered Silver Paste Market: AI Chip (2021-2026) & (Tons)
- Figure 27. Chip Level Sintered Silver Paste Consumed in 5G Communication & Data Center
- Figure 28. Global Chip Level Sintered Silver Paste Market: 5G Communication & Data Center (2021-2026) & (Tons)
- Figure 29. Chip Level Sintered Silver Paste Consumed in Consumer Electronics
- Figure 30. Global Chip Level Sintered Silver Paste Market: Consumer Electronics (2021-2026) & (Tons)
- Figure 31. Chip Level Sintered Silver Paste Consumed in Aerospace and Military Industry
- Figure 32. Global Chip Level Sintered Silver Paste Market: Aerospace and Military Industry (2021-2026) & (Tons)
- Figure 33. Chip Level Sintered Silver Paste Consumed in Other
- Figure 34. Global Chip Level Sintered Silver Paste Market: Other (2021-2026) & (Tons)
- Figure 35. Global Chip Level Sintered Silver Paste Sale Market Share by Application (2025)
- Figure 36. Global Chip Level Sintered Silver Paste Revenue Market Share by Application in 2025
- Figure 37. Chip Level Sintered Silver Paste Sales by Company in 2025 (Tons)
- Figure 38. Global Chip Level Sintered Silver Paste Sales Market Share by Company in 2025
- Figure 39. Chip Level Sintered Silver Paste Revenue by Company in 2025 (\$ millions)
- Figure 40. Global Chip Level Sintered Silver Paste Revenue Market Share by Company in 2025
- Figure 41. Global Chip Level Sintered Silver Paste Sales Market Share by Geographic Region (2021-2026)
- Figure 42. Global Chip Level Sintered Silver Paste Revenue Market Share by Geographic Region in 2025
- Figure 43. Americas Chip Level Sintered Silver Paste Sales 2021-2026 (Tons)
- Figure 44. Americas Chip Level Sintered Silver Paste Revenue 2021-2026 (\$ millions)
- Figure 45. APAC Chip Level Sintered Silver Paste Sales 2021-2026 (Tons)
- Figure 46. APAC Chip Level Sintered Silver Paste Revenue 2021-2026 (\$ millions)
- Figure 47. Europe Chip Level Sintered Silver Paste Sales 2021-2026 (Tons)
- Figure 48. Europe Chip Level Sintered Silver Paste Revenue 2021-2026 (\$ millions)
- Figure 49. Middle East & Africa Chip Level Sintered Silver Paste Sales 2021-2026 (Tons)
- Figure 50. Middle East & Africa Chip Level Sintered Silver Paste Revenue 2021-2026 (\$

millions)

Figure 51. Americas Chip Level Sintered Silver Paste Sales Market Share by Country in 2025

Figure 52. Americas Chip Level Sintered Silver Paste Revenue Market Share by Country (2021-2026)

Figure 53. Americas Chip Level Sintered Silver Paste Sales Market Share by Type (2021-2026)

Figure 54. Americas Chip Level Sintered Silver Paste Sales Market Share by Application (2021-2026)

Figure 55. United States Chip Level Sintered Silver Paste Revenue Growth 2021-2026 (\$ millions)

Figure 56. Canada Chip Level Sintered Silver Paste Revenue Growth 2021-2026 (\$ millions)

Figure 57. Mexico Chip Level Sintered Silver Paste Revenue Growth 2021-2026 (\$ millions)

Figure 58. Brazil Chip Level Sintered Silver Paste Revenue Growth 2021-2026 (\$ millions)

Figure 59. APAC Chip Level Sintered Silver Paste Sales Market Share by Region in 2025

Figure 60. APAC Chip Level Sintered Silver Paste Revenue Market Share by Region (2021-2026)

Figure 61. APAC Chip Level Sintered Silver Paste Sales Market Share by Type (2021-2026)

Figure 62. APAC Chip Level Sintered Silver Paste Sales Market Share by Application (2021-2026)

Figure 63. China Chip Level Sintered Silver Paste Revenue Growth 2021-2026 (\$ millions)

Figure 64. Japan Chip Level Sintered Silver Paste Revenue Growth 2021-2026 (\$ millions)

Figure 65. South Korea Chip Level Sintered Silver Paste Revenue Growth 2021-2026 (\$ millions)

Figure 66. Southeast Asia Chip Level Sintered Silver Paste Revenue Growth 2021-2026 (\$ millions)

Figure 67. India Chip Level Sintered Silver Paste Revenue Growth 2021-2026 (\$ millions)

Figure 68. Australia Chip Level Sintered Silver Paste Revenue Growth 2021-2026 (\$ millions)

Figure 69. China Taiwan Chip Level Sintered Silver Paste Revenue Growth 2021-2026 (\$ millions)

Figure 70. Europe Chip Level Sintered Silver Paste Sales Market Share by Country in 2025

Figure 71. Europe Chip Level Sintered Silver Paste Revenue Market Share by Country (2021-2026)

Figure 72. Europe Chip Level Sintered Silver Paste Sales Market Share by Type (2021-2026)

Figure 73. Europe Chip Level Sintered Silver Paste Sales Market Share by Application (2021-2026)

Figure 74. Germany Chip Level Sintered Silver Paste Revenue Growth 2021-2026 (\$ millions)

Figure 75. France Chip Level Sintered Silver Paste Revenue Growth 2021-2026 (\$ millions)

Figure 76. UK Chip Level Sintered Silver Paste Revenue Growth 2021-2026 (\$ millions)

Figure 77. Italy Chip Level Sintered Silver Paste Revenue Growth 2021-2026 (\$ millions)

Figure 78. Russia Chip Level Sintered Silver Paste Revenue Growth 2021-2026 (\$ millions)

Figure 79. Middle East & Africa Chip Level Sintered Silver Paste Sales Market Share by Country (2021-2026)

Figure 80. Middle East & Africa Chip Level Sintered Silver Paste Sales Market Share by Type (2021-2026)

Figure 81. Middle East & Africa Chip Level Sintered Silver Paste Sales Market Share by Application (2021-2026)

Figure 82. Egypt Chip Level Sintered Silver Paste Revenue Growth 2021-2026 (\$ millions)

Figure 83. South Africa Chip Level Sintered Silver Paste Revenue Growth 2021-2026 (\$ millions)

Figure 84. Israel Chip Level Sintered Silver Paste Revenue Growth 2021-2026 (\$ millions)

Figure 85. Turkey Chip Level Sintered Silver Paste Revenue Growth 2021-2026 (\$ millions)

Figure 86. GCC Countries Chip Level Sintered Silver Paste Revenue Growth 2021-2026 (\$ millions)

Figure 87. Manufacturing Cost Structure Analysis of Chip Level Sintered Silver Paste in 2026

Figure 88. Manufacturing Process Analysis of Chip Level Sintered Silver Paste

Figure 89. Industry Chain Structure of Chip Level Sintered Silver Paste

Figure 90. Channels of Distribution

Figure 91. Global Chip Level Sintered Silver Paste Sales Market Forecast by Region

(2027-2032)

Figure 92. Global Chip Level Sintered Silver Paste Revenue Market Share Forecast by Region (2027-2032)

Figure 93. Global Chip Level Sintered Silver Paste Sales Market Share Forecast by Type (2027-2032)

Figure 94. Global Chip Level Sintered Silver Paste Revenue Market Share Forecast by Type (2027-2032)

Figure 95. Global Chip Level Sintered Silver Paste Sales Market Share Forecast by Application (2027-2032)

Figure 96. Global Chip Level Sintered Silver Paste Revenue Market Share Forecast by Application (2027-2032)

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