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Global Automotive GPU Chip Market Growth 2026-2032

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Abstracts

The global Automotive GPU Chip market size is predicted to grow from US$ 4745 million in 2025 to US$ 10130 million in 2032; it is expected to grow at a CAGR of 11.7% from 2026 to 2032.

An Automotive GPU Chip is a graphics and massively parallel compute processor designed to meet automotive-grade requirements?wide temperature range, vibration tolerance, long service life, functional safety, and high reliability?appearing either as a discrete GPU (less common) or, more typically, as the GPU subsystem inside an infotainment/instrument/central-compute/ADAS SoC. It addresses the core gap between ?consumer-electronics-like? in-vehicle experiences and real-time, safety-constrained vehicle operation by enabling smooth multi-display rendering, 3D HMI, navigation and map compositing, AR-HUD pipelines, surround-view visualization, high-throughput video encode/decode, and increasingly GPU-parallel acceleration for AI inference and sensor fusion, all under strict power, thermal, EMC, and ASIL-oriented constraints. Historically, the category evolved from early head units dominated by MCUs and basic 2D acceleration, to smartphone-derived GPU architectures powering modern digital cockpits with multi-screen 3D UI and rich media, and further into centralized domain controllers where GPU, CPU, NPU, ISP, and safety islands are tightly integrated into automotive compute platforms backed by mature software stacks (drivers, real-time OS/hypervisors, middleware, and AI frameworks) that make performance reusable and behavior certifiable. Upstream, the supply chain spans foundational materials and manufacturing inputs (silicon wafers and epitaxy, lithography chemicals, specialty gases and targets, advanced packaging substrates and interconnect materials, thermal interface materials and mechanical parts), and critical components and services such as IP/EDA enablement, automotive-grade foundry processes, packaging/test and reliability qualification, memories (DRAM/LPDDR and Flash), power management and power

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devices (PMICs and DC-DC converters), high-reliability clocks/oscillators, high-speed automotive interconnects and transceivers (PCIe/Ethernet/SerDes), plus passives?together enabling scalable production and the quality/continuity demanded by OEM programs.In 2025, global production capacity for automotive GPU chips is estimated at 20 million units, while sales reached approximately 17.32 million units. The average selling price is about USD 280 per chip, and gross margins across suppliers generally range between 50% and 70%.

The market today is defined by broadening demand, converging system architectures, and tiered competition. On the demand side, digital cockpits push multi-display, high-resolution, 3D-rich interfaces and media workloads, while automated driving pushes visualization-heavy development workflows and real-time inference requirements into domain controllers?making GPU capability a shared backbone for both graphics and parallel compute. Architecturally, the industry is moving from scattered ECUs toward consolidated cockpit/ADAS domain controllers and, increasingly, centralized compute platforms. As a result, competition is less about isolated peak metrics and more about platform delivery: a cohesive stack of hardware, drivers and graphics runtime, AI tooling, virtualization and safety isolation, automotive-grade qualification, and tight integration with OEM software architectures. Procurement follows the same shift?buyers increasingly evaluate complete platforms (silicon plus board support, middleware, reference designs, and ecosystem) rather than a single chip, which amplifies lock-in and raises the barrier for entrants who only compete on one headline specification.

Looking forword, the trajectory stacks three themes: higher sustained performance and efficiency, deeper software-defined differentiation, and tighter heterogenous coordination. Workloads will keep mixing?UI rendering, video pipelines, mapping and AR overlays, alongside visualization for perception and growing AI inference?so architectures will prioritize deterministic behavior, thermal discipline, and controllable latency as much as raw throughput. Software becomes the decisive battleground: more mature graphics APIs and rendering frameworks, unified AI deployment pipelines, robust profiling and diagnostics, and OTA-friendly lifecycle management all turn into selection gatekeepers. Virtualization and partitioning will become more prevalent as OEMs isolate cockpit, cluster, and ADAS into separate safety domains, pushing GPU resources to be scheduled and shared with finer-grained control. With faster in-vehicle networks and interconnects, GPU capability may also become more composable?local acceleration for low-latency graphics and critical tasks, coordinated with higher-power compute elsewhere for heavier inference and iterative updates?forming a cooperative, cross-domain compute topology.

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The engines of growth come from user experience expectations, regulatory/safety requirements, and engineering productivity goals: smoother and more consistent cockpit experiences, faster ADAS development and iteration, and OEM pressure to reduce ECU fragmentation while shortening development cycles and long-term maintenance burden. The blockers, however, are equally structural. Automotive-grade reliability and functional safety qualification impose long, expensive verification loops, and even ?small? changes in drivers, firmware, or scheduling can trigger system-level re-validation. GPU workloads are inherently less predictable under mixed rendering-and-AI concurrency, making real-time guarantees and isolation a hard engineering problem. Supply-chain and lifecycle constraints are unforgiving?OEMs expect long-term availability and consistency, while advanced silicon and packaging evolve rapidly and don?t naturally align with automotive timelines. Finally, ecosystem and IP boundaries shape collaboration: tooling transparency, compiler and driver accessibility, and the degree of standards and open-source alignment can determine long-term flexibility, turning platform choice into a multi-year strategic commitment. In practice, market gravity tends to favor platforms that are not only fast, but deliverable, certifiable, and maintainable over time.

LP Information, Inc. (LPI) ' newest research report, the ?Automotive GPU Chip Industry Forecast? looks at past sales and reviews total world Automotive GPU Chip sales in 2025, providing a comprehensive analysis by region and market sector of projected Automotive GPU Chip sales for 2026 through 2032. With Automotive GPU Chip sales broken down by region, market sector and sub-sector, this report provides a detailed analysis in US$ millions of the world Automotive GPU Chip industry.

This Insight Report provides a comprehensive analysis of the global Automotive GPU Chip landscape and highlights key trends related to product segmentation, company formation, revenue, and market share, latest development, and M&A activity. This report also analyzes the strategies of leading global companies with a focus on Automotive GPU Chip portfolios and capabilities, market entry strategies, market positions, and geographic footprints, to better understand these firms? unique position in an accelerating global Automotive GPU Chip market.

This Insight Report evaluates the key market trends, drivers, and affecting factors shaping the global outlook for Automotive GPU Chip and breaks down the forecast by Type, by Application, geography, and market size to highlight emerging pockets of opportunity. With a transparent methodology based on hundreds of bottom-up qualitative and quantitative market inputs, this study forecast offers a highly nuanced

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view of the current state and future trajectory in the global Automotive GPU Chip.

This report presents a comprehensive overview, market shares, and growth opportunities of Automotive GPU Chip market by product type, application, key manufacturers and key regions and countries.

Segmentation by Type:

Discrete GPU

Integrated GPU

Segmentation by Compute Performance Tier:

Entry-Level

Mainstream

High-Performance

Ultra-High Performance

Segmentation by Workload Focus:

Graphics-Centric

Vision-Centric

AI Inference-Centric

Mixed Workloads

Segmentation by Application:

ADAS

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Automatic Driving

Central Control Information System

Other

This report also splits the market by region:

Americas

United States

Canada

Mexico

Brazil

APAC

China

Japan

Korea

Southeast Asia

India

Australia

Europe

Germany

France

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UK

Italy

Russia

Middle East & Africa

Egypt

South Africa

Israel

Turkey

GCC Countries

The below companies that are profiled have been selected based on inputs gathered from primary experts and analysing the company's coverage, product portfolio, its market penetration.

NVIDIA

Qualcomm

Renesas Electronics

Samsung Electronics

MediaTek

SemiDrive

UNISOC

SiEngine

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Key Questions Addressed in this Report

What is the 10-year outlook for the global Automotive GPU Chip market?
What factors are driving Automotive GPU Chip market growth, globally and by region?
Which technologies are poised for the fastest growth by market and region?
How do Automotive GPU Chip market opportunities vary by end market size?
How does Automotive GPU Chip break out by Type, by Application?

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Contents

1 SCOPE OF THE REPORT

1.1 Market Introduction
1.2 Years Considered
1.3 Research Objectives
1.4 Market Research Methodology
1.5 Research Process and Data Source
1.6 Economic Indicators
1.7 Currency Considered
1.8 Market Estimation Caveats

2 EXECUTIVE SUMMARY

2.1 World Market Overview
  2.1.1 Global Automotive GPU Chip Annual Sales 2021-2032
  2.1.2 World Current & Future Analysis for Automotive GPU Chip by Geographic Region, 2021, 2025 & 2032
  2.1.3 World Current & Future Analysis for Automotive GPU Chip by Country/Region, 2021, 2025 & 2032
2.2 Automotive GPU Chip Segment by Type
  2.2.1 Discrete GPU
  2.2.2 Integrated GPU
  2.2.3 Automotive GPU Chip Sales by Type
    2.2.3.1 Global Automotive GPU Chip Sales Market Share by Type (2021-2026)
    2.2.3.2 Global Automotive GPU Chip Revenue and Market Share by Type (2021-2026)
    2.2.3.3 Global Automotive GPU Chip Sale Price by Type (2021-2026)
2.3 Automotive GPU Chip Segment by Compute Performance Tier
  2.3.1 Entry-Level
  2.3.2 Mainstream
  2.3.3 High-Performance
  2.3.4 Ultra-High Performance
  2.3.5 Automotive GPU Chip Sales by Compute Performance Tier
    2.3.5.1 Global Automotive GPU Chip Sales Market Share by Compute Performance Tier (2021-2026)
    2.3.5.2 Global Automotive GPU Chip Revenue and Market Share by Compute Performance Tier (2021-2026)

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2.3.5.3 Global Automotive GPU Chip Sale Price by Compute Performance Tier (2021-2026)
2.4 Automotive GPU Chip Segment by Workload Focus
2.4.1 Graphics-Centric
2.4.2 Vision-Centric
2.4.3 AI Inference-Centric
2.4.4 Mixed Workloads
2.4.5 Automotive GPU Chip Sales by Workload Focus
2.4.5.1 Global Automotive GPU Chip Sales Market Share by Workload Focus (2021-2026)
2.4.5.2 Global Automotive GPU Chip Revenue and Market Share by Workload Focus (2021-2026)
2.4.5.3 Global Automotive GPU Chip Sale Price by Workload Focus (2021-2026)
2.5 Automotive GPU Chip Segment by Application
2.5.1 ADAS
2.5.2 Automatic Driving
2.5.3 Central Control Information System
2.5.4 Other
2.5.5 Automotive GPU Chip Sales by Application
2.5.5.1 Global Automotive GPU Chip Sale Market Share by Application (2021-2026)
2.5.5.2 Global Automotive GPU Chip Revenue and Market Share by Application (2021-2026)
2.5.5.3 Global Automotive GPU Chip Sale Price by Application (2021-2026)

3 GLOBAL BY COMPANY

3.1 Global Automotive GPU Chip Breakdown Data by Company
3.1.1 Global Automotive GPU Chip Annual Sales by Company (2021-2026)
3.1.2 Global Automotive GPU Chip Sales Market Share by Company (2021-2026)
3.2 Global Automotive GPU Chip Annual Revenue by Company (2021-2026)
3.2.1 Global Automotive GPU Chip Revenue by Company (2021-2026)
3.2.2 Global Automotive GPU Chip Revenue Market Share by Company (2021-2026)
3.3 Global Automotive GPU Chip Sale Price by Company
3.4 Key Manufacturers Automotive GPU Chip Producing Area Distribution, Sales Area, Product Type
3.4.1 Key Manufacturers Automotive GPU Chip Product Location Distribution
3.4.2 Players Automotive GPU Chip Products Offered
3.5 Market Concentration Rate Analysis
3.5.1 Competition Landscape Analysis

+357 96 030922
info@marketpublishers.com

3.5.2 Concentration Ratio (CR3, CR5 and CR10) & (2024-2026)
3.6 New Products and Potential Entrants
3.7 Market M&A Activity & Strategy

4 WORLD HISTORIC REVIEW FOR AUTOMOTIVE GPU CHIP BY GEOGRAPHIC REGION

4.1 World Historic Automotive GPU Chip Market Size by Geographic Region (2021-2026)
4.1.1 Global Automotive GPU Chip Annual Sales by Geographic Region (2021-2026)
4.1.2 Global Automotive GPU Chip Annual Revenue by Geographic Region (2021-2026)
4.2 World Historic Automotive GPU Chip Market Size by Country/Region (2021-2026)
4.2.1 Global Automotive GPU Chip Annual Sales by Country/Region (2021-2026)
4.2.2 Global Automotive GPU Chip Annual Revenue by Country/Region (2021-2026)
4.3 Americas Automotive GPU Chip Sales Growth
4.4 APAC Automotive GPU Chip Sales Growth
4.5 Europe Automotive GPU Chip Sales Growth
4.6 Middle East & Africa Automotive GPU Chip Sales Growth

5 AMERICAS

5.1 Americas Automotive GPU Chip Sales by Country
5.1.1 Americas Automotive GPU Chip Sales by Country (2021-2026)
5.1.2 Americas Automotive GPU Chip Revenue by Country (2021-2026)
5.2 Americas Automotive GPU Chip Sales by Type (2021-2026)
5.3 Americas Automotive GPU Chip Sales by Application (2021-2026)
5.4 United States
5.5 Canada
5.6 Mexico
5.7 Brazil

6 APAC

6.1 APAC Automotive GPU Chip Sales by Region
6.1.1 APAC Automotive GPU Chip Sales by Region (2021-2026)
6.1.2 APAC Automotive GPU Chip Revenue by Region (2021-2026)
6.2 APAC Automotive GPU Chip Sales by Type (2021-2026)
6.3 APAC Automotive GPU Chip Sales by Application (2021-2026)

+357 96 030922
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6.4 China
6.5 Japan
6.6 South Korea
6.7 Southeast Asia
6.8 India
6.9 Australia
6.10 China Taiwan

7 EUROPE

7.1 Europe Automotive GPU Chip by Country
  7.1.1 Europe Automotive GPU Chip Sales by Country (2021-2026)
  7.1.2 Europe Automotive GPU Chip Revenue by Country (2021-2026)
7.2 Europe Automotive GPU Chip Sales by Type (2021-2026)
7.3 Europe Automotive GPU Chip Sales by Application (2021-2026)
7.4 Germany
7.5 France
7.6 UK
7.7 Italy
7.8 Russia

8 MIDDLE EAST & AFRICA

8.1 Middle East & Africa Automotive GPU Chip by Country
  8.1.1 Middle East & Africa Automotive GPU Chip Sales by Country (2021-2026)
  8.1.2 Middle East & Africa Automotive GPU Chip Revenue by Country (2021-2026)
8.2 Middle East & Africa Automotive GPU Chip Sales by Type (2021-2026)
8.3 Middle East & Africa Automotive GPU Chip Sales by Application (2021-2026)
8.4 Egypt
8.5 South Africa
8.6 Israel
8.7 Turkey
8.8 GCC Countries

9 MARKET DRIVERS, CHALLENGES AND TRENDS

9.1 Market Drivers & Growth Opportunities
9.2 Market Challenges & Risks
9.3 Industry Trends

+357 96 030922
info@marketpublishers.com

10 MANUFACTURING COST STRUCTURE ANALYSIS

10.1 Raw Material and Suppliers
10.2 Manufacturing Cost Structure Analysis of Automotive GPU Chip
10.3 Manufacturing Process Analysis of Automotive GPU Chip
10.4 Industry Chain Structure of Automotive GPU Chip

11 MARKETING, DISTRIBUTORS AND CUSTOMER

11.1 Sales Channel
  11.1.1 Direct Channels
  11.1.2 Indirect Channels
11.2 Automotive GPU Chip Distributors
11.3 Automotive GPU Chip Customer

12 WORLD FORECAST REVIEW FOR AUTOMOTIVE GPU CHIP BY GEOGRAPHIC REGION

12.1 Global Automotive GPU Chip Market Size Forecast by Region
  12.1.1 Global Automotive GPU Chip Forecast by Region (2027-2032)
  12.1.2 Global Automotive GPU Chip Annual Revenue Forecast by Region (2027-2032)
12.2 Americas Forecast by Country (2027-2032)
12.3 APAC Forecast by Region (2027-2032)
12.4 Europe Forecast by Country (2027-2032)
12.5 Middle East & Africa Forecast by Country (2027-2032)
12.6 Global Automotive GPU Chip Forecast by Type (2027-2032)
12.7 Global Automotive GPU Chip Forecast by Application (2027-2032)

13 KEY PLAYERS ANALYSIS

13.1 NVIDIA
  13.1.1 NVIDIA Company Information
  13.1.2 NVIDIA Automotive GPU Chip Product Portfolios and Specifications
  13.1.3 NVIDIA Automotive GPU Chip Sales, Revenue, Price and Gross Margin (2021-2026)
  13.1.4 NVIDIA Main Business Overview
  13.1.5 NVIDIA Latest Developments
13.2 Qualcomm

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13.2.1 Qualcomm Company Information
13.2.2 Qualcomm Automotive GPU Chip Product Portfolios and Specifications
13.2.3 Qualcomm Automotive GPU Chip Sales, Revenue, Price and Gross Margin (2021-2026)
13.2.4 Qualcomm Main Business Overview
13.2.5 Qualcomm Latest Developments
13.3 Renesas Electronics
13.3.1 Renesas Electronics Company Information
13.3.2 Renesas Electronics Automotive GPU Chip Product Portfolios and Specifications
13.3.3 Renesas Electronics Automotive GPU Chip Sales, Revenue, Price and Gross Margin (2021-2026)
13.3.4 Renesas Electronics Main Business Overview
13.3.5 Renesas Electronics Latest Developments
13.4 Samsung Electronics
13.4.1 Samsung Electronics Company Information
13.4.2 Samsung Electronics Automotive GPU Chip Product Portfolios and Specifications
13.4.3 Samsung Electronics Automotive GPU Chip Sales, Revenue, Price and Gross Margin (2021-2026)
13.4.4 Samsung Electronics Main Business Overview
13.4.5 Samsung Electronics Latest Developments
13.5 MediaTek
13.5.1 MediaTek Company Information
13.5.2 MediaTek Automotive GPU Chip Product Portfolios and Specifications
13.5.3 MediaTek Automotive GPU Chip Sales, Revenue, Price and Gross Margin (2021-2026)
13.5.4 MediaTek Main Business Overview
13.5.5 MediaTek Latest Developments
13.6 SemiDrive
13.6.1 SemiDrive Company Information
13.6.2 SemiDrive Automotive GPU Chip Product Portfolios and Specifications
13.6.3 SemiDrive Automotive GPU Chip Sales, Revenue, Price and Gross Margin (2021-2026)
13.6.4 SemiDrive Main Business Overview
13.6.5 SemiDrive Latest Developments
13.7 UNISOC
13.7.1 UNISOC Company Information
13.7.2 UNISOC Automotive GPU Chip Product Portfolios and Specifications

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info@marketpublishers.com

13.7.3 UNISOC Automotive GPU Chip Sales, Revenue, Price and Gross Margin (2021-2026)
13.7.4 UNISOC Main Business Overview
13.7.5 UNISOC Latest Developments
13.8 SiEngine
13.8.1 SiEngine Company Information
13.8.2 SiEngine Automotive GPU Chip Product Portfolios and Specifications
13.8.3 SiEngine Automotive GPU Chip Sales, Revenue, Price and Gross Margin (2021-2026)
13.8.4 SiEngine Main Business Overview
13.8.5 SiEngine Latest Developments

14 RESEARCH FINDINGS AND CONCLUSION

+357 96 030922
info@marketpublishers.com

List Of Tables

LIST OF TABLES

Table 1. Automotive GPU Chip Annual Sales CAGR by Geographic Region (2021, 2025 & 2032) & ($ millions)
Table 2. Automotive GPU Chip Annual Sales CAGR by Country/Region (2021, 2025 & 2032) & ($ millions)
Table 3. Major Players of Discrete GPU
Table 4. Major Players of Integrated GPU
Table 5. Global Automotive GPU Chip Sales by Type (2021-2026) & (K Pcs)
Table 6. Global Automotive GPU Chip Sales Market Share by Type (2021-2026)
Table 7. Global Automotive GPU Chip Revenue by Type (2021-2026) & ($ million)
Table 8. Global Automotive GPU Chip Revenue Market Share by Type (2021-2026)
Table 9. Global Automotive GPU Chip Sale Price by Type (2021-2026) & (US$/Pcs)
Table 10. Major Players of Entry-Level
Table 11. Major Players of Mainstream
Table 12. Major Players of High-Performance
Table 13. Major Players of Ultra-High Performance
Table 14. Global Automotive GPU Chip Sales by Compute Performance Tier (2021-2026) & (K Pcs)
Table 15. Global Automotive GPU Chip Sales Market Share by Compute Performance Tier (2021-2026)
Table 16. Global Automotive GPU Chip Revenue by Compute Performance Tier (2021-2026) & ($ million)
Table 17. Global Automotive GPU Chip Revenue Market Share by Compute Performance Tier (2021-2026)
Table 18. Global Automotive GPU Chip Sale Price by Compute Performance Tier (2021-2026) & (US$/Pcs)
Table 19. Major Players of Graphics-Centric
Table 20. Major Players of Vision-Centric
Table 21. Major Players of AI Inference-Centric
Table 22. Major Players of Mixed Workloads
Table 23. Global Automotive GPU Chip Sales by Workload Focus (2021-2026) & (K Pcs)
Table 24. Global Automotive GPU Chip Sales Market Share by Workload Focus (2021-2026)
Table 25. Global Automotive GPU Chip Revenue by Workload Focus (2021-2026) & ($ million)

+357 96 030922
info@marketpublishers.com

Table 26. Global Automotive GPU Chip Revenue Market Share by Workload Focus (2021-2026)
Table 27. Global Automotive GPU Chip Sale Price by Workload Focus (2021-2026) & (US$/Pcs)
Table 28. Global Automotive GPU Chip Sale by Application (2021-2026) & (K Pcs)
Table 29. Global Automotive GPU Chip Sale Market Share by Application (2021-2026)
Table 30. Global Automotive GPU Chip Revenue by Application (2021-2026) & ($ million)
Table 31. Global Automotive GPU Chip Revenue Market Share by Application (2021-2026)
Table 32. Global Automotive GPU Chip Sale Price by Application (2021-2026) & (US$/Pcs)
Table 33. Global Automotive GPU Chip Sales by Company (2021-2026) & (K Pcs)
Table 34. Global Automotive GPU Chip Sales Market Share by Company (2021-2026)
Table 35. Global Automotive GPU Chip Revenue by Company (2021-2026) & ($ millions)
Table 36. Global Automotive GPU Chip Revenue Market Share by Company (2021-2026)
Table 37. Global Automotive GPU Chip Sale Price by Company (2021-2026) & (US$/Pcs)
Table 38. Key Manufacturers Automotive GPU Chip Producing Area Distribution and Sales Area
Table 39. Players Automotive GPU Chip Products Offered
Table 40. Automotive GPU Chip Concentration Ratio (CR3, CR5 and CR10) & (2024-2026)
Table 41. New Products and Potential Entrants
Table 42. Market M&A Activity & Strategy
Table 43. Global Automotive GPU Chip Sales by Geographic Region (2021-2026) & (K Pcs)
Table 44. Global Automotive GPU Chip Sales Market Share Geographic Region (2021-2026)
Table 45. Global Automotive GPU Chip Revenue by Geographic Region (2021-2026) & ($ millions)
Table 46. Global Automotive GPU Chip Revenue Market Share by Geographic Region (2021-2026)
Table 47. Global Automotive GPU Chip Sales by Country/Region (2021-2026) & (K Pcs)
Table 48. Global Automotive GPU Chip Sales Market Share by Country/Region (2021-2026)
Table 49. Global Automotive GPU Chip Revenue by Country/Region (2021-2026) & ($

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millions)
Table 50. Global Automotive GPU Chip Revenue Market Share by Country/Region (2021-2026)
Table 51. Americas Automotive GPU Chip Sales by Country (2021-2026) & (K Pcs)
Table 52. Americas Automotive GPU Chip Sales Market Share by Country (2021-2026)
Table 53. Americas Automotive GPU Chip Revenue by Country (2021-2026) & ($ millions)
Table 54. Americas Automotive GPU Chip Sales by Type (2021-2026) & (K Pcs)
Table 55. Americas Automotive GPU Chip Sales by Application (2021-2026) & (K Pcs)
Table 56. APAC Automotive GPU Chip Sales by Region (2021-2026) & (K Pcs)
Table 57. APAC Automotive GPU Chip Sales Market Share by Region (2021-2026)
Table 58. APAC Automotive GPU Chip Revenue by Region (2021-2026) & ($ millions)
Table 59. APAC Automotive GPU Chip Sales by Type (2021-2026) & (K Pcs)
Table 60. APAC Automotive GPU Chip Sales by Application (2021-2026) & (K Pcs)
Table 61. Europe Automotive GPU Chip Sales by Country (2021-2026) & (K Pcs)
Table 62. Europe Automotive GPU Chip Revenue by Country (2021-2026) & ($ millions)
Table 63. Europe Automotive GPU Chip Sales by Type (2021-2026) & (K Pcs)
Table 64. Europe Automotive GPU Chip Sales by Application (2021-2026) & (K Pcs)
Table 65. Middle East & Africa Automotive GPU Chip Sales by Country (2021-2026) & (K Pcs)
Table 66. Middle East & Africa Automotive GPU Chip Revenue Market Share by Country (2021-2026)
Table 67. Middle East & Africa Automotive GPU Chip Sales by Type (2021-2026) & (K Pcs)
Table 68. Middle East & Africa Automotive GPU Chip Sales by Application (2021-2026) & (K Pcs)
Table 69. Key Market Drivers & Growth Opportunities of Automotive GPU Chip
Table 70. Key Market Challenges & Risks of Automotive GPU Chip
Table 71. Key Industry Trends of Automotive GPU Chip
Table 72. Automotive GPU Chip Raw Material
Table 73. Key Suppliers of Raw Materials
Table 74. Automotive GPU Chip Distributors List
Table 75. Automotive GPU Chip Customer List
Table 76. Global Automotive GPU Chip Sales Forecast by Region (2027-2032) & (K Pcs)
Table 77. Global Automotive GPU Chip Revenue Forecast by Region (2027-2032) & ($ millions)
Table 78. Americas Automotive GPU Chip Sales Forecast by Country (2027-2032) & (K Pcs)

+357 96 030922
info@marketpublishers.com

Table 79. Americas Automotive GPU Chip Annual Revenue Forecast by Country (2027-2032) & ($ millions)
Table 80. APAC Automotive GPU Chip Sales Forecast by Region (2027-2032) & (K Pcs)
Table 81. APAC Automotive GPU Chip Annual Revenue Forecast by Region (2027-2032) & ($ millions)
Table 82. Europe Automotive GPU Chip Sales Forecast by Country (2027-2032) & (K Pcs)
Table 83. Europe Automotive GPU Chip Revenue Forecast by Country (2027-2032) & ($ millions)
Table 84. Middle East & Africa Automotive GPU Chip Sales Forecast by Country (2027-2032) & (K Pcs)
Table 85. Middle East & Africa Automotive GPU Chip Revenue Forecast by Country (2027-2032) & ($ millions)
Table 86. Global Automotive GPU Chip Sales Forecast by Type (2027-2032) & (K Pcs)
Table 87. Global Automotive GPU Chip Revenue Forecast by Type (2027-2032) & ($ millions)
Table 88. Global Automotive GPU Chip Sales Forecast by Application (2027-2032) & (K Pcs)
Table 89. Global Automotive GPU Chip Revenue Forecast by Application (2027-2032) & ($ millions)
Table 90. NVIDIA Basic Information, Automotive GPU Chip Manufacturing Base, Sales Area and Its Competitors
Table 91. NVIDIA Automotive GPU Chip Product Portfolios and Specifications
Table 92. NVIDIA Automotive GPU Chip Sales (K Pcs), Revenue ($ Million), Price (US$/Pcs) and Gross Margin (2021-2026)
Table 93. NVIDIA Main Business
Table 94. NVIDIA Latest Developments
Table 95. Qualcomm Basic Information, Automotive GPU Chip Manufacturing Base, Sales Area and Its Competitors
Table 96. Qualcomm Automotive GPU Chip Product Portfolios and Specifications
Table 97. Qualcomm Automotive GPU Chip Sales (K Pcs), Revenue ($ Million), Price (US$/Pcs) and Gross Margin (2021-2026)
Table 98. Qualcomm Main Business
Table 99. Qualcomm Latest Developments
Table 100. Renesas Electronics Basic Information, Automotive GPU Chip Manufacturing Base, Sales Area and Its Competitors
Table 101. Renesas Electronics Automotive GPU Chip Product Portfolios and Specifications

+357 96 030922
info@marketpublishers.com

Table 102. Renesas Electronics Automotive GPU Chip Sales (K Pcs), Revenue ($ Million), Price (US$/Pcs) and Gross Margin (2021-2026)
Table 103. Renesas Electronics Main Business
Table 104. Renesas Electronics Latest Developments
Table 105. Samsung Electronics Basic Information, Automotive GPU Chip Manufacturing Base, Sales Area and Its Competitors
Table 106. Samsung Electronics Automotive GPU Chip Product Portfolios and Specifications
Table 107. Samsung Electronics Automotive GPU Chip Sales (K Pcs), Revenue ($ Million), Price (US$/Pcs) and Gross Margin (2021-2026)
Table 108. Samsung Electronics Main Business
Table 109. Samsung Electronics Latest Developments
Table 110. MediaTek Basic Information, Automotive GPU Chip Manufacturing Base, Sales Area and Its Competitors
Table 111. MediaTek Automotive GPU Chip Product Portfolios and Specifications
Table 112. MediaTek Automotive GPU Chip Sales (K Pcs), Revenue ($ Million), Price (US$/Pcs) and Gross Margin (2021-2026)
Table 113. MediaTek Main Business
Table 114. MediaTek Latest Developments
Table 115. SemiDrive Basic Information, Automotive GPU Chip Manufacturing Base, Sales Area and Its Competitors
Table 116. SemiDrive Automotive GPU Chip Product Portfolios and Specifications
Table 117. SemiDrive Automotive GPU Chip Sales (K Pcs), Revenue ($ Million), Price (US$/Pcs) and Gross Margin (2021-2026)
Table 118. SemiDrive Main Business
Table 119. SemiDrive Latest Developments
Table 120. UNISOC Basic Information, Automotive GPU Chip Manufacturing Base, Sales Area and Its Competitors
Table 121. UNISOC Automotive GPU Chip Product Portfolios and Specifications
Table 122. UNISOC Automotive GPU Chip Sales (K Pcs), Revenue ($ Million), Price (US$/Pcs) and Gross Margin (2021-2026)
Table 123. UNISOC Main Business
Table 124. UNISOC Latest Developments
Table 125. SiEngine Basic Information, Automotive GPU Chip Manufacturing Base, Sales Area and Its Competitors
Table 126. SiEngine Automotive GPU Chip Product Portfolios and Specifications
Table 127. SiEngine Automotive GPU Chip Sales (K Pcs), Revenue ($ Million), Price (US$/Pcs) and Gross Margin (2021-2026)
Table 128. SiEngine Main Business

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Table 129. SiEngine Latest Developments

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List Of Figures

LIST OF FIGURES

Figure 1. Picture of Automotive GPU Chip
Figure 2. Automotive GPU Chip Report Years Considered
Figure 3. Research Objectives
Figure 4. Research Methodology
Figure 5. Research Process and Data Source
Figure 6. Global Automotive GPU Chip Sales Growth Rate 2021-2032 (K Pcs)
Figure 7. Global Automotive GPU Chip Revenue Growth Rate 2021-2032 ($ millions)
Figure 8. Automotive GPU Chip Sales by Geographic Region (2021, 2025 & 2032) & ($ millions)
Figure 9. Automotive GPU Chip Sales Market Share by Country/Region (2025)
Figure 10. Automotive GPU Chip Sales Market Share by Country/Region (2021, 2025 & 2032)
Figure 11. Product Picture of Discrete GPU
Figure 12. Product Picture of Integrated GPU
Figure 13. Global Automotive GPU Chip Sales Market Share by Type in 2026
Figure 14. Global Automotive GPU Chip Revenue Market Share by Type (2021-2026)
Figure 15. Product Picture of Entry-Level
Figure 16. Product Picture of Mainstream
Figure 17. Product Picture of High-Performance
Figure 18. Product Picture of Ultra-High Performance
Figure 19. Global Automotive GPU Chip Sales Market Share by Compute Performance Tier in 2026
Figure 20. Global Automotive GPU Chip Revenue Market Share by Compute Performance Tier (2021-2026)
Figure 21. Product Picture of Graphics-Centric
Figure 22. Product Picture of Vision-Centric
Figure 23. Product Picture of AI Inference-Centric
Figure 24. Product Picture of Mixed Workloads
Figure 25. Global Automotive GPU Chip Sales Market Share by Workload Focus in 2026
Figure 26. Global Automotive GPU Chip Revenue Market Share by Workload Focus (2021-2026)
Figure 27. Automotive GPU Chip Consumed in ADAS
Figure 28. Global Automotive GPU Chip Market: ADAS (2021-2026) & (K Pcs)
Figure 29. Automotive GPU Chip Consumed in Automatic Driving

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Figure 30. Global Automotive GPU Chip Market: Automatic Driving (2021-2026) & (K Pcs)
Figure 31. Automotive GPU Chip Consumed in Central Control Information System
Figure 32. Global Automotive GPU Chip Market: Central Control Information System (2021-2026) & (K Pcs)
Figure 33. Automotive GPU Chip Consumed in Other
Figure 34. Global Automotive GPU Chip Market: Other (2021-2026) & (K Pcs)
Figure 35. Global Automotive GPU Chip Sale Market Share by Application (2025)
Figure 36. Global Automotive GPU Chip Revenue Market Share by Application in 2026
Figure 37. Automotive GPU Chip Sales by Company in 2026 (K Pcs)
Figure 38. Global Automotive GPU Chip Sales Market Share by Company in 2026
Figure 39. Automotive GPU Chip Revenue by Company in 2026 ($ millions)
Figure 40. Global Automotive GPU Chip Revenue Market Share by Company in 2026
Figure 41. Global Automotive GPU Chip Sales Market Share by Geographic Region (2021-2026)
Figure 42. Global Automotive GPU Chip Revenue Market Share by Geographic Region in 2026
Figure 43. Americas Automotive GPU Chip Sales 2021-2026 (K Pcs)
Figure 44. Americas Automotive GPU Chip Revenue 2021-2026 ($ millions)
Figure 45. APAC Automotive GPU Chip Sales 2021-2026 (K Pcs)
Figure 46. APAC Automotive GPU Chip Revenue 2021-2026 ($ millions)
Figure 47. Europe Automotive GPU Chip Sales 2021-2026 (K Pcs)
Figure 48. Europe Automotive GPU Chip Revenue 2021-2026 ($ millions)
Figure 49. Middle East & Africa Automotive GPU Chip Sales 2021-2026 (K Pcs)
Figure 50. Middle East & Africa Automotive GPU Chip Revenue 2021-2026 ($ millions)
Figure 51. Americas Automotive GPU Chip Sales Market Share by Country in 2026
Figure 52. Americas Automotive GPU Chip Revenue Market Share by Country (2021-2026)
Figure 53. Americas Automotive GPU Chip Sales Market Share by Type (2021-2026)
Figure 54. Americas Automotive GPU Chip Sales Market Share by Application (2021-2026)
Figure 55. United States Automotive GPU Chip Revenue Growth 2021-2026 ($ millions)
Figure 56. Canada Automotive GPU Chip Revenue Growth 2021-2026 ($ millions)
Figure 57. Mexico Automotive GPU Chip Revenue Growth 2021-2026 ($ millions)
Figure 58. Brazil Automotive GPU Chip Revenue Growth 2021-2026 ($ millions)
Figure 59. APAC Automotive GPU Chip Sales Market Share by Region in 2026
Figure 60. APAC Automotive GPU Chip Revenue Market Share by Region (2021-2026)
Figure 61. APAC Automotive GPU Chip Sales Market Share by Type (2021-2026)
Figure 62. APAC Automotive GPU Chip Sales Market Share by Application (2021-2026)

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Figure 63. China Automotive GPU Chip Revenue Growth 2021-2026 ($ millions)
Figure 64. Japan Automotive GPU Chip Revenue Growth 2021-2026 ($ millions)
Figure 65. South Korea Automotive GPU Chip Revenue Growth 2021-2026 ($ millions)
Figure 66. Southeast Asia Automotive GPU Chip Revenue Growth 2021-2026 ($ millions)
Figure 67. India Automotive GPU Chip Revenue Growth 2021-2026 ($ millions)
Figure 68. Australia Automotive GPU Chip Revenue Growth 2021-2026 ($ millions)
Figure 69. China Taiwan Automotive GPU Chip Revenue Growth 2021-2026 ($ millions)
Figure 70. Europe Automotive GPU Chip Sales Market Share by Country in 2026
Figure 71. Europe Automotive GPU Chip Revenue Market Share by Country (2021-2026)
Figure 72. Europe Automotive GPU Chip Sales Market Share by Type (2021-2026)
Figure 73. Europe Automotive GPU Chip Sales Market Share by Application (2021-2026)
Figure 74. Germany Automotive GPU Chip Revenue Growth 2021-2026 ($ millions)
Figure 75. France Automotive GPU Chip Revenue Growth 2021-2026 ($ millions)
Figure 76. UK Automotive GPU Chip Revenue Growth 2021-2026 ($ millions)
Figure 77. Italy Automotive GPU Chip Revenue Growth 2021-2026 ($ millions)
Figure 78. Russia Automotive GPU Chip Revenue Growth 2021-2026 ($ millions)
Figure 79. Middle East & Africa Automotive GPU Chip Sales Market Share by Country (2021-2026)
Figure 80. Middle East & Africa Automotive GPU Chip Sales Market Share by Type (2021-2026)
Figure 81. Middle East & Africa Automotive GPU Chip Sales Market Share by Application (2021-2026)
Figure 82. Egypt Automotive GPU Chip Revenue Growth 2021-2026 ($ millions)
Figure 83. South Africa Automotive GPU Chip Revenue Growth 2021-2026 ($ millions)
Figure 84. Israel Automotive GPU Chip Revenue Growth 2021-2026 ($ millions)
Figure 85. Turkey Automotive GPU Chip Revenue Growth 2021-2026 ($ millions)
Figure 86. GCC Countries Automotive GPU Chip Revenue Growth 2021-2026 ($ millions)
Figure 87. Manufacturing Cost Structure Analysis of Automotive GPU Chip in 2026
Figure 88. Manufacturing Process Analysis of Automotive GPU Chip
Figure 89. Industry Chain Structure of Automotive GPU Chip
Figure 90. Channels of Distribution
Figure 91. Global Automotive GPU Chip Sales Market Forecast by Region (2027-2032)
Figure 92. Global Automotive GPU Chip Revenue Market Share Forecast by Region (2027-2032)
Figure 93. Global Automotive GPU Chip Sales Market Share Forecast by Type

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(2027-2032)
Figure 94. Global Automotive GPU Chip Revenue Market Share Forecast by Type (2027-2032)
Figure 95. Global Automotive GPU Chip Sales Market Share Forecast by Application (2027-2032)
Figure 96. Global Automotive GPU Chip Revenue Market Share Forecast by Application (2027-2032)

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