

Global 3D Stacked CMOS Image Sensor Market Growth 2026-2032

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Abstracts

The global 3D Stacked CMOS Image Sensor market size is predicted to grow from US\$ 936 million in 2025 to US\$ 1468 million in 2032; it is expected to grow at a CAGR of 6.6% from 2026 to 2032.

In 2025, global 3D Stacked CMOS Image Sensor production reached approximately 176.1 M units, with an average global market price of around 5,430 USD/K units.

3D Stacked CMOS Image Sensor is an advanced image sensor structured by vertically stacking two or more semiconductor wafers (including photosensitive layer, circuit layer and functional layer) through through-silicon vias (TSVs) or hybrid bonding technology. Different from traditional front-illuminated (FI) and back-illuminated (BSI) planar structures, it separates the photosensitive area from the signal processing circuit, allowing each layer to be independently optimized for its specific function. This design significantly enhances performance indicators such as light sensitivity, data transmission speed and integration density, while reducing chip area and power consumption, and is widely applied in high-end full-frame imaging scenarios that require balanced performance in resolution, speed and noise control.

The demand for 3D Stacked CMOS Image Sensors is driven by the upgrading of professional imaging equipment, the growing need for high-performance sensors in industrial measurement and medical diagnosis, and the expanding application of high-speed, high-definition imaging in aerospace and other fields. As a core technology for high-end full-frame sensors, it caters to the market pursuit of extreme imaging performance. Business opportunities lie in optimizing stacking processes to improve product yield and reliability, integrating intelligent processing functions into independent functional layers to meet smart imaging needs, developing products compatible with

global shutter technology for high-speed detection scenarios, and expanding applications in high-end full-frame equipment to seize the high-value market, while exploring cost-effective solutions to penetrate mid-range application fields.

LP Information, Inc. (LPI) ' newest research report, the “3D Stacked CMOS Image Sensor Industry Forecast” looks at past sales and reviews total world 3D Stacked CMOS Image Sensor sales in 2025, providing a comprehensive analysis by region and market sector of projected 3D Stacked CMOS Image Sensor sales for 2026 through 2032. With 3D Stacked CMOS Image Sensor sales broken down by region, market sector and sub-sector, this report provides a detailed analysis in US\$ millions of the world 3D Stacked CMOS Image Sensor industry.

This Insight Report provides a comprehensive analysis of the global 3D Stacked CMOS Image Sensor landscape and highlights key trends related to product segmentation, company formation, revenue, and market share, latest development, and M&A activity. This report also analyzes the strategies of leading global companies with a focus on 3D Stacked CMOS Image Sensor portfolios and capabilities, market entry strategies, market positions, and geographic footprints, to better understand these firms' unique position in an accelerating global 3D Stacked CMOS Image Sensor market.

This Insight Report evaluates the key market trends, drivers, and affecting factors shaping the global outlook for 3D Stacked CMOS Image Sensor and breaks down the forecast by Stacking Layer Count, by Application, geography, and market size to highlight emerging pockets of opportunity. With a transparent methodology based on hundreds of bottom-up qualitative and quantitative market inputs, this study forecast offers a highly nuanced view of the current state and future trajectory in the global 3D Stacked CMOS Image Sensor.

This report presents a comprehensive overview, market shares, and growth opportunities of 3D Stacked CMOS Image Sensor market by product type, application, key manufacturers and key regions and countries.

Segmentation by Stacking Layer Count:

Dual-Layer Stacked

Triple-Layer Stacked

Multi-Layer Stacked

Segmentation by Bonding Technology:

TSV Bonding

Hybrid Bonding

Wafer-Level Bonding

Segmentation by Functional Layer Integration:

Photosensitive + Circuit Layer Stacked Sensor

Multi-Functional Circuit Layer Stacked Sensor

AI Processing Layer Integrated Stacked Sensor

Segmentation by Application:

Consumer Electronics

Automotive Electronics

Industrial Measurement

Medical Imaging

Others

This report also splits the market by region:

Americas

United States

Canada

Mexico

Brazil

APAC

China

Japan

Korea

Southeast Asia

India

Australia

Europe

Germany

France

UK

Italy

Russia

Middle East & Africa

Egypt

South Africa

Israel

Turkey

GCC Countries

The below companies that are profiled have been selected based on inputs gathered from primary experts and analysing the company's coverage, product portfolio, its market penetration.

SONY

Samsung

OmniVision Technologies

STMicroelectronics

Onsemi

SK Hynix

SmartSens

Canon

Key Questions Addressed in this Report

What is the 10-year outlook for the global 3D Stacked CMOS Image Sensor market?

What factors are driving 3D Stacked CMOS Image Sensor market growth, globally and by region?

Which technologies are poised for the fastest growth by market and region?

How do 3D Stacked CMOS Image Sensor market opportunities vary by end market size?

How does 3D Stacked CMOS Image Sensor break out by Stacking Layer Count, by Application?

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