

Global 3D Packaging Market Growth (Status and Outlook) 2024-2030

<https://marketpublishers.com/r/G995952898E7EN.html>

Date: August 2024

Pages: 84

Price: US\$ 3,660.00 (Single User License)

ID: G995952898E7EN

Abstracts

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According to this study, the global 3D Packaging market size will reach US\$ million by 2030.

The global market for semiconductor was estimated at US\$ 579 billion in the year 2022, is projected to US\$ 790 billion by 2029, growing at a CAGR of 6% during the forecast period. Although some major categories are still double-digit year-over-year growth in 2022, led by Analog with 20.76%, Sensor with 16.31%, and Logic with 14.46% growth, Memory declined with 12.64% year over year. The microprocessor (MPU) and microcontroller (MCU) segments will experience stagnant growth due to weak shipments and investment in notebooks, computers, and standard desktops. In the current market scenario, the growing popularity of IoT-based electronics is stimulating the need for powerful processors and controllers. Hybrid MPUs and MCUs provide real-time embedded processing and control for the topmost IoT-based applications, resulting in significant market growth. The Analog IC segment is expected to grow gradually, while demand from the networking and communications industries is limited. Few of the emerging trends in the growing demand for Analog integrated circuits include signal conversion, automotive-specific Analog applications, and power management. They drive the growing demand for discrete power devices.

This report presents a comprehensive overview, market shares, and growth opportunities of 3D Packaging market by product type, application, key players and key regions and countries.

Segmentation by product type:

3D Wire Bonding

3D TSV

Others

Segmentation by Application:

Consumer Electronics

Industrial

Automotive & Transport

IT & Telecommunication

Others

This report also splits the market by region:

United States

China

Europe

Other regions:

Japan

South Korea

Southeast Asia

Rest of world

The report also presents the market competition landscape and a corresponding detailed analysis of the major players in the market. The key players covered in this report:

IASE

Amkor

Intel

Samsung

AT&S

Toshiba

JCET

Qualcomm

IBM

SK Hynix

UTAC

TSMC

China Wafer Level CSP

Interconnect Systems

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