

2018 Top 5 Ferroelectric RAM Players in North America, Europe, Asia-Pacific, South America, Middle East and Africa

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Abstracts

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Ferroelectric RAM (FeRAM, F-RAM or FRAM) is a random-access memory similar in construction to DRAM but utilizing a ferroelectric layer instead of a dielectric layer to achieve non-volatility. FeRAM is one of a growing number of alternative non-volatile random-access memory technologies which can offer that same functionality as flash memory.

FeRAM consists of a grid of small capacitors and associated wiring and signaling transistors. Each storage element, a cell, consists of one capacitor and one transistor. Unlike the DRAM use a linear dielectric in its cell capacitor, dielectric structure in the FeRAM cell capacitor usually contains ferroelectric material, typically lead zirconate titanate (PZT).

A ferroelectric material has a nonlinear relationship between the applied electric field and the apparent stored charge. The ferroelectric characteristic has the form of a hysteresis loop, which is very similar in shape to the hysteresis loop of ferromagnetic materials. The dielectric constant of a ferroelectric is typically much higher than that of a linear dielectric because of the effects of semi-permanent electric dipoles formed in the crystal structure of the ferroelectric material. When an external electric field is applied across a dielectric, the dipoles tend to align themselves with the field direction, produced by small shifts in the positions of atoms and shifts in the distributions of electronic charge in the crystal structure. After the charge is removed, the dipoles retain their polarization state. Binary "0"s and "1"s are stored as one of two possible electric polarizations in each data storage cell. For example, in the figure a "1" is encoded using

the negative remnant polarization "-Pr", and a "0" is encoded using the positive remnant polarization "+Pr". In terms of operation, FeRAM is similar to DRAM. Writing is accomplished by applying a field across the ferroelectric layer by charging the plates on either side of it, forcing the atoms inside into the "up" or "down" orientation (depending on the polarity of the charge), thereby storing a "1" or "0". Reading, however, is somewhat different than in DRAM. The transistor forces the cell into a particular state, say "0". If the cell already held a "0", nothing will happen in the output lines. If the cell held a "1", the re-orientation of the atoms in the film will cause a brief pulse of current in the output as they push electrons out of the metal on the "down" side. The presence of this pulse means the cell held a "1". Since this process overwrites the cell, reading FeRAM is a destructive process, and requires the cell to be re-written if it was changed. Ferroelectric RAM was proposed by MIT graduate student Dudley Allen Buck in his master's thesis, *Ferroelectrics for Digital Information Storage and Switching*, published in 1952. Development of FeRAM began in the late 1980s. Work was done in 1991 at NASA's Jet Propulsion Laboratory on improving methods of read out, including a novel method of non-destructive readout using pulses of UV radiation. Much of the current FeRAM technology was developed by Ramtron, a fabless semiconductor company. One major licensee is Fujitsu, who operates what is probably the largest semiconductor foundry production line with FeRAM capability. Since 1999 they have been using this line to produce standalone FeRAMs, as well as specialized chips (e.g. chips for smart cards) with embedded FeRAMs. Fujitsu produced devices for Ramtron until 2010. Since 2010 Ramtron's fabricators have been TI (Texas Instruments) and IBM. Since at least 2001 Texas Instruments has collaborated with Ramtron to develop FeRAM test chips in a modified 130 nm process. In the fall of 2005, Ramtron reported that they were evaluating prototype samples of an 8-megabit FeRAM manufactured using Texas Instruments' FeRAM process. Fujitsu and Seiko-Epson were in 2005 collaborating in the development of a 180 nm FeRAM process. In 2012 Ramtron was acquired by Cypress Semiconductor. FeRAM research projects have also been reported at Samsung, Matsushita, Oki, Toshiba, Infineon, Hynix, Symetrix, Cambridge University, University of Toronto, and the Interuniversity Microelectronics Centre (IMEC, Belgium).

Over the next five years, LPI(LP Information) projects that Ferroelectric RAM will register a 3.7% CAGR in terms of revenue, reach US\$ 300 million by 2023, from US\$ 240 million in 2017.

This report studies the global market, especially in North America, Europe, Asia-Pacific, South America, Middle East and Africa, focuses on the top 5 players in each region, with sales, price, revenue and market share from 2013 to 2018, the top players:

Cypress Semiconductor

Fujitsu

Texas Instruments

IBM

Infineon

Market Segment by Regions, this report splits Global into several key Regions, with sales, revenue, market share of top players in these regions, from 2013 to 2018 (forecast), like

North America (United States, Canada and Mexico)

Asia-Pacific (China, Japan, Southeast Asia, India and Korea)

Europe (Germany, UK, France, Italy and Russia etc.)

South America (Brazil, Chile, Peru and Argentina)

Middle East and Africa (Egypt, South Africa, Saudi Arabia)

Split by Product Types, with sales, revenue, price, market share of each type, can be divided into

Serial Memory

Parallel Memory

Others

Split by applications, this report focuses on sales, market share and growth rate in each application, can be divided into

Smart Meters

Automotive Electronics

Medical Devices

Wearable Devices

Contents

1 FERROELECTRIC RAM MARKET OVERVIEW

- 1.1 Product Overview and Scope of Ferroelectric RAM
- 1.2 Ferroelectric RAM Segment by Types
 - 1.2.1 Global Sales Market Share of Ferroelectric RAM by Types in 2017
 - 1.2.2 Serial Memory
 - 1.2.2.1 Major Players of Type I
 - 1.2.3 Parallel Memory
 - 1.2.3.1 Major Players of Parallel Memory
 - 1.2.4 Others
 - 1.2.4.1 Major Players of Others
- 1.3 Ferroelectric RAM Segment by Applications
 - 1.3.1 Ferroelectric RAM Sales Market Share by Applications in 2017
 - 1.3.2 Smart Meters
 - 1.3.3 Automotive Electronics
 - 1.3.4 Medical Devices
 - 1.3.5 Wearable Devices
- 1.4 Ferroelectric RAM Market by Regions
 - 1.4.1 North America Status and Prospect (2013-2023)
 - 1.4.1.1 North America Ferroelectric RAM Revenue (Million USD) and Growth Rate (2013-2023)
 - 1.4.2 Asia-Pacific Status and Prospect (2013-2023)
 - 1.4.2.1 Asia-Pacific Ferroelectric RAM Revenue (Million USD) and Growth Rate (2013-2023)
 - 1.4.3 Europe Status and Prospect (2013-2023)
 - 1.4.3.1 Europe Ferroelectric RAM Revenue (Million USD) and Growth Rate (2013-2023)
 - 1.4.4 South America Status and Prospect (2013-2023)
 - 1.4.4.1 South America Ferroelectric RAM Revenue (Million USD) and Growth Rate (2013-2023)
 - 1.4.5 Middle East and Africa Status and Prospect (2013-2023)
 - 1.4.5.1 Middle East and Africa Ferroelectric RAM Revenue (Million USD) and Growth Rate (2013-2023)
- 1.5 Global Market Size (Value) of Ferroelectric RAM (2013-2023)

2 GLOBAL FERROELECTRIC RAM SALES, REVENUE (VALUE) AND MARKET SHARE BY PLAYERS

- 2.1 Global Ferroelectric RAM Sales and Market Share by Players in 2016 and 2017
 - 2.1.1 Global Ferroelectric RAM Sales by Players in 2016 and 2017
 - 2.1.2 Global Ferroelectric RAM Sales Market Share (%) by Players in 2016 and 2017
- 2.2 Global Ferroelectric RAM Revenue and Market Share by Players in 2016 and 2017
 - 2.2.1 Global Ferroelectric RAM Revenue by Players in 2016 and 2017
 - 2.2.2 Global Ferroelectric RAM Revenue Market Share (%) by Players in 2016 and 2017
- 2.3 Global Ferroelectric RAM Average Price by Players in 2016 and 2017
- 2.4 Global Ferroelectric RAM Manufacturing Base Distribution, Sales Area, Product Types by Players
 - 2.4.1 Global Ferroelectric RAM Manufacturing Base Distribution and Sales Area by Players
 - 2.4.2 Players Ferroelectric RAM Product Types
- 2.5 Ferroelectric RAM Market Competitive Situation and Trends
 - 2.5.1 Ferroelectric RAM Market Concentration Rate
 - 2.5.2 Ferroelectric RAM Market Share of Top 3 and Top 5 Players
 - 2.5.3 Mergers & Acquisitions, Expansion

3 GLOBAL FERROELECTRIC RAM SALES, REVENUE (VALUE) BY REGIONS, TYPE AND APPLICATION (2013-2018)

- 3.1 Global Ferroelectric RAM Sales, Revenue and Market Share by Regions (2013-2018)
 - 3.1.1 Global Ferroelectric RAM Sales and Market Share by Regions (2013-2018)
 - 3.1.2 Global Ferroelectric RAM Revenue and Market Share by Regions (2013-2018)
- 3.2 Global Ferroelectric RAM Sales, Revenue, Market Share and Price by Type (2013-2018)
 - 3.2.1 Global Ferroelectric RAM Sales and Market Share by Type (2013-2018)
 - 3.2.2 Global Ferroelectric RAM Revenue and Market Share by Type (2013-2018)
 - 3.2.3 Global Ferroelectric RAM Price by Type (2013-2018)
- 3.3 Global Ferroelectric RAM Sales and Market Share by Application (2013-2018)
 - 3.3.1 Global Ferroelectric RAM Sales by Application (2013-2018)
 - 3.3.2 Global Ferroelectric RAM Sales Market Share by Application (2013-2018)
- 3.4 Global Ferroelectric RAM Sales, Revenue, Price and Gross Margin (2013-2018)

4 NORTH AMERICA TOP 5 PLAYERS FERROELECTRIC RAM SALES, REVENUE AND PRICE

4.1 North America Top 5 Players Ferroelectric RAM Sales, Revenue and Market Share in 2016 and 2017

4.1.1 North America Top 5 Players Ferroelectric RAM Sales and Market Share in 2016 and 2017

4.1.2 North America Top 5 Players Ferroelectric RAM Revenue and Market Share in 2016 and 2017

4.2 North America Ferroelectric RAM Sales, Revenue, Market Share and Price by Type (2013-2018)

4.2.1 North America Ferroelectric RAM Sales and Market Share by Type (2013-2018)

4.2.1.1 North America Ferroelectric RAM Sales by Type (2013-2018)

4.2.1.2 North America Ferroelectric RAM Sales Market Share by Type (2013-2018)

4.2.2 North America Ferroelectric RAM Revenue and Market Share by Type (2013-2018)

4.2.2.1 North America Ferroelectric RAM Revenue by Type (2013-2018)

4.2.2.2 North America Ferroelectric RAM Revenue Market Share by Type (2013-2018)

4.2.3 North America Ferroelectric RAM Price by Type (2013-2018)

4.3 North America Ferroelectric RAM Sales and Market Share by Application (2013-2018)

4.3.1 North America Ferroelectric RAM Sales by Application (2013-2018)

4.3.2 North America Ferroelectric RAM Sales Market Share by Application (2013-2018)

4.4 North America Ferroelectric RAM Sales and Market Share by Country (US, Canada and Mexico) (2013-2018)

4.4.1 North America Ferroelectric RAM Sales by Country (2013-2018)

4.4.2 North America Ferroelectric RAM Sales Market Share by Country (2013-2018)

4.5 North America Ferroelectric RAM Import & Export (2013-2018)

5 EUROPE TOP 5 PLAYERS FERROELECTRIC RAM SALES, REVENUE AND PRICE

5.1 Europe Top 5 Players Ferroelectric RAM Sales, Revenue and Market Share in 2016 and 2017

5.1.1 Europe Top 5 Players Ferroelectric RAM Sales and Market Share in 2016 and 2017

5.1.2 Europe Top 5 Players Ferroelectric RAM Revenue and Market Share in 2016 and 2017

5.2 Europe Ferroelectric RAM Sales, Revenue, Market Share and Price by Type (2013-2018)

- 5.2.1 Europe Ferroelectric RAM Sales and Market Share by Type (2013-2018)
 - 5.2.1.1 Europe Ferroelectric RAM Sales by Type (2013-2018)
 - 5.2.1.2 Europe Ferroelectric RAM Sales Market Share by Type (2013-2018)
- 5.2.2 Europe Ferroelectric RAM Revenue and Market Share by Type (2013-2018)
 - 5.2.2.1 Europe Ferroelectric RAM Revenue by Type (2013-2018)
 - 5.2.2.2 Europe Ferroelectric RAM Revenue Market Share by Type (2013-2018)
- 5.2.3 Europe Ferroelectric RAM Price by Type (2013-2018)
- 5.3 Europe Ferroelectric RAM Sales and Market Share by Application (2013-2018)
 - 5.3.1 Europe Ferroelectric RAM Sales by Application (2013-2018)
 - 5.3.2 Europe Ferroelectric RAM Sales Market Share by Application (2013-2018)
- 5.4 Europe Ferroelectric RAM Sales and Market Share by Country (Germany, UK, France, Italy and Russia) (2013-2018)
 - 5.4.1 Europe Ferroelectric RAM Sales by Country (2013-2018)
 - 5.4.2 Europe Ferroelectric RAM Sales Market Share by Country (2013-2018)
- 5.5 Europe Ferroelectric RAM Import & Export (2013-2018)

6 ASIA-PACIFIC TOP 5 PLAYERS FERROELECTRIC RAM SALES, REVENUE AND PRICE

- 6.1 Asia-Pacific Top 5 Players Ferroelectric RAM Sales, Revenue and Market Share in 2016 and 2017
 - 6.1.1 Asia-Pacific Top 5 Players Ferroelectric RAM Sales and Market Share in 2016 and 2017
 - 6.1.2 Asia-Pacific Top 5 Players Ferroelectric RAM Revenue and Market Share in 2016 and 2017
- 6.2 Asia-Pacific Ferroelectric RAM Sales, Revenue, Market Share and Price by Type (2013-2018)
 - 6.2.1 Asia-Pacific Ferroelectric RAM Sales and Market Share by Type (2013-2018)
 - 6.2.1.1 Asia-Pacific Ferroelectric RAM Sales by Type (2013-2018)
 - 6.2.1.2 Asia-Pacific Ferroelectric RAM Sales Market Share by Type (2013-2018)
 - 6.2.2 Asia-Pacific Ferroelectric RAM Revenue and Market Share by Type (2013-2018)
 - 6.2.3 Asia-Pacific Ferroelectric RAM Price by Type (2013-2018)
- 6.3 Asia-Pacific Ferroelectric RAM Sales and Market Share by Application (2013-2018)
 - 6.3.1 Asia-Pacific Ferroelectric RAM Sales by Application (2013-2018)
 - 6.3.2 Asia-Pacific Ferroelectric RAM Sales Market Share by Application (2013-2018)
- 6.4 Asia-Pacific Ferroelectric RAM Sales and Market Share by Country (China, Japan, Southeast Asia, India, Korea and Australia) (2013-2018)
 - 6.4.1 Asia-Pacific Ferroelectric RAM Sales by Country (2013-2018)
 - 6.4.2 Asia-Pacific Ferroelectric RAM Sales Market Share by Country (2013-2018)

6.5 Asia-Pacific Ferroelectric RAM Import & Export (2013-2018)

7 SOUTH AMERICA TOP 5 PLAYERS FERROELECTRIC RAM SALES, REVENUE AND PRICE

7.1 South America Top 5 Players Ferroelectric RAM Sales, Revenue and Market Share in 2016 and 2017

7.1.1 South America Top 5 Players Ferroelectric RAM Sales and Market Share in 2016 and 2017

7.1.2 South America Top 5 Players Ferroelectric RAM Revenue and Market Share in 2016 and 2017

7.2 South America Ferroelectric RAM Sales, Revenue, Market Share and Price by Type (2013-2018)

7.2.1 South America Ferroelectric RAM Sales and Market Share by Type (2013-2018)

7.2.1.1 South America Ferroelectric RAM Sales by Type (2013-2018)

7.2.1.2 South America Ferroelectric RAM Sales Market Share by Type (2013-2018)

7.2.2 South America Ferroelectric RAM Revenue and Market Share by Type (2013-2018)

7.2.2.1 South America Ferroelectric RAM Revenue by Type (2013-2018)

7.2.2.2 South America Ferroelectric RAM Revenue and Market Share by Type (2013-2018)

7.2.3 South America Ferroelectric RAM Price by Type (2013-2018)

7.3 South America Ferroelectric RAM Sales and Market Share by Application (2013-2018)

7.3.1 South America Ferroelectric RAM Sales by Application (2013-2018)

7.3.2 South America Ferroelectric RAM Sales Market Share by Application (2013-2018)

7.4 South America Ferroelectric RAM Sales and Market Share by Country (Brazil, Argentina, Chile and Peru) (2013-2018)

7.4.1 South America Ferroelectric RAM Sales by Country (2013-2018)

7.4.2 South America Ferroelectric RAM Sales Market Share by Country (2013-2018)

7.5 South America Ferroelectric RAM Import & Export (2013-2018)

8 MIDDLE EAST & AFRICA TOP 5 PLAYERS FERROELECTRIC RAM SALES, REVENUE AND PRICE

8.1 Middle East & Africa Top 5 Players Ferroelectric RAM Sales, Revenue and Market Share in 2016 and 2017

8.1.1 Middle East & Africa Top 5 Players Ferroelectric RAM Sales and Market Share in

2016 and 2017

8.1.2 Middle East & Africa Top 5 Players Ferroelectric RAM Revenue and Market Share in 2016 and 2017

8.2 Middle East & Africa Ferroelectric RAM Sales, Revenue, Market Share and Price by Type (2013-2018)

8.2.1 Middle East & Africa Ferroelectric RAM Sales and Market Share by Type (2013-2018)

8.2.1.1 Middle East & Africa Ferroelectric RAM Sales by Type (2013-2018)

8.2.1.2 Middle East & Africa Ferroelectric RAM Sales Market Share by Type (2013-2018)

8.2.2 Middle East & Africa Ferroelectric RAM Revenue and Market Share by Type (2013-2018)

8.2.2.1 Middle East & Africa Ferroelectric RAM Revenue by Type (2013-2018)

8.2.2.2 Middle East & Africa Ferroelectric RAM Revenue Market Share by Type (2013-2018)

8.2.3 Middle East & Africa Ferroelectric RAM Price by Type (2013-2018)

8.3 Middle East & Africa Ferroelectric RAM Sales and Market Share by Application (2013-2018)

8.3.1 Middle East & Africa Ferroelectric RAM Sales by Application (2013-2018)

8.3.2 Middle East & Africa Ferroelectric RAM Sales Market Share by Application (2013-2018)

8.4 Middle East & Africa Ferroelectric RAM Sales and Market Share by Country (Egypt, Saudi Arabia, South Africa and Iran) (2013-2018)

8.4.1 Middle East & Africa Ferroelectric RAM Sales by Country (2013-2018)

8.4.2 Middle East & Africa Ferroelectric RAM Sales Market Share by Country (2013-2018)

8.5 Middle East & Africa Ferroelectric RAM Import & Export (2013-2018)

9 GLOBAL FERROELECTRIC RAM PLAYERS PROFILES/ANALYSIS

9.1 Cypress Semiconductor

9.1.1 Company Basic Information, Manufacturing Base, Sales Area and Its Competitors

9.1.2 Ferroelectric RAM Product Types, Application and Specification

9.1.2.1 Product A

9.1.2.2 Product B

9.1.3 Cypress Semiconductor Ferroelectric RAM Sales, Revenue, Price and Gross Margin (2013-2018)

9.1.4 Main Business/Business Overview

- 9.1.5 Cypress Semiconductor News
- 9.2 Fujitsu
 - 9.2.1 Company Basic Information, Manufacturing Base, Sales Area and Its Competitors
 - 9.2.2 Ferroelectric RAM Product Types, Application and Specification
 - 9.2.2.1 Product A
 - 9.2.2.2 Product B
 - 9.2.3 Fujitsu Ferroelectric RAM Sales, Revenue, Price and Gross Margin (2013-2018)
 - 9.2.4 Main Business/Business Overview
 - 9.2.5 Fujitsu News
- 9.3 Texas Instruments
 - 9.3.1 Company Basic Information, Manufacturing Base, Sales Area and Its Competitors
 - 9.3.2 Ferroelectric RAM Product Types, Application and Specification
 - 9.3.2.1 Product A
 - 9.3.2.2 Product B
 - 9.3.3 Texas Instruments Ferroelectric RAM Sales, Revenue, Price and Gross Margin (2013-2018)
 - 9.3.4 Main Business/Business Overview
 - 9.3.5 Texas Instruments News
- 9.4 IBM
 - 9.4.1 Company Basic Information, Manufacturing Base, Sales Area and Its Competitors
 - 9.4.2 Ferroelectric RAM Product Types, Application and Specification
 - 9.4.2.1 Product A
 - 9.4.2.2 Product B
 - 9.4.3 IBM Ferroelectric RAM Sales, Revenue, Price and Gross Margin (2013-2018)
 - 9.4.4 Main Business/Business Overview
 - 9.4.5 IBM News
- 9.5 Infineon
 - 9.5.1 Company Basic Information, Manufacturing Base, Sales Area and Its Competitors
 - 9.5.2 Ferroelectric RAM Product Types, Application and Specification
 - 9.5.2.1 Product A
 - 9.5.2.2 Product B
 - 9.5.3 Infineon Ferroelectric RAM Sales, Revenue, Price and Gross Margin (2013-2018)
 - 9.5.4 Main Business/Business Overview
 - 9.5.5 Infineon News

10 GLOBAL FERROELECTRIC RAM MARKET FORECAST (2018-2023)

- 10.1 Global Ferroelectric RAM Sales, Revenue Forecast (2018-2023)
- 10.2 Global Ferroelectric RAM Sales, Consumption Forecast by Regions (2018-2023)
 - 10.2.1 Global Ferroelectric RAM Sales Forecast by Regions (2018-2023)
 - 10.2.2 Global Ferroelectric RAM Sales Market Share Forecast by Regions (2018-2023)
- 10.3 Global Ferroelectric RAM Sales and Market Share Forecast by Type (2018-2023)
 - 10.3.1 Global Ferroelectric RAM Sales Forecast by Type (2018-2023)
 - 10.3.2 Global Ferroelectric RAM Sales Market Share Forecast by Type (2018-2023)
- 10.4 Global Ferroelectric RAM Sales and Market Share Forecast by Application (2018-2023)
 - 10.4.1 Global Ferroelectric RAM Sales Forecast by Application (2018-2023)
 - 10.4.2 Global Ferroelectric RAM Sales Market Share Forecast by Application (2018-2023)
- 10.5 Ferroelectric RAM Price Forecast (2018-2023)

11 FERROELECTRIC RAM MANUFACTURING COST ANALYSIS

- 11.1 Ferroelectric RAM Key Raw Materials Analysis
 - 11.1.1 Key Raw Materials
 - 11.1.2 Price Trend of Key Raw Materials
 - 11.1.3 Key Suppliers of Raw Materials
 - 11.1.4 Market Concentration Rate of Raw Materials
- 11.2 Proportion of Manufacturing Cost Structure
 - 11.2.1 Raw Materials
 - 11.2.2 Labor Cost
 - 11.2.3 Manufacturing Expenses
- 11.3 Manufacturing Process Analysis of Ferroelectric RAM

12 INDUSTRIAL CHAIN, SOURCING STRATEGY AND DOWNSTREAM BUYERS

- 12.1 Ferroelectric RAM Industrial Chain Analysis
- 12.2 Upstream Raw Materials Sourcing
- 12.3 Raw Materials Sources of Ferroelectric RAM Major Players in 2017
- 12.4 Downstream Buyers

13 MARKETING STRATEGY ANALYSIS, DISTRIBUTORS/TRADERS

13.1 Marketing Channel

13.1.1 Direct Marketing

13.1.2 Indirect Marketing

13.1.3 Marketing Channel Development Trend

13.2 Market Positioning

13.2.1 Pricing Strategy

13.2.2 Brand Strategy

13.2.3 Target Client

13.3 Distributors/Traders List

14 MARKET EFFECT FACTORS ANALYSIS

14.1 Technology Progress/Risk

14.1.1 Substitutes Threat

14.1.2 Technology Progress in Related Industry

14.2 Consumer Needs/Customer Preference Change

14.3 Economic/Political Environmental Change

15 RESEARCH FINDINGS AND CONCLUSION

16 APPENDIX

16.1 Methodology

16.2 Data Source

List Of Tables

LIST OF TABLES AND FIGURES

Figure Picture of Ferroelectric RAM

Table Product Specifications of Ferroelectric RAM

Figure Global Sales Market Share of Ferroelectric RAM by Types in 2017

Figure Product Picture of Serial Memory

Table Major Players of Serial Memory

Figure Product Picture of Parallel Memory

Table Major Players of Parallel Memory

Figure Product Picture of Others

Table Major Players of Others

Figure Ferroelectric RAM Sales Market Share by Applications in 2017

Table Applications of Ferroelectric RAM

Figure Automotive Electronics Examples

Figure Medical Devices Examples

Figure Wearable Devices Examples

Figure North America Ferroelectric RAM Revenue (Million USD) and Growth Rate (2013-2023)

Figure Asia-Pacific Ferroelectric RAM Revenue (Million USD) and Growth Rate (2013-2023)

Figure Europe Ferroelectric RAM Revenue (Million USD) and Growth Rate (2013-2023)

Figure South America Ferroelectric RAM Revenue (Million USD) and Growth Rate (2013-2023)

Figure Middle East and Africa Ferroelectric RAM Revenue (Million USD) and Growth Rate (2013-2023)

Figure Global Ferroelectric RAM Revenue (Million USD) and Growth Rate (2013-2023)

Table Global Ferroelectric RAM Sales by Players in 2016 and 2017

Table Global Ferroelectric RAM Sales Market Share (%) by Players in 2016 and 2017

Figure Global Ferroelectric RAM Sales Market Share by Players in 2016

Figure Global Ferroelectric RAM Sales Market Share by Players in 2017

Table Global Ferroelectric RAM Revenue by Players in 2016 and 2017

Table Global Ferroelectric RAM Revenue Market Share (%) by Players in 2016 and 2017

Figure Global Ferroelectric RAM Revenue Market Share by Players in 2016

Figure Global Ferroelectric RAM Revenue Market Share by Players in 2017

Table Global Ferroelectric RAM Average Price by Players in 2016 and 2017

Figure Global Ferroelectric RAM Average Price by Players in 2017

Table Global Ferroelectric RAM Manufacturing Base Distribution and Sales Area by Players

Table Players Ferroelectric RAM Product Types

Figure Ferroelectric RAM Market Share of Top 3 Players

Figure Ferroelectric RAM Market Share of Top 5 Players

Table Global Ferroelectric RAM Sales by Regions (2013-2018)

Table Global Ferroelectric RAM Sales Market Share by Regions (2013-2018)

Figure Global Ferroelectric RAM Sales Market Share by Regions (2013-2018)

Figure Global Ferroelectric RAM Sales Market Share by Regions in 2017

Table Global Ferroelectric RAM Revenue by Regions (2013-2018)

Table Global Ferroelectric RAM Revenue Market Share by Regions (2013-2018)

Figure Global Ferroelectric RAM Revenue Market Share by Regions (2013-2018)

Figure Global Ferroelectric RAM Revenue Market Share by Regions in 2017

Table Global Ferroelectric RAM Sales by Type (2013-2018)

Table Global Ferroelectric RAM Sales Market Share by Type (2013-2018)

Figure Global Ferroelectric RAM Sales Market Share by Type (2013-2018)

Figure Global Ferroelectric RAM Sales Market Share by Type in 2017

Table Global Ferroelectric RAM Revenue by Type (2013-2018)

Table Global Ferroelectric RAM Revenue Market Share by Type (2013-2018)

Figure Global Ferroelectric RAM Revenue Market Share by Type (2013-2018)

Figure Global Ferroelectric RAM Revenue Market Share by Type in 2017

Table Global Ferroelectric RAM Price by Type (2013-2018)

Table Global Ferroelectric RAM Sales by Application (2013-2018)

Figure Global Ferroelectric RAM Sales Market Share by Application in 2017

Table Global Ferroelectric RAM Sales Market Share by Application (2013-2018)

Figure Global Ferroelectric RAM Sales Market Share by Application (2013-2018)

Table Global Ferroelectric RAM Sales, Revenue, Price and Gross Margin (2013-2018)

Table North America Top 5 Players Ferroelectric RAM Sales in 2016 and 2017

Table North America Top 5 Players Ferroelectric RAM Sales Market Share (%) in 2016 and 2017

Figure North America Top 5 Players Ferroelectric RAM Sales Share in 2016

Figure North America Top 5 Players Ferroelectric RAM Sales Share in 2017

Table North America Top 5 Players Ferroelectric RAM Revenue in 2016 and 2017

Table North America Top 5 Players Ferroelectric RAM Revenue Market Share (%) in 2016 and 2017

Figure North America Top 5 Players Ferroelectric RAM Revenue Share in 2016

Figure North America Top 5 Players Ferroelectric RAM Revenue Share in 2017

Table North America Ferroelectric RAM Sales by Type (2013-2018)

Figure 2017 North America Ferroelectric RAM Sales Market Share by Type

Table North America Ferroelectric RAM Sales Market Share by Type (2013-2018)
Figure North America Ferroelectric RAM Sales Market Share by Type (2013-2018)
Table North America Ferroelectric RAM Revenue by Type (2013-2018)
Figure North America Ferroelectric RAM Revenue Market Share by Type in 2017
Table North America Ferroelectric RAM Revenue Market Share by Type (2013-2018)
Figure North America Ferroelectric RAM Revenue Market Share by Type (2013-2018)
Table North America Ferroelectric RAM Price by Type (2013-2018)
Table North America Ferroelectric RAM Sales by Application (2013-2018)
Figure North America Ferroelectric RAM Sales by Application in 2017
Table North America Ferroelectric RAM Sales Market Share by Application (2013-2018)
Figure North America Ferroelectric RAM Sales Market Share by Application (2013-2018)
Table North America Ferroelectric RAM Sales by Country (2013-2018)
Figure 2017 North America Ferroelectric RAM Sales Market Share by Country
Table North America Ferroelectric RAM Sales Market Share by Country (2013-2018)
Figure North America Ferroelectric RAM Sales Market Share by Country (2013-2018)
Table North America Ferroelectric RAM Import & Export (2013-2018)
Table Europe Top 5 Players Ferroelectric RAM Sales in 2016 and 2017
Table Europe Top 5 Players Ferroelectric RAM Sales Market Share (%) in 2016 and 2017
Figure Europe Top 5 Players Ferroelectric RAM Sales Share in 2016
Figure Europe Top 5 Players Ferroelectric RAM Sales Share in 2017
Table Europe Top 5 Players Ferroelectric RAM Revenue in 2016 and 2017
Table Europe Top 5 Players Ferroelectric RAM Revenue Market Share (%) in 2016 and 2017
Figure Europe Top 5 Players Ferroelectric RAM Revenue Share in 2016
Figure Europe Top 5 Players Ferroelectric RAM Revenue Share in 2017
Table Europe Ferroelectric RAM Sales by Type (2013-2018)
Figure 2017 Europe Ferroelectric RAM Sales Market Share by Type
Table Europe Ferroelectric RAM Sales Market Share by Type (2013-2018)
Figure Europe Ferroelectric RAM Sales Market Share by Type (2013-2018)
Table Europe Ferroelectric RAM Revenue by Type (2013-2018)
Figure Europe Ferroelectric RAM Revenue Market Share by Type in 2017
Table Europe Ferroelectric RAM Revenue Market Share by Type (2013-2018)
Figure Europe Ferroelectric RAM Revenue Market Share by Type (2013-2018)
Table Europe Ferroelectric RAM Price by Type (2013-2018)
Table Europe Ferroelectric RAM Sales by Application (2013-2018)
Figure Europe Ferroelectric RAM Sales Market Share by Application in 2017
Table Europe Ferroelectric RAM Sales Market Share by Application (2013-2018)

Figure Europe Ferroelectric RAM Sales Market Share by Application (2013-2018)

Table Europe Ferroelectric RAM Sales by Country (2013-2018)

Figure Europe Ferroelectric RAM Sales Market Share by Country in 2017

Table Europe Ferroelectric RAM Sales Market Share by Country (2013-2018)

Figure Europe Ferroelectric RAM Sales Market Share by Country (2013-2018)

Table Europe Ferroelectric RAM Import & Export (2013-2018)

Table Asia-Pacific Top 5 Players Ferroelectric RAM Sales in 2016 and 2017

Table Asia-Pacific Top 5 Players Ferroelectric RAM Sales Market Share (%) in 2016 and 2017

Figure Asia-Pacific Top 5 Players Ferroelectric RAM Sales Share in 2016

Figure Asia-Pacific Top 5 Players Ferroelectric RAM Sales Share in 2017

Table Asia-Pacific Top 5 Players Ferroelectric RAM Revenue in 2016 and 2017

Table Asia-Pacific Top 5 Players Ferroelectric RAM Revenue Market Share (%) in 2016 and 2017

Figure Asia-Pacific Top 5 Players Ferroelectric RAM Revenue Share in 2016

Figure Asia-Pacific Top 5 Players Ferroelectric RAM Revenue Share in 2017

Table Asia-Pacific Ferroelectric RAM Sales by Type (2013-2018)

Figure Asia-Pacific Ferroelectric RAM Sales Market Share by Type in 2017

Table Asia-Pacific Ferroelectric RAM Sales Market Share by Type (2013-2018)

Figure Asia-Pacific Ferroelectric RAM Sales Market Share by Type (2013-2018)

Table Asia-Pacific Ferroelectric RAM Revenue by Type (2013-2018)

Table Asia-Pacific Ferroelectric RAM Revenue Market Share by Type (2013-2018)

Figure Asia-Pacific Ferroelectric RAM Revenue Market Share by Type (2013-2018)

Figure Asia-Pacific Ferroelectric RAM Revenue Market Share by Type in 2017

Table Asia-Pacific Ferroelectric RAM Price by Type (2013-2018)

Table Asia-Pacific Ferroelectric RAM Sales by Application (2013-2018)

Figure Asia-Pacific Ferroelectric RAM Sales by Application in 2017

Table Asia-Pacific Ferroelectric RAM Sales Market Share by Application (2013-2018)

Figure Asia-Pacific Ferroelectric RAM Sales Market Share by Application (2013-2018)

Table Asia-Pacific Ferroelectric RAM Sales by Country (2013-2018)

Figure Asia-Pacific Ferroelectric RAM Sales Market Share by Country in 2017

Table Asia-Pacific Ferroelectric RAM Sales Market Share by Country (2013-2018)

Figure Asia-Pacific Ferroelectric RAM Sales Market Share by Country (2013-2018)

Table Asia-Pacific Ferroelectric RAM Import & Export (2013-2018)

Table South America Top 5 Players Ferroelectric RAM Sales in 2016 and 2017

Table South America Top 5 Players Ferroelectric RAM Sales Market Share (%) in 2016 and 2017

Figure South America Top 5 Players Ferroelectric RAM Sales Share in 2016

Figure South America Top 5 Players Ferroelectric RAM Sales Share in 2017

Table South America Top 5 Players Ferroelectric RAM Revenue in 2016 and 2017

Table South America Top 5 Players Ferroelectric RAM Revenue Market Share (%) in 2016 and 2017

Figure South America Top 5 Players Ferroelectric RAM Revenue Share in 2016

Figure South America Top 5 Players Ferroelectric RAM Revenue Share in 2017

Table South America Ferroelectric RAM Sales by Type (2013-2018)

Figure South America Ferroelectric RAM Sales by Type in 2017

Table South America Ferroelectric RAM Sales Market Share by Type (2013-2018)

Figure South America Ferroelectric RAM Sales Market Share by Type (2013-2018)

Table South America Ferroelectric RAM Revenue by Type (2013-2018)

Figure South America Ferroelectric RAM Revenue by Type in 2017

Table South America Ferroelectric RAM Revenue Market Share by Type (2013-2018)

Figure South America Ferroelectric RAM Revenue Market Share by Type (2013-2018)

Table South America Ferroelectric RAM Price by Type (2013-2018)

Table South America Ferroelectric RAM Sales by Application (2013-2018)

Figure South America Ferroelectric RAM Sales by Application in 2017

Table South America Ferroelectric RAM Sales Market Share by Application (2013-2018)

Figure South America Ferroelectric RAM Sales Market Share by Application (2013-2018)

Table South America Ferroelectric RAM Sales by Country (2013-2018)

Figure South America Ferroelectric RAM Sales by Country in 2017

Table South America Ferroelectric RAM Sales Market Share by Country (2013-2018)

Figure South America Ferroelectric RAM Sales Market Share by Country (2013-2018)

Table South America Ferroelectric RAM Import & Export (2013-2018)

Table Middle East & Africa Top 5 Players Ferroelectric RAM Sales in 2016 and 2017

Table Middle East & Africa Top 5 Players Ferroelectric RAM Sales Market Share (%) in 2016 and 2017

Figure Middle East & Africa Top 5 Players Ferroelectric RAM Sales Share in 2016

Figure Middle East & Africa Top 5 Players Ferroelectric RAM Sales Share in 2017

Table Middle East & Africa Top 5 Players Ferroelectric RAM Revenue in 2016 and 2017

Table Middle East & Africa Top 5 Players Ferroelectric RAM Revenue Market Share (%) in 2016 and 2017

Figure Middle East & Africa Top 5 Players Ferroelectric RAM Revenue Share in 2016

Figure Middle East & Africa Top 5 Players Ferroelectric RAM Revenue Share in 2017

Table Middle East & Africa Ferroelectric RAM Sales by Type (2013-2018)

Figure Middle East & Africa Ferroelectric RAM Sales by Type in 2017

Table Middle East & Africa Ferroelectric RAM Sales Market Share by Type (2013-2018)

Figure Middle East & Africa Ferroelectric RAM Sales Market Share by Type (2013-2018)

Table Middle East & Africa Ferroelectric RAM Revenue by Type (2013-2018)
Figure Middle East & Africa Ferroelectric RAM Revenue by Type in 2017
Table Middle East & Africa Ferroelectric RAM Revenue Market Share by Type (2013-2018)
Figure Middle East & Africa Ferroelectric RAM Revenue Market Share by Type (2013-2018)
Table Middle East & Africa Ferroelectric RAM Price by Type (2013-2018)
Table Middle East & Africa Ferroelectric RAM Sales by Application (2013-2018)
Figure Middle East & Africa Ferroelectric RAM Sales by Application in 2017
Table Middle East & Africa Ferroelectric RAM Sales Market Share by Application (2013-2018)
Figure Middle East & Africa Ferroelectric RAM Sales Market Share by Application (2013-2018)
Table Middle East & Africa Ferroelectric RAM Sales by Country (2013-2018)
Figure Middle East & Africa Ferroelectric RAM Sales by Country in 2017
Table Middle East & Africa Ferroelectric RAM Sales Market Share by Country (2013-2018)
Figure Middle East & Africa Ferroelectric RAM Sales Market Share by Country (2013-2018)
Table Middle East & Africa Ferroelectric RAM Import & Export (2013-2018)
Table Cypress Semiconductor Basic Information, Manufacturing Base, Sales Area and Its Competitors
Table Cypress Semiconductor Ferroelectric RAM Sales, Revenue, Price and Gross Margin (2013-2018)
Figure Cypress Semiconductor Ferroelectric RAM Market Share (2013-2018)
Table Fujitsu Basic Information, Manufacturing Base, Sales Area and Its Competitors
Table Fujitsu Ferroelectric RAM Sales, Revenue, Price and Gross Margin (2013-2018)
Figure Fujitsu Ferroelectric RAM Market Share (2013-2018)
Table Texas Instruments Basic Information, Manufacturing Base, Sales Area and Its Competitors
Table Texas Instruments Ferroelectric RAM Sales, Revenue, Price and Gross Margin (2013-2018)
Figure Texas Instruments Ferroelectric RAM Market Share (2013-2018)
Table IBM Basic Information, Manufacturing Base, Sales Area and Its Competitors
Table IBM Ferroelectric RAM Sales, Revenue, Price and Gross Margin (2013-2018)
Figure IBM Ferroelectric RAM Market Share (2013-2018)
Table Infineon Basic Information, Manufacturing Base, Sales Area and Its Competitors
Table Infineon Ferroelectric RAM Sales, Revenue, Price and Gross Margin (2013-2018)
Figure Infineon Ferroelectric RAM Market Share (2013-2018)

Figure Global Ferroelectric RAM Sales and Growth Rate Forecast (2018-2023)
Figure Global Ferroelectric RAM Revenue and Growth Rate Forecast (2018-2023)
Table Global Ferroelectric RAM Sales Forecast by Regions (2018-2023)
Table Global Ferroelectric RAM Sales Market Share Forecast by Regions (2018-2023)
Table Global Ferroelectric RAM Sales Forecast by Type (2018-2023)
Table Global Ferroelectric RAM Sales Market Share Forecast by Type (2018-2023)
Table Global Ferroelectric RAM Sales Forecast by Application (2018-2023)
Table Global Ferroelectric RAM Sales Market Share Forecast by Application (2018-2023)
Table Sales Base and Market Concentration Rate of Raw Material
Figure Price Trend of Key Raw Materials
Table Key Suppliers of Raw Materials
Figure Manufacturing Cost Structure of Ferroelectric RAM
Figure Manufacturing Process Analysis of Ferroelectric RAM
Figure Ferroelectric RAM Industrial Chain Analysis
Table Raw Materials Sources of Ferroelectric RAM Major Players in 2017
Table Major Buyers of Ferroelectric RAM
Table Distributors/Traders List

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