

2018-2023 Global Semiconductor Chip Handler Consumption Market Report

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Abstracts

The report requires updating with new data and is sent in 48 hours after order is placed.

In this report, LP Information covers the present scenario (with the base year being 2017) and the growth prospects of global Semiconductor Chip Handler market for 2018-2023.

Semiconductor Chip Handlers are primarily used to for sorting, testing and transferring chips with the simple electrical interface diagram: ATE > ITA > DUT (package) Normally, packaged parts use a handler to place the device on a customized interface board, then IC chip handler transfers finished chips to the inspection equipment for final visual and electrical performance test and then sort the chips into good, bad or other bins based on test results.

Global semiconductor chip handler market will grow at a modest CAGR of just over 4% by the end of the forecast period. One of the primary drivers of this market is the increased demand for compact electronic devices. This trend is evident in sectors such as mobile phones, automotive, healthcare equipment, and even industrial manufacturing. This has led to semiconductor IC manufacturers investing in R&D to work towards reducing the size of ICs along with improved performance. Such R&D activities have resulted in the emergence of micro-electrical-mechanical systems (MEMs) and 3D stacked ICs. Consequently, growth in the MEMs market will propel demand for testing equipment for the complex ICs until the end of the forecast period. An important trend which is spurring market growth is the emergence of strip testing. Integrated device manufacturers and subcontractors use test-in-strip technology to cut down on costs. This technology helps testers to handle multiple small ICs at the same time, test over 100 in parallel while reducing bottlenecks. Unlike gravity, pick-and-place, and turret type handlers, strip test handlers can handle IC chips with 0.3mm x 0.6mm

dimensions and can test more than 100 devices in parallel without jams. The OSAT segment is the largest of the chip handler market and is expected to garner close to 63% of market share by the end of the forecast period. The growth of the semiconductor industry and rising costs of packaging and testing equipment will create a high demand for automated test equipment along with chip handlers from these OSATs. They are also investing in precision and high-speed IC test handlers so that they can speed up production and deliver high-performance products to market faster. Over the next five years, LPI(LP Information) projects that Semiconductor Chip Handler will register a 4.8% CAGR in terms of revenue, reach US\$ 680 million by 2023, from US\$ 510 million in 2017.

This report presents a comprehensive overview, market shares, and growth opportunities of Semiconductor Chip Handler market by product type, application, key manufacturers and key regions.

To calculate the market size, LP Information considers value and volume generated from the sales of the following segments:

Segmentation by product type:

Logic

Memory

Segmentation by application:

Outsourced Semiconductor Assembly and Test Providers (OSATs)

Integrated Device Manufacturers (IDMs)

This report also splits the market by region:

Americas

United States

Canada

Mexico

Brazil

APAC

China

Japan

Korea

Southeast Asia

India

Australia

Europe

Germany

France

UK

Italy

Russia

Spain

Middle East & Africa

Egypt

South Africa

Israel

Turkey

GCC Countries

The report also presents the market competition landscape and a corresponding detailed analysis of the major vendor/manufacturers in the market. The key manufacturers covered in this report:

Advantest

Cohu

Multitest

Boston Semi Equipment

Seiko Epson Corporation

ASM Pacific Technology

Hon Technologies

Chroma

SRM Integration

MCT

CST

TESEC Corporation

SYNAX

ChangChuan Technology

In addition, this report discusses the key drivers influencing market growth, opportunities, the challenges and the risks faced by key manufacturers and the market as a whole. It also analyzes key emerging trends and their impact on present and future development.

Research objectives

To study and analyze the global Semiconductor Chip Handler consumption (value & volume) by key regions/countries, product type and application, history data from 2013 to 2017, and forecast to 2023.

To understand the structure of Semiconductor Chip Handler market by identifying its various subsegments.

Focuses on the key global Semiconductor Chip Handler manufacturers, to define, describe and analyze the sales volume, value, market share, market competition landscape, SWOT analysis and development plans in next few years.

To analyze the Semiconductor Chip Handler with respect to individual growth trends, future prospects, and their contribution to the total market.

To share detailed information about the key factors influencing the growth of the market (growth potential, opportunities, drivers, industry-specific challenges and risks).

To project the consumption of Semiconductor Chip Handler submarkets, with respect to key regions (along with their respective key countries).

To analyze competitive developments such as expansions, agreements, new product launches, and acquisitions in the market.

To strategically profile the key players and comprehensively analyze their growth strategies.

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