

SRAM and ROM Design IP Market Report by Type (SRAM (Static Random Access Memory), ROM (Read-Only Memory)), and Region 2023-2028

<https://marketpublishers.com/r/SAF62473885AEN.html>

Date: November 2023

Pages: 140

Price: US\$ 2,499.00 (Single User License)

ID: SAF62473885AEN

Abstracts

The global SRAM and ROM design IP market size reached US\$ 646.4 Million in 2022. Looking forward, IMARC Group expects the market to reach US\$ 712.1 Million by 2028, exhibiting a growth rate (CAGR) of 3.0% during 2022-2028. The growing adoption of cloud storage solutions among small and large-scale enterprises, technological advancements in semiconductor manufacturing, and rising demand in data centers to handle huge data represent some of the key factors driving the market.

Static random-access memory (SRAM) design intellectual property (IP) is a memory technology that is designed for electronic systems. It is a volatile memory that stores data as long as power is supplied. It stores a bit of data on four transistors through two cross-coupled inverters. It can be operated at a higher speed and does not require data to be refreshed. It is widely utilized as cache memory in microprocessors and other high-speed applications. On the other hand, read-only memory (ROM) design IP is a non-volatile memory that contains pre-programmed data. It is easy to test and offers more reliability as compared to random access memory (RAM). It stores data and information that can only be read and cannot be modified. Besides this, it is utilized for storing firmware, boot code, and other fixed data in electronic systems.

SRAM and ROM Design IP Market Trends:

At present, the increasing adoption of cloud storage solutions among small and large-scale enterprises across the globe represents one of the key factors impelling the growth of the market. Besides this, the growing demand for efficient storage and processing due to the increasing utilization of the Internet of Things (IoT) devices is positively influencing the market. Additionally, the rising adoption of low-power and high-density storage solutions is bolstering the growth of the market. Apart from this, the increasing demand in data centers to handle a large amount of data smoothly is propelling the growth of the market. In addition, technological advancements in

semiconductor manufacturing, such as smaller node sizes and innovative memory architectures, to provide enhanced performance and reduce power consumption is contributing to the growth of the market. Moreover, the growing adoption of SRAM and ROM design IP on account of the increasing demand for high-performance memory solutions is offering a favorable market outlook. In line with this, the rising employment of SRAM, as it is faster than dynamic random-access memory (DRAM), is supporting the growth of the market. Furthermore, the increasing demand for SRAM and ROM design IP due to the introduction of advanced wireless networks is offering lucrative growth opportunities to industry investors.

Key Market Segmentation:

IMARC Group provides an analysis of the key trends in each segment of the global SRAM and ROM design IP market, along with forecasts at the global, regional, and country levels from 2023-2028. Our report has categorized the market based on type.

Type Insights:

SRAM (Static Random Access Memory)

ROM (Read-Only Memory)

The report has provided a detailed breakup and analysis of the SRAM and ROM design IP market based on the type. This includes SRAM (static random access memory) and ROM (read-only memory). According to the report, SRAM (static random access memory) represented the largest segment.

Regional Insights:

North America

United States

Canada

Asia Pacific

China

Japan

India

South Korea

Australia

Indonesia

Others

Europe

Germany

France

United Kingdom

Italy

Spain

Russia

Others

Latin America

Brazil

Mexico

Others

Middle East and Africa

The report has also provided a comprehensive analysis of all the major regional markets, which include North America (the United States and Canada); Asia Pacific (China, Japan, India, South Korea, Australia, Indonesia, and others); Europe (Germany, France, the United Kingdom, Italy, Spain, Russia, and others); Latin America (Brazil, Mexico, and others); and the Middle East and Africa. According to the report, Asia Pacific was the largest market for SRAM and ROM design IP. Some of the factors driving the Asia Pacific SRAM and ROM design IP market included the growing use of electronic products, rising adoption of memory solutions among various enterprises, presence of key market players, etc.

Competitive Landscape:

The report has also provided a comprehensive analysis of the competitive landscape in the global SRAM and ROM design IP market. Detailed profiles of all major companies have been provided. Some of the companies covered include Advanced Micro Devices Inc., Dolphin Technology Inc., Renesas Electronics Corporation, Surecore Limited, Synopsys Inc., etc. Kindly note that this only represents a partial list of companies, and the complete list has been provided in the report.

Key Questions Answered in This Report:

How has the global SRAM and ROM design IP market performed so far, and how will it perform in the coming years?

What are the drivers, restraints, and opportunities in the global SRAM and ROM design IP market?

What is the impact of each driver, restraint, and opportunity on the global SRAM and ROM design IP market?

What are the key regional markets?

Which countries represent the most attractive SRAM and ROM design IP market?

What is the breakup of the market based on the type?

Which is the most attractive type in the SRAM and ROM design IP market?

What is the competitive structure of the global SRAM and ROM design IP market?

Who are the key players/companies in the global SRAM and ROM design IP market?

Contents

1 PREFACE

2 SCOPE AND METHODOLOGY

- 2.1 Objectives of the Study
- 2.2 Stakeholders
- 2.3 Data Sources
 - 2.3.1 Primary Sources
 - 2.3.2 Secondary Sources
- 2.4 Market Estimation
 - 2.4.1 Bottom-Up Approach
 - 2.4.2 Top-Down Approach
- 2.5 Forecasting Methodology

3 EXECUTIVE SUMMARY

4 INTRODUCTION

- 4.1 Overview
- 4.2 Key Industry Trends

5 GLOBAL SRAM AND ROM DESIGN IP MARKET

- 5.1 Market Overview
- 5.2 Market Performance
- 5.3 Impact of COVID-19
- 5.4 Market Forecast

6 MARKET BREAKUP BY TYPE

- 6.1 SRAM (Static Random Access Memory)
 - 6.1.1 Market Trends
 - 6.1.2 Market Forecast
- 6.2 ROM (Read-Only Memory)
 - 6.2.1 Market Trends

6.2.2 Market Forecast

7 MARKET BREAKUP BY REGION

7.1 North America

7.1.1 United States

7.1.1.1 Market Trends

7.1.1.2 Market Forecast

7.1.2 Canada

7.1.2.1 Market Trends

7.1.2.2 Market Forecast

7.2 Asia-Pacific

7.2.1 China

7.2.1.1 Market Trends

7.2.1.2 Market Forecast

7.2.2 Japan

7.2.2.1 Market Trends

7.2.2.2 Market Forecast

7.2.3 India

7.2.3.1 Market Trends

7.2.3.2 Market Forecast

7.2.4 South Korea

7.2.4.1 Market Trends

7.2.4.2 Market Forecast

7.2.5 Australia

7.2.5.1 Market Trends

7.2.5.2 Market Forecast

7.2.6 Indonesia

7.2.6.1 Market Trends

7.2.6.2 Market Forecast

7.2.7 Others

7.2.7.1 Market Trends

7.2.7.2 Market Forecast

7.3 Europe

7.3.1 Germany

7.3.1.1 Market Trends

7.3.1.2 Market Forecast

7.3.2 France

7.3.2.1 Market Trends

- 7.3.2.2 Market Forecast
- 7.3.3 United Kingdom
 - 7.3.3.1 Market Trends
 - 7.3.3.2 Market Forecast
- 7.3.4 Italy
 - 7.3.4.1 Market Trends
 - 7.3.4.2 Market Forecast
- 7.3.5 Spain
 - 7.3.5.1 Market Trends
 - 7.3.5.2 Market Forecast
- 7.3.6 Russia
 - 7.3.6.1 Market Trends
 - 7.3.6.2 Market Forecast
- 7.3.7 Others
 - 7.3.7.1 Market Trends
 - 7.3.7.2 Market Forecast
- 7.4 Latin America
 - 7.4.1 Brazil
 - 7.4.1.1 Market Trends
 - 7.4.1.2 Market Forecast
 - 7.4.2 Mexico
 - 7.4.2.1 Market Trends
 - 7.4.2.2 Market Forecast
 - 7.4.3 Others
 - 7.4.3.1 Market Trends
 - 7.4.3.2 Market Forecast
- 7.5 Middle East and Africa
 - 7.5.1 Market Trends
 - 7.5.2 Market Breakup by Country
 - 7.5.3 Market Forecast

8 DRIVERS, RESTRAINTS, AND OPPORTUNITIES

- 8.1 Overview
- 8.2 Drivers
- 8.3 Restraints
- 8.4 Opportunities

9 VALUE CHAIN ANALYSIS

10 PORTERS FIVE FORCES ANALYSIS

- 10.1 Overview
- 10.2 Bargaining Power of Buyers
- 10.3 Bargaining Power of Suppliers
- 10.4 Degree of Competition
- 10.5 Threat of New Entrants
- 10.6 Threat of Substitutes

11 PRICE ANALYSIS

12 COMPETITIVE LANDSCAPE

- 12.1 Market Structure
- 12.2 Key Players
- 12.3 Profiles of Key Players
 - 12.3.1 Advanced Micro Devices Inc.
 - 12.3.1.1 Company Overview
 - 12.3.1.2 Product Portfolio
 - 12.3.1.3 Financials
 - 12.3.1.4 SWOT Analysis
 - 12.3.2 Dolphin Technology Inc.
 - 12.3.2.1 Company Overview
 - 12.3.2.2 Product Portfolio
 - 12.3.3 Renesas Electronics Corporation
 - 12.3.3.1 Company Overview
 - 12.3.3.2 Product Portfolio
 - 12.3.3.3 Financials
 - 12.3.3.4 SWOT Analysis
 - 12.3.4 Surecore Limited
 - 12.3.4.1 Company Overview
 - 12.3.4.2 Product Portfolio
 - 12.3.5 Synopsys Inc.
 - 12.3.5.1 Company Overview
 - 12.3.5.2 Product Portfolio
 - 12.3.5.3 Financials

12.3.5.4 SWOT Analysis

Kindly note that this only represents a partial list of companies, and the complete list has been provided in the report.

List Of Tables

LIST OF TABLES

Table 1: Global: SRAM and ROM Design IP Market: Key Industry Highlights, 2022 & 2028

Table 2: Global: SRAM and ROM Design IP Market Forecast: Breakup by Type (in Million US\$), 2023-2028

Table 3: Global: SRAM and ROM Design IP Market Forecast: Breakup by Region (in Million US\$), 2023-2028

Table 4: Global: SRAM and ROM Design IP Market: Competitive Structure

Table 5: Global: SRAM and ROM Design IP Market: Key Players

List Of Figures

LIST OF FIGURES

Figure 1: Global: SRAM and ROM Design IP Market: Major Drivers and Challenges

Figure 2: Global: SRAM and ROM Design IP Market: Sales Value (in Million US\$), 2017-2022

Figure 3: Global: SRAM and ROM Design IP Market Forecast: Sales Value (in Million US\$), 2023-2028

Figure 4: Global: SRAM and ROM Design IP Market: Breakup by Type (in %), 2022

Figure 5: Global: SRAM and ROM Design IP Market: Breakup by Region (in %), 2022

Figure 6: Global: SRAM and ROM Design IP (SRAM (Static Random Access Memory)) Market: Sales Value (in Million US\$), 2017 & 2022

Figure 7: Global: SRAM and ROM Design IP (SRAM (Static Random Access Memory)) Market Forecast: Sales Value (in Million US\$), 2023-2028

Figure 8: Global: SRAM and ROM Design IP (ROM (Read-Only Memory)) Market: Sales Value (in Million US\$), 2017 & 2022

Figure 9: Global: SRAM and ROM Design IP (ROM (Read-Only Memory)) Market Forecast: Sales Value (in Million US\$), 2023-2028

Figure 10: North America: SRAM and ROM Design IP Market: Sales Value (in Million US\$), 2017 & 2022

Figure 11: North America: SRAM and ROM Design IP Market Forecast: Sales Value (in Million US\$), 2023-2028

Figure 12: United States: SRAM and ROM Design IP Market: Sales Value (in Million US\$), 2017 & 2022

Figure 13: United States: SRAM and ROM Design IP Market Forecast: Sales Value (in Million US\$), 2023-2028

Figure 14: Canada: SRAM and ROM Design IP Market: Sales Value (in Million US\$), 2017 & 2022

Figure 15: Canada: SRAM and ROM Design IP Market Forecast: Sales Value (in Million US\$), 2023-2028

Figure 16: Asia-Pacific: SRAM and ROM Design IP Market: Sales Value (in Million US\$), 2017 & 2022

Figure 17: Asia-Pacific: SRAM and ROM Design IP Market Forecast: Sales Value (in Million US\$), 2023-2028

Figure 18: China: SRAM and ROM Design IP Market: Sales Value (in Million US\$), 2017 & 2022

Figure 19: China: SRAM and ROM Design IP Market Forecast: Sales Value (in Million US\$), 2023-2028

Figure 20: Japan: SRAM and ROM Design IP Market: Sales Value (in Million US\$), 2017 & 2022

Figure 21: Japan: SRAM and ROM Design IP Market Forecast: Sales Value (in Million US\$), 2023-2028

Figure 22: India: SRAM and ROM Design IP Market: Sales Value (in Million US\$), 2017 & 2022

Figure 23: India: SRAM and ROM Design IP Market Forecast: Sales Value (in Million US\$), 2023-2028

Figure 24: South Korea: SRAM and ROM Design IP Market: Sales Value (in Million US\$), 2017 & 2022

Figure 25: South Korea: SRAM and ROM Design IP Market Forecast: Sales Value (in Million US\$), 2023-2028

Figure 26: Australia: SRAM and ROM Design IP Market: Sales Value (in Million US\$), 2017 & 2022

Figure 27: Australia: SRAM and ROM Design IP Market Forecast: Sales Value (in Million US\$), 2023-2028

Figure 28: Indonesia: SRAM and ROM Design IP Market: Sales Value (in Million US\$), 2017 & 2022

Figure 29: Indonesia: SRAM and ROM Design IP Market Forecast: Sales Value (in Million US\$), 2023-2028

Figure 30: Others: SRAM and ROM Design IP Market: Sales Value (in Million US\$), 2017 & 2022

Figure 31: Others: SRAM and ROM Design IP Market Forecast: Sales Value (in Million US\$), 2023-2028

Figure 32: Europe: SRAM and ROM Design IP Market: Sales Value (in Million US\$), 2017 & 2022

Figure 33: Europe: SRAM and ROM Design IP Market Forecast: Sales Value (in Million US\$), 2023-2028

Figure 34: Germany: SRAM and ROM Design IP Market: Sales Value (in Million US\$), 2017 & 2022

Figure 35: Germany: SRAM and ROM Design IP Market Forecast: Sales Value (in Million US\$), 2023-2028

Figure 36: France: SRAM and ROM Design IP Market: Sales Value (in Million US\$), 2017 & 2022

Figure 37: France: SRAM and ROM Design IP Market Forecast: Sales Value (in Million US\$), 2023-2028

Figure 38: United Kingdom: SRAM and ROM Design IP Market: Sales Value (in Million US\$), 2017 & 2022

Figure 39: United Kingdom: SRAM and ROM Design IP Market Forecast: Sales Value

(in Million US\$), 2023-2028

Figure 40: Italy: SRAM and ROM Design IP Market: Sales Value (in Million US\$), 2017 & 2022

Figure 41: Italy: SRAM and ROM Design IP Market Forecast: Sales Value (in Million US\$), 2023-2028

Figure 42: Spain: SRAM and ROM Design IP Market: Sales Value (in Million US\$), 2017 & 2022

Figure 43: Spain: SRAM and ROM Design IP Market Forecast: Sales Value (in Million US\$), 2023-2028

Figure 44: Russia: SRAM and ROM Design IP Market: Sales Value (in Million US\$), 2017 & 2022

Figure 45: Russia: SRAM and ROM Design IP Market Forecast: Sales Value (in Million US\$), 2023-2028

Figure 46: Others: SRAM and ROM Design IP Market: Sales Value (in Million US\$), 2017 & 2022

Figure 47: Others: SRAM and ROM Design IP Market Forecast: Sales Value (in Million US\$), 2023-2028

Figure 48: Latin America: SRAM and ROM Design IP Market: Sales Value (in Million US\$), 2017 & 2022

Figure 49: Latin America: SRAM and ROM Design IP Market Forecast: Sales Value (in Million US\$), 2023-2028

Figure 50: Brazil: SRAM and ROM Design IP Market: Sales Value (in Million US\$), 2017 & 2022

Figure 51: Brazil: SRAM and ROM Design IP Market Forecast: Sales Value (in Million US\$), 2023-2028

Figure 52: Mexico: SRAM and ROM Design IP Market: Sales Value (in Million US\$), 2017 & 2022

Figure 53: Mexico: SRAM and ROM Design IP Market Forecast: Sales Value (in Million US\$), 2023-2028

Figure 54: Others: SRAM and ROM Design IP Market: Sales Value (in Million US\$), 2017 & 2022

Figure 55: Others: SRAM and ROM Design IP Market Forecast: Sales Value (in Million US\$), 2023-2028

Figure 56: Middle East and Africa: SRAM and ROM Design IP Market: Sales Value (in Million US\$), 2017 & 2022

Figure 57: Middle East and Africa: SRAM and ROM Design IP Market: Breakup by Country (in %), 2022

Figure 58: Middle East and Africa: SRAM and ROM Design IP Market Forecast: Sales Value (in Million US\$), 2023-2028

Figure 59: Global: SRAM and ROM Design IP Industry: Drivers, Restraints, and Opportunities

Figure 60: Global: SRAM and ROM Design IP Industry: Value Chain Analysis

Figure 61: Global: SRAM and ROM Design IP Industry: Porter's Five Forces Analysis

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